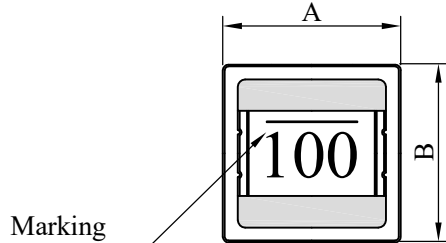


# SPECIFICATION FOR APPROVAL

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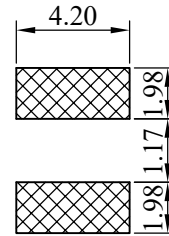
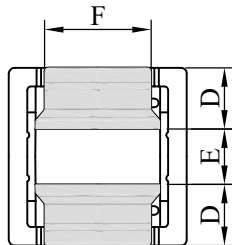
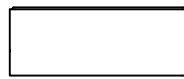
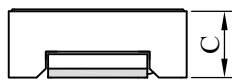
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## I . Configuration and dimensions :



Marking

Top line indicate the terminal side of winding start.



( PCB Pattern )

Unit : mm

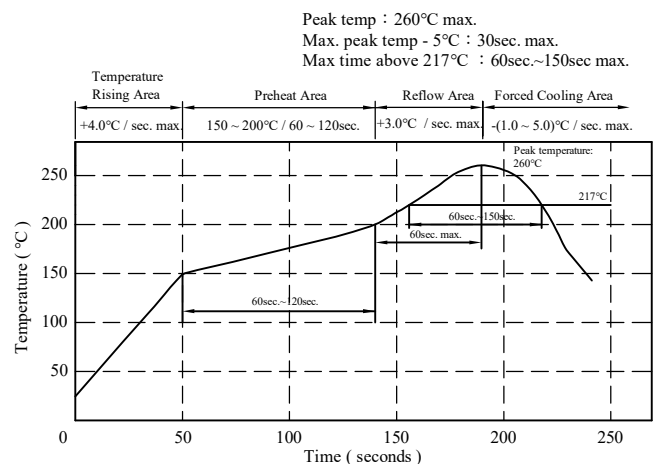
A	B	C	D	E	F
4.80 ±0.2	4.80 ±0.2	1.80 ±0.2	1.60 ±0.2	1.60 ±0.2	2.90 ±0.2

## II . Description :

- a . Ferrite drum core construction
- b . Magnetically shielded
- c . Enamelled copper wire : H class
- d . Product weight : 0.19 g ( ref. )
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free
- h . AEC-Q200 Grade 0

## III . General specification :

- a . Operating temp. : -55°C----+150°C  
(Temp. rise included)
- b . Resistance to solder heat : 260°C.10 sec.



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## IV . Electrical characteristics :

DWG. No.	Inductance ( $\mu$ H)	RDC ( $m\Omega$ )		SRF (MHz) typ.	Isat (A) typ.	Irms1 (A) typ.	Irms2 (A) typ.
		typ.	max.				
AQS48181R0YLB-□□□	1.0 $\pm$ 30%	19.2	25.0	140	3.60	4.00	5.10
AQS48181R5YLB-□□□	1.5 $\pm$ 30%	25.2	35.0	105	3.00	3.75	4.70
AQS48182R2YLB-□□□	2.2 $\pm$ 30%	33.7	45.0	90	2.43	2.70	3.50
AQS48183R3YLB-□□□	3.3 $\pm$ 30%	42.8	55.0	70	2.10	2.50	3.10
AQS48183R9YLB-□□□	3.9 $\pm$ 30%	54.5	70.0	65	1.90	2.20	2.70
AQS48184R7YLB-□□□	4.7 $\pm$ 30%	59.4	80.0	55	1.50	2.00	2.60
AQS48185R6YLB-□□□	5.6 $\pm$ 30%	74.3	90.0	50	1.35	1.75	2.30
AQS48186R8YLB-□□□	6.8 $\pm$ 30%	82.1	100.0	45	1.25	1.65	2.20
AQS48188R2YLB-□□□	8.2 $\pm$ 30%	97.7	130.0	43	1.15	1.55	1.90
AQS4818100MLB-□□□	10.0 $\pm$ 20%	109.8	140.0	40	1.05	1.45	1.80
AQS4818120MLB-□□□	12.0 $\pm$ 20%	132.2	170.0	37	0.95	1.30	1.60
AQS4818150MLB-□□□	15.0 $\pm$ 20%	176.7	220.0	30	0.87	1.25	1.50
AQS4818180MLB-□□□	18.0 $\pm$ 20%	214.8	280.0	28	0.79	1.10	1.40
AQS4818220MLB-□□□	22.0 $\pm$ 20%	280.3	360.0	25	0.72	1.00	1.25
AQS4818270MLB-□□□	27.0 $\pm$ 20%	317.6	400.0	22	0.63	0.90	1.15
AQS4818330MLB-□□□	33.0 $\pm$ 20%	399.1	500.0	20	0.56	0.70	0.90
AQS4818390MLB-□□□	39.0 $\pm$ 20%	439.3	540.0	17	0.53	0.63	0.80
AQS4818470MLB-□□□	47.0 $\pm$ 20%	504.1	630.0	16	0.47	0.60	0.75

- 1). Electrical specifications at 25°C
- 2). Inductance Test Freq :100kHz /0.1V
- 3). Isat base on  $\Delta L / L0A=35\%$  typ.
- 4). Irms1 base on Temp. rise 20°C typ.
- 5). Irms2 base on Temp. rise 40°C typ.

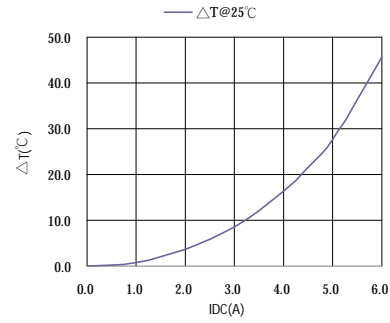
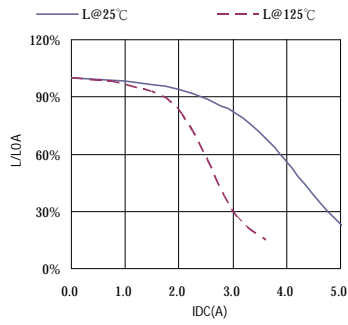
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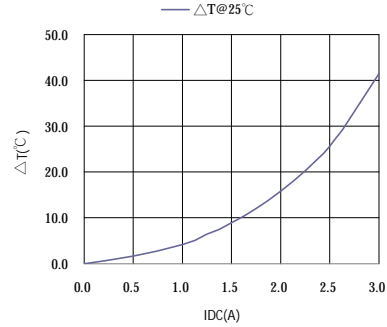
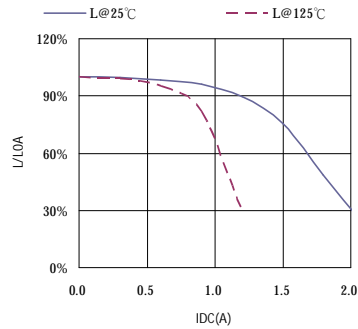
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V . Curve :

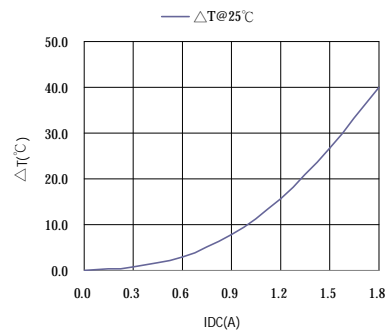
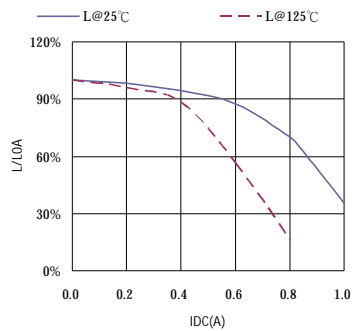
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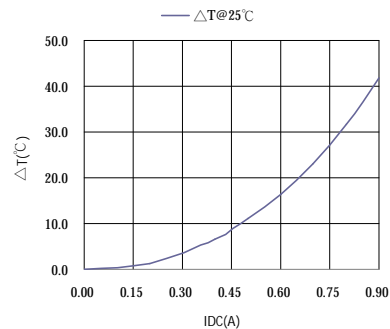
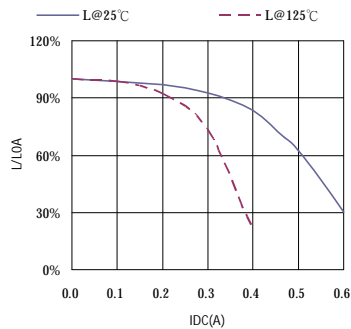
**AQS48184R7YLB**



**AQS4818150MLB**



**AQS4818470MLB**



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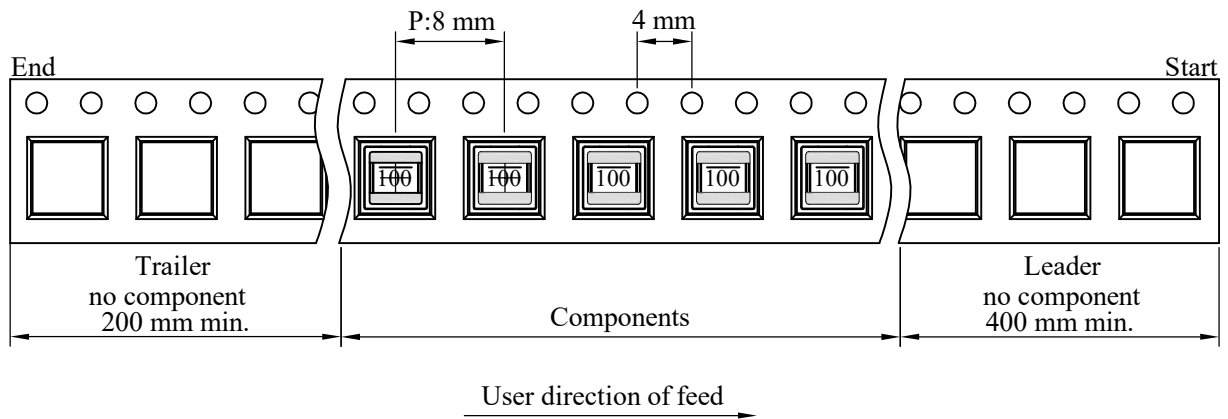
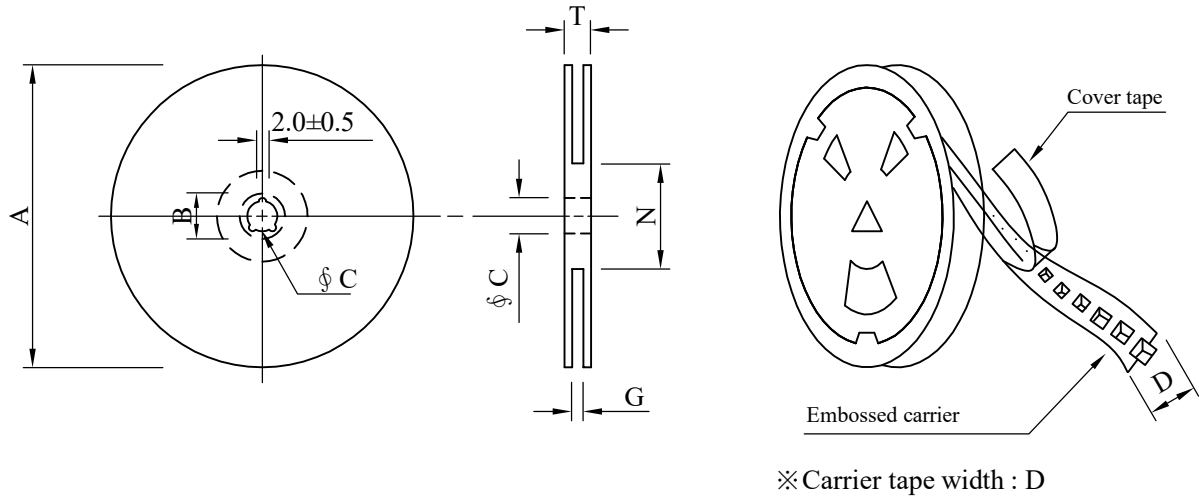
# SPECIFICATION FOR APPROVAL

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## VI . Packaging information :

### (1) Configuration



### (2) Dimensions

Unit:mm

Style	A	B	C	D	G	N	T
07 - 12	178	21±0.8	13	12	14 <sup>+0</sup>	50 <sup>-0</sup>	16.5

### (3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (g)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
B	800	260	07 - 12	32,000	11.7	42 x 41 x 24

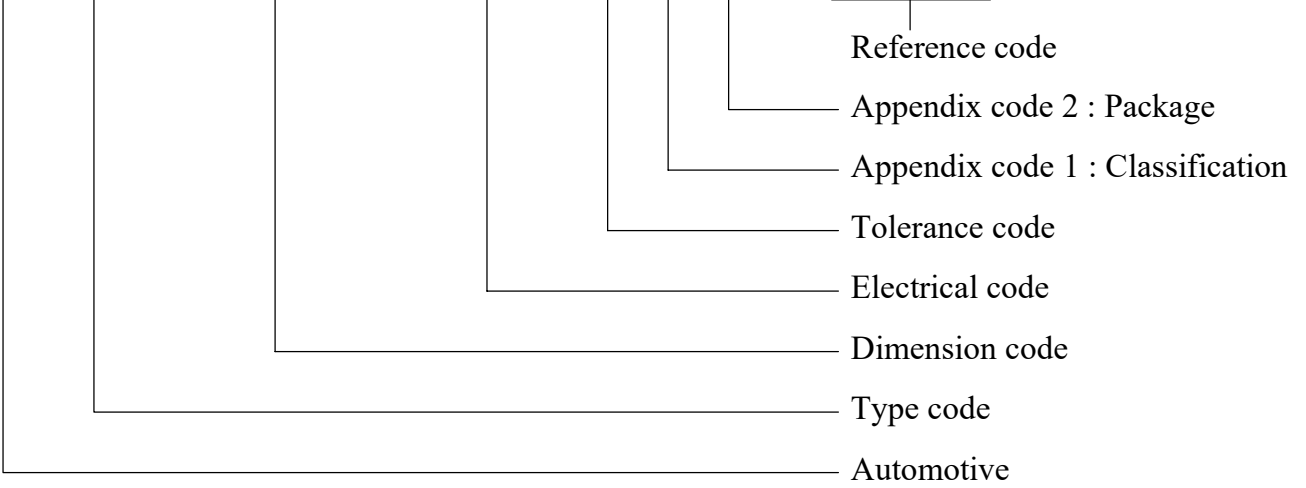
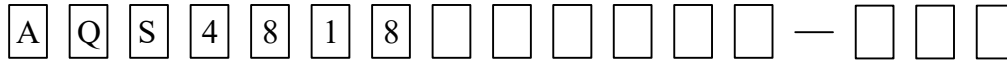
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## VII . Drawing number expression :



Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
B	T /R (Reel package)	UCT	Non-antistatic	Antistatic	800 pcs	

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## VIII . Reliability test :

Item	Reference documents	AEC-Q200 Test Condition	Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 150°C 2.Time:1000 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -55°C ~ 150°C 2.Number of cycle:1000 cycles 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±5 °C 2.Time:1000 Hours 3.Humidity : 85±5% RH.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	MIL-PRF-27-3.26/4.7.23 & User Spec.	1.Temperature:150°C (Temp. rise included) 2.Time:1000 hours. 3.Apply rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body deformation change in appearance or obliteration of marking. 2.Inductance shall not change more than ±20%.
8.Mechanical Shock	MIL-STD-202 Method 213	1.Peak acceleration 100g's 2.Duration of pulse: 6ms 3.Waveform : Half-sine 4.Velocity change: 12.3ft/sec 5.Direction : ±X, ±Y, ±Z (3times / axis )	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued :10-2000-10 Hz 2.Sweep time : 20 min 3.Acceleration : 5g 4.Direction : X , Y , Z 5.Number of sweep : 12 time/axis	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	a) Reflow : 1.Highest temperature : 260±5 °C. 2.Time(temp.≥ 217°C): 60~150seconds. 3.IR reflow times: 3 times. b) DIP : 1. Temperature : 260±5°C. 2. Time : 10 sec. 3. Number of times : 1 time.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%. 3.RDC shall conform to specification requirements.
11.ESD	AEC-Q200-002 or ISO/DIS 10605	1.ESD Voltage : 15kV 2.Mode 1 : 150 pF / 330 Ω 3.Mode 2 : 150 pF / 2000 Ω 4.Discharge times and polarity : 3 times pos. / 3 times eng. for each condition	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
12.Solderability Test	J-STD-002	a) Reflow : 1. Baking in pre-testing:150±5°C/16Hours±30 min. 2. Peak temperature : 240±5°C. 3.Time(temp.≥ 217°C): 60~150seconds. 4.IR reflow times: 1 time. b) DIP : 1. Baking in pre-testing: 150±5°C/16 Hours±30 min. 2. After fluxing,inductor shall be dipped in a melted solder pot at 240±5°C for 5±0.5 seconds.	Product terminals should be wetted.  More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User Spec.	1.Operating temperature : -40°C~-150°C 2.Room temperature : 25°C.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
14.Flammability			
15.Board Flex	AEC-Q200-005	1.Deflection speed : 1 mm/ sec 2.Amount of deflection : 2 mm 3.Span : 90 mm 4.Direction for test : Bottom of PCB 5.Holding time : 60 sec	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
16.Terminal Strength Test	AEC-Q200-006	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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ABC-ATEC ELECTRONICS GROUP

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## IX . Safety notes :

1. Products do not be used in related applications that directly affect the personal safety system or cause significant impacts and losses on society. If you apply to these applications, please be sure to contact us at first to confirm.  
產品不可使用於直接影響人身安全系統或對社會會造成重大影響與損失之相關用途，若您應用於此，請務必優先與我們聯繫確認。
2. The storage period is less than 12 months. Ensure to follow the storage conditions (Temperature: 5 to 30 °C, Humidity: 10 to 60% RH or less). If the storage time is exceeded the limit, the electrodes might be deteriorate of terminal soldering.  
儲存期不超過12個月，務必遵守儲存條件（溫度：5至30°C，濕度：10至60%RH以下）。如果超過了儲存時間，端子電極可能會氧化而影響焊接。
3. Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).  
不要在有氣體腐蝕等條件的地方使用或存放（鹽，酸，鹼等）。
4. Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.  
安裝時的焊接條件應在規格範圍內。如果超過要求，可能會發生短路，性能下降或壽命縮短。
5. When using, it should try to avoid excessive mechanical impact on the product, such as collision / drop ... and other reasons.  
使用時，應盡量避免產品受到過度機械衝擊，如碰撞/掉落...等原因。
6. When embedding a printed circuit board where a chip is mounted to a set, be sure that residual stress is not given to the chip due to the overall distortion of the printed circuit board and partial distortion such as at screw tightening portions.  
將已安裝新片的電路板組裝到裝置時，請注意應盡量避免電路板受到組裝變形...等，導致產品受到應力。
7. Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.  
產品會因通電而自我發熱(溫度上升)，因此在熱影響設計方面，需保留適當公差。
8. Do not expose the products to magnets or magnetic fields.  
請勿將產品暴露於磁鐵或是磁場中。
9. If you would like to use this products to performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property, or if you have special requirements exceeding the range or conditions set forth in the each catalog, please contact us.  
如果您希望將此產品用於性能和/或質量要求更嚴格的安全性或可靠性，或其失敗、故障或麻煩可能對社會、個人或財產造成嚴重損害，或者您有特殊情況要求超出目錄中規定的範圍或條件，請與我們聯繫。

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