



#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

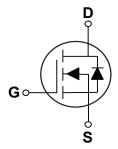
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
60 V	100 mΩ	3 A

#### **Features**

- $R_{DS(ON)} \le 100 m\Omega @V_{GS} = 10V$
- · Improved dv/dt Capability
- · Fast Switching
- · Green Device Available

#### SOT-23 Pin Configuration





#### **Applications**

- Notebook
- · Load Switch
- · Hand-Held Instruments

#### Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted) **Symbol Parameter** Value Units $V_{DS}$ Drain-Source Voltage 60 ٧ $V_{GS}$ Gate-Source Voltage ±20 V 3 $I_{\mathsf{D}}$ Drain Current - Continuous Α Drain Current - Pulsed (NOTE 1) 12 $I_{DM}$ Α $P_{\mathsf{D}}$ Power Dissipation (NOTE 1) 1.5 W $T_{\mathsf{J}}$ Operating Junction Temperature Range -55 to 150 ٥С -55 to 150 $T_{STG}$ Storage Temperature Range ٥С 2310 Marking Code

Thermal Characteristics					
Symbol	Parameter	Value	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	83.3	°C/W		





#### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	60			٧
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =48V , $V_{GS}$ =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =2A			100	mΩ
		$V_{GS}$ =4.5V , $I_D$ =1A			110	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.0		2.5	V

#### **Dynamic and switching Characteristics (NOTE 3)**

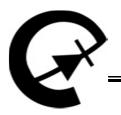
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge			7		
$Q_gs$	Gate-Source Charge	$V_{DS}$ =30V , $V_{GS}$ =4.5V , $I_{D}$ =2A		1.2		nC
$Q_{gd}$	Gate-Drain Charge	Γ		1.5		
$T_{d(on)}$	Turn-On Delay Time			6.5		
T <sub>r</sub>	Rise Time	$V_{DD}$ =30V , $V_{GS}$ =10V , $R_{GEN}$ =1 $\Omega$ , $I_{D}$ =2A		15.2		ns
$T_{d(off)}$	Turn-Off Delay Time			15.2		115
$T_f$	Fall Time			10.3		
C <sub>iss</sub>	Input Capacitance			515		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =30V , $V_{GS}$ =0V , f=1MHz		26		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1		20		

#### **Drain-Source Diode Characteristics and Ratings**

Symbo	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current				3	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A			1.2	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. This value is guaranteed by design hence it is not included in the production test.



# **TNMNG100A**



## **60V N-Channel MOSFETs**

#### **Characteristics Curves**

FIG. 1-Drain Current

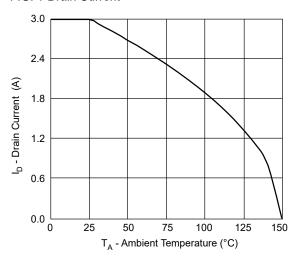


FIG. 2-Normalized  $V_{\text{GS}(\text{th})}$  vs  $T_J$ 

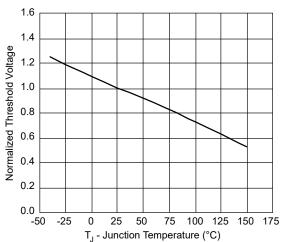


FIG. 2-Normalized  $R_{DS(ON)}$  vs  $T_J$ 

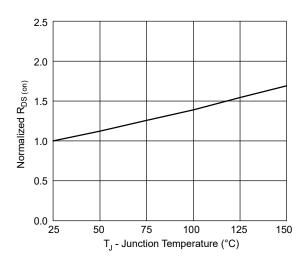


FIG. 4-Gate Charge Characteristics

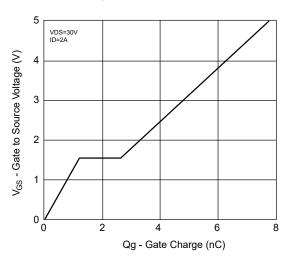


FIG. 5-Transfer Characteristics

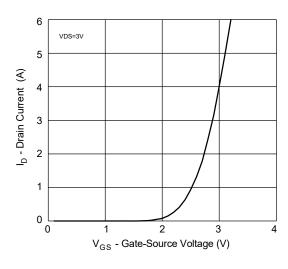
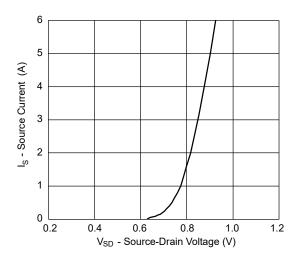
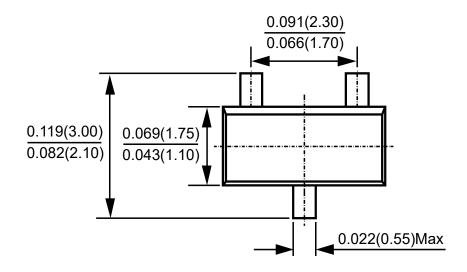


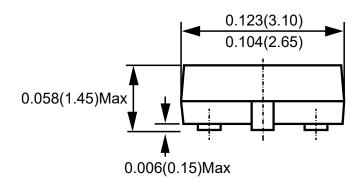
FIG. 6-Forward Characteristics

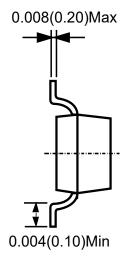




#### **Package Outline Dimensions**







**SOT-23** Dimensions in inches and (millimeters)





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