



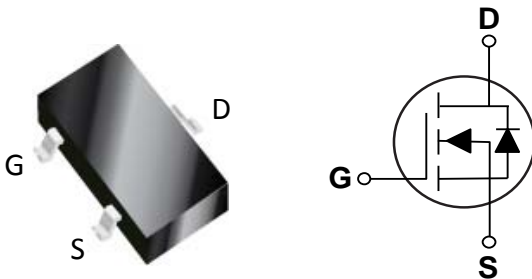
**General Description**

The TNMNC033 is the high cell density trenched N-ch MOSFETs, which provides excellent  $R_{DS(ON)}$  and efficiency for most of the small power switching and load switch applications.

The TNMNC033 meets the RoHS and Green Product requirement with full function reliability approved.

<b><math>BV_{DSS}</math></b>	<b><math>R_{DS(ON)}</math></b>	<b><math>I_D</math></b>
30 V	33 m $\Omega$	4.6 A

SOT-23 Pin Configuration



**Features**

- 30V, 4.6A,  $R_{DS(ON)}=33m\Omega @V_{GS}=10V$
- Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Advanced high cell density Trench technology

**Absolute Maximum Ratings  $T_c=25^\circ C$  unless otherwise noted**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous, $V_{GS} @ 10V$ (NOTE 1) ( $T_A=25^\circ C$ )	4.6	A
	Drain Current - Continuous, $V_{GS} @ 10V$ (NOTE 1) ( $T_A=70^\circ C$ )	3.7	A
$I_{DM}$	Drain Current - Pulsed (NOTE 2)	18.4	A
$P_D$	Total Power Dissipation (NOTE 3) ( $T_A=25^\circ C$ )	1	W
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ C$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ C$
Marking Code		AC	

**Thermal Characteristics**

Symbol	Parameter	Typ.	Max	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (NOTE 1)	---	125	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case (NOTE 1)	---	80	$^\circ C/W$



# 30V N-Channel MOSFETs

## Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

### On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance (NOTE 2)	V <sub>GS</sub> =10V, I <sub>D</sub> =4A	---	27	33	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	---	36	50	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.5	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =4A	---	7	---	S

### Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	5.0	6.9	nC
Q <sub>gs</sub>	Gate-Source Charge		---	1.1	2.2	
Q <sub>gd</sub>	Gate-Drain Charge		---	2.6	2.8	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =4A	---	2	4	ns
T <sub>r</sub>	Rise Time		---	34.4	62	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	13.2	26	
T <sub>f</sub>	Fall Time		---	4.8	9.6	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, F=1MHz	---	420	582	pF
C <sub>oss</sub>	Output Capacitance		---	60	87	
C <sub>riss</sub>	Reverse Transfer Capacitance		---	53	71	

### Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current (NOTE 1 · 4)	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	4.6	A
I <sub>SM</sub>	Pulsed Source Current (NOTE 2 · 4)		---	---	18.4	A
V <sub>SD</sub>	Diode Forward Voltage (NOTE 2)	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =4A, di/dt=100A/us, T <sub>J</sub> =25°C	---	8.7	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge		---	2.3	---	nC

### NOTES :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2oz copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature
4. The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.



Characteristics Curves

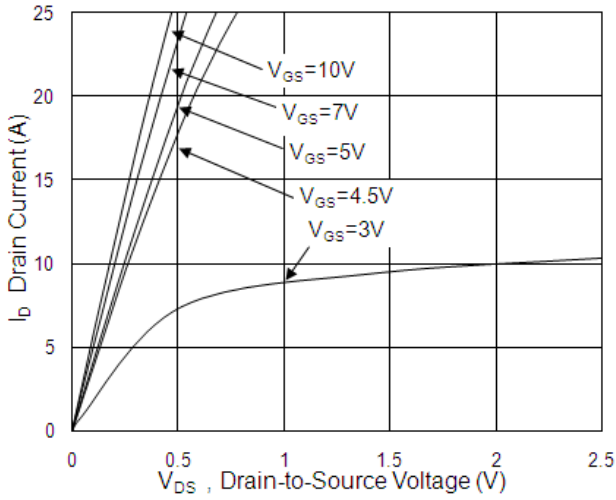


Fig.1 Typical Output Characteristics

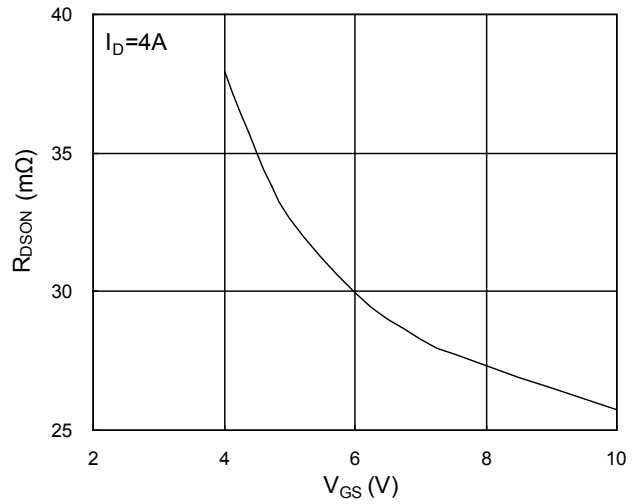


Fig.2 On-Resistance vs. Gate-Source

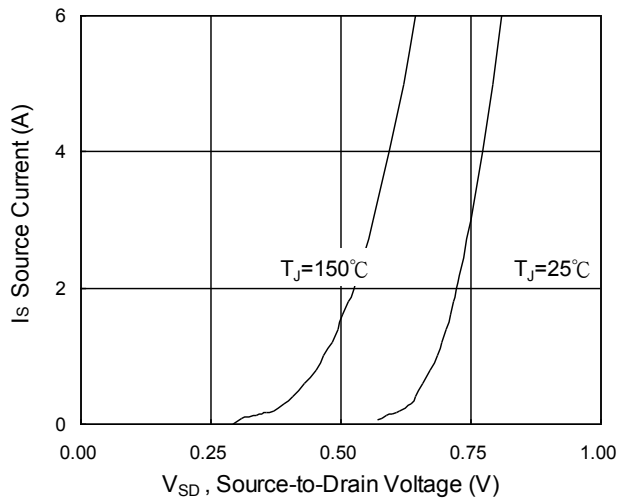


Fig.3 Forward Characteristics Of Reverse

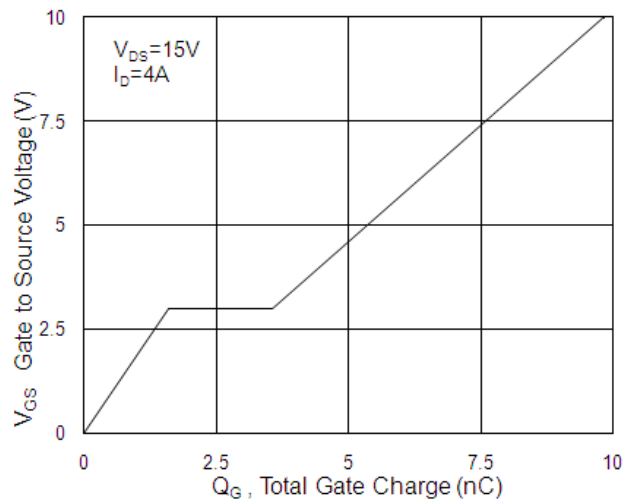


Fig.4 Gate-Charge Characteristics

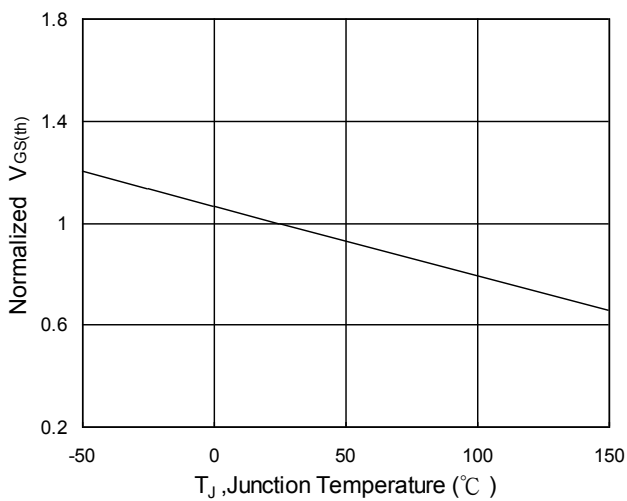


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

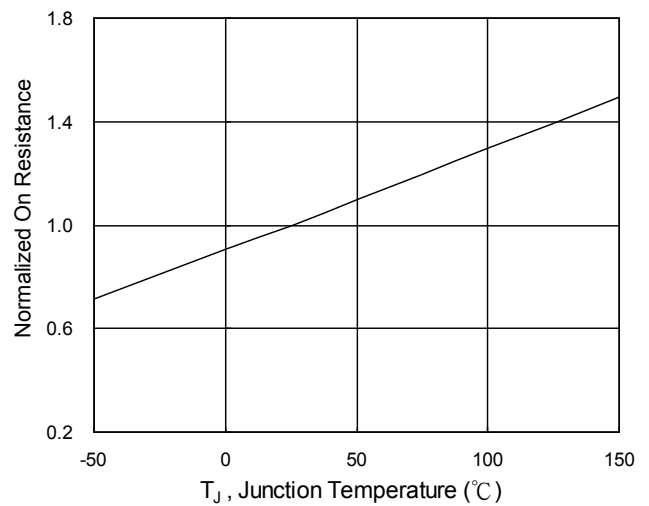


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



Characteristics Curves

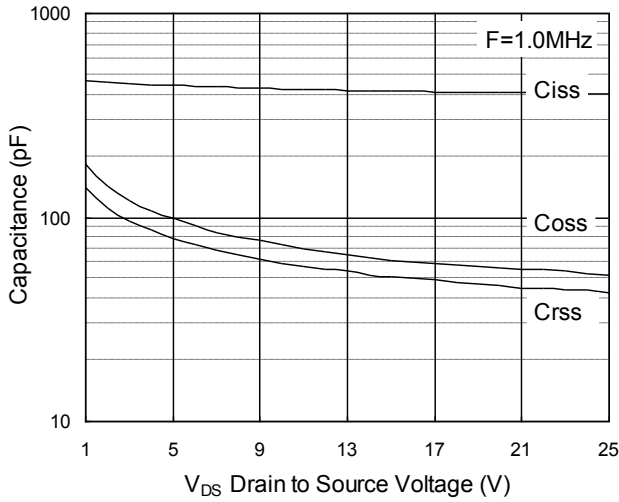


Fig.7 Capacitance

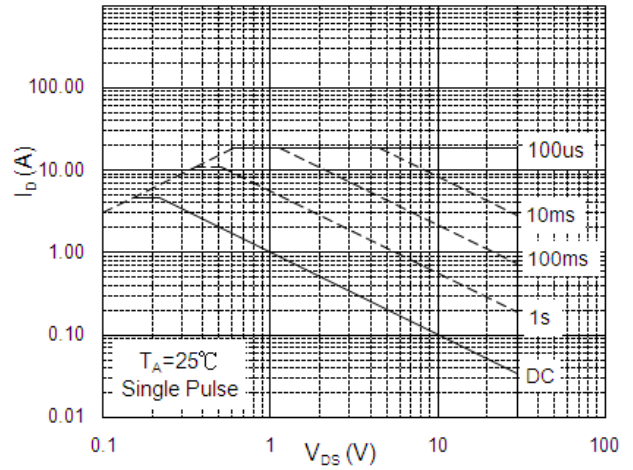


Fig.8 Safe Operating Area

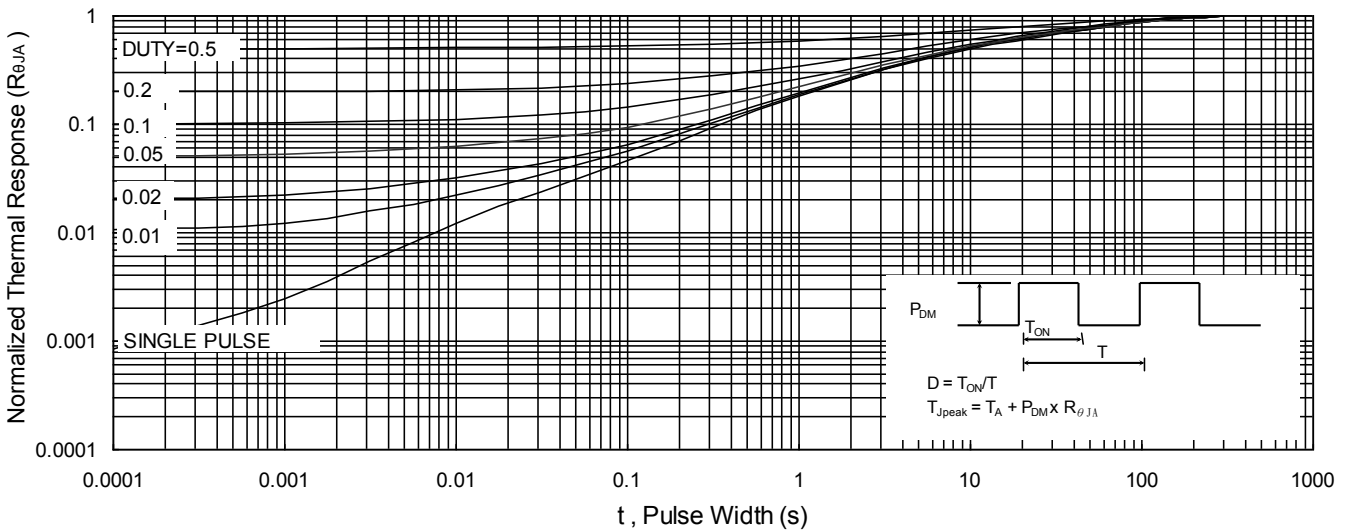


Fig.9 Normalized Maximum Transient Thermal Impedance

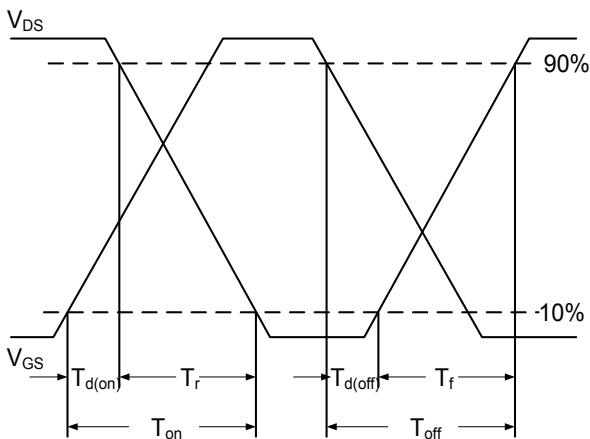


Fig.10 Switching Time Waveform

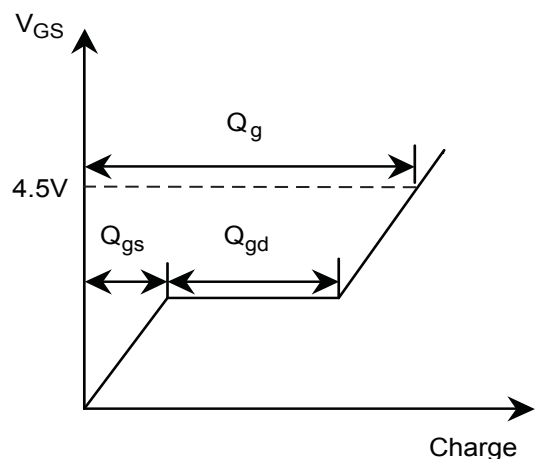
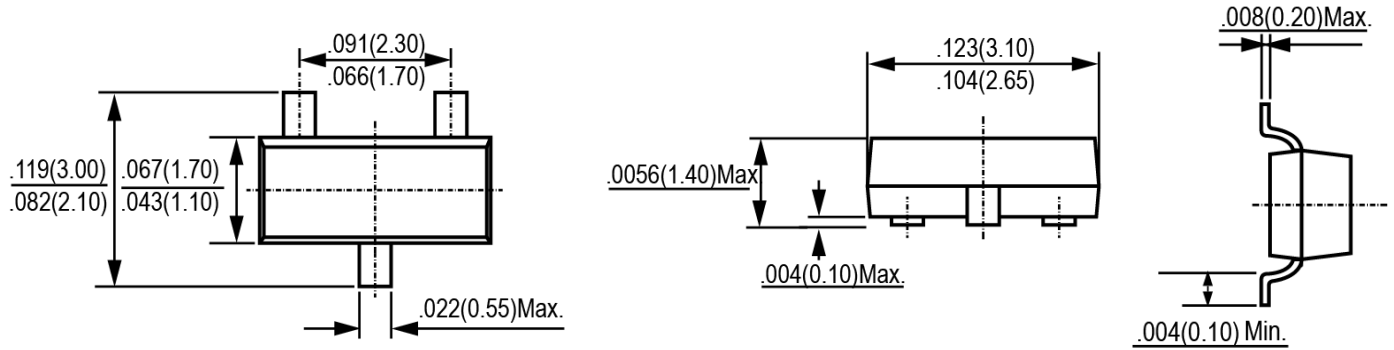


Fig.11 Gate Charge Waveform



Package Outline Dimensions



SOT-23

Dimensions in inches and (millimeters)



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