



#### **General Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

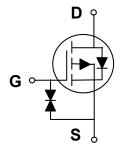
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
-20 V	33 mΩ	-5.8 A

#### **Features**

- $R_{DS(ON)} \le 33 m\Omega @V_{GS} = -4.5V$
- · Improved dv/dt capability
- Fast switching
- · Green Device Available
- · Suit for -1.8V Gate Drive Applications

### SOT-23S Pin Configuration





#### **Applications**

- Notebook
- · Load Switch
- Battery Protection
- · Hand-held Instruments

Absolute Maximum Ratings T <sub>C</sub> =25°C unless otherwise noted							
Symbol	Parameter	Rating	Units				
$V_{DS}$	Drain-Source Voltage	-20	V				
$V_{GS}$	Gate-Source Voltage	±10	V				
1	Drain Current - Continuous (T <sub>C</sub> =25°C)	-5.8	Α				
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	-3.7	Α				
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	-23.2	Α				
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	1.56	W				
ı D	Power Dissipation – Derate above 25°C	0.012	W/°C				
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C				
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C				

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		80	°C/W	





### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V , I <sub>D</sub> = -250uA	-20			V
I <sub>DSS</sub> Drain-Source Le	Drain Source Leekage Current	$V_{DS}$ = -20V , $V_{GS}$ = 0V , $T_{J}$ =25°C			-1	uA
	Drain-Source Leakage Guirent	$V_{DS}$ = -16V , $V_{GS}$ = 0V , $T_{J}$ =125 $^{\circ}$ C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±10V , $V_{DS}$ = 0V			±10	uA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
	Static Drain-Source On-Resistance	$V_{GS}$ = -4.5V , $I_D$ = -4A		28	33	mΩ
D		$V_{GS}$ = -2.5V , $I_D$ = -3A		37	45	
R <sub>DS(ON)</sub>		V <sub>GS</sub> = -1.8V , I <sub>D</sub> = -2A		49	65	11122
		V <sub>GS</sub> = -1.5V , I <sub>D</sub> = -2A		56	80	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-0.3		-1	V
gfs	Forward Transconductance	$V_{DS}$ = -10V , $I_D$ = -3A		8.4		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	-V <sub>DS</sub> = -10V , V <sub>GS</sub> = -4.5V ,		16.1	25	
$Q_gs$	Gate-Source Charge	$I_{D}$ = -4A (NOTE 2 \ 3)		1.8	3	nC
$Q_{gd}$	Gate-Drain Charge	ID4A (NOTE 2 · 3)		3.8	7	
$T_{d(on)}$	Turn-On Delay Time	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		8.2	16	
T <sub>r</sub>	Rise Time	$V_{DD}$ = -10V , $V_{GS}$ = -4.5V , $R_{G}$ = 25 $\Omega$ , $I_{D}$ = -1A (NOTE 2 \		30	57	nS
$T_{d(off)}$	Turn-Off Delay Time	$R_{G} = 2502$ , $I_{D} = -1A$ (NOTE 2		71.1	135	113
$T_f$	Fall Time			19.8	38	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -15V , V <sub>GS</sub> = 0V , F= 1MHz		1440	2100	
C <sub>oss</sub>	Output Capacitance			155	230	pF
$C_{rss}$	Reverse Transfer Capacitance			115	170	

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> = V <sub>D</sub> = 0V,Force Current		-	-5.8	Α
I <sub>SM</sub>	Pulsed Source Current				-23.2	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ = 0V , $I_{S}$ = -1A , $T_{J}$ = 25°C			-1	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$ .
- ${\it 3. Essentially independent of operating temperature.}\\$





#### **Characteristics Curves**

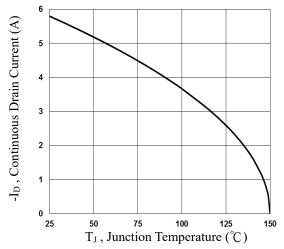


Fig.1 Continuous Drain Current vs. Tc

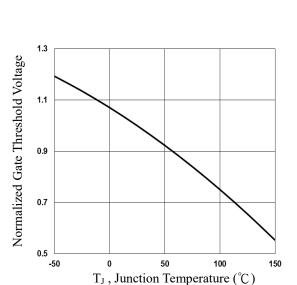


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

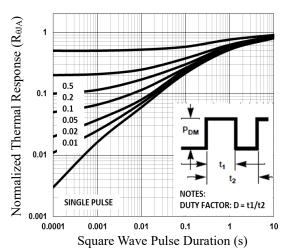


Fig.5 Normalized Transient Impedance

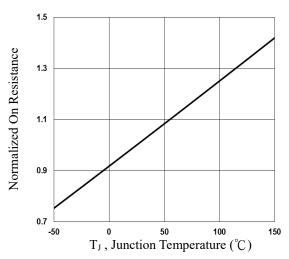


Fig.2 Normalized RDSON vs. T<sub>J</sub>

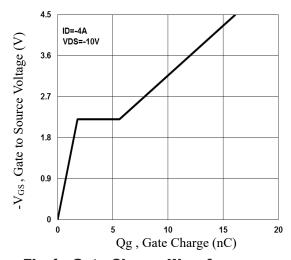


Fig.4 Gate Charge Waveform

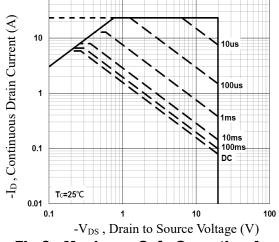


Fig.6 Maximum Safe Operation Area





#### **Characteristics Curves**

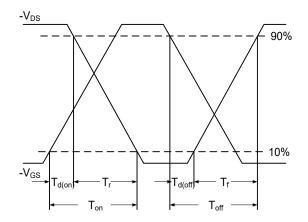


Fig.7 Switching Time Waveform

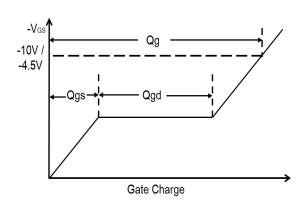
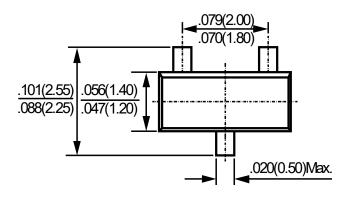
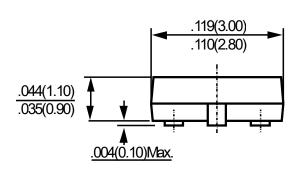
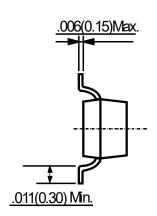


Fig.8 Gate Charge Waveform

### **Package Outline Dimensions**







**SOT-23S** 

Dimensions in inches and (millimeters)





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