



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

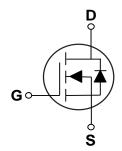
BV _{DSS}	R _{DS(ON)}	I _D
60 V	75 mΩ	3.2 A

Features

- $\cdot R_{DS(ON)} \le 75 m\Omega @V_{GS} = 10V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

SOT-23S Pin Configuration





Applications

- Motor Drive
- · Power Tools
- LED Lighting

Absolute Maximum Ratings T _c =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V_{DS}	Drain-Source Voltage	60	V			
V_{GS}	Gate-Source Voltage	±20	V			
l ₌	Drain Current - Continuous (T _C =25°C)	3.2	Α			
I _D	Drain Current - Continuous (T _C =100°C)	2	Α			
I _{DM}	Drain Current - Pulsed (NOTE 1)	12.8	Α			
P_D	Power Dissipation (T _C =25°C)	1.56	W			
i D	Power Dissipation - Derate above 25°C	0.012	W/°C			
T_J	Operating Junction Temperature Range	-50 to 150	°C			
T _{STG}	Storage Temperature Range	-50 to 150	°C			
Marking Code		N				

Thermal Characteristics				
Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		80	°C/W





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	60			V
I _{DSS}	IDrain-Source Leakage Current	V_{DS} =60V , V_{GS} =0V , T_J =25°C			1	uA
		V _{DS} =48V , V _{GS} =0V , T _J =125°C			10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V_{GS} =10V , I_D =3A		60	75	- mΩ
		V_{GS} =4.5V , I_D =1.5A		70	90	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2	1.8	2.5	V
gfs	Forward Transconductance	V_{DS} =10V , I_{D} =3A		7		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	101/11/11/11/11/11		9.3	14	
Q_gs	Gate-Source Charge	V_{DS} =48V , V_{GS} =10V , I_{D} =3A (NOTE 2 \cdot 3)		2.1	4	nC
Q_{gd}	Gate-Drain Charge	(140122 + 3)		1.8	4	ı
$T_{d(on)}$	Turn-On Delay Time	V_{DD} =30V , V_{GS} =10V , R_{G} =3.3 Ω , I_{D} =1A (NOTE 2 \cdot 3)		2.9	6	
T_r	Rise Time			9.5	18	nS
$T_{d(off)}$	Turn-Off Delay Time			18.4	35	113
T_f	Fall Time			5.3	10	
C_{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , F=1MHz		500	725	
C _{oss}	Output Capacitance			45	65	pF
C_{rss}	Reverse Transfer Capacitance			16	30	
R_g	Gate resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz		2	4	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current	-V _G =V _D =0V,Force Current			3.2	Α
I _{SM}	Pulsed Source Current				6.4	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_{S} =1A , T_{J} =25 $^{\circ}$ C			1	V
t _{rr}	Reverse Recovery Time (NOTE 2)	V _{GS} =30V , I _S =1A ,		23.2		nS
Q_{rr}	Reverse Recovery Charge (NOTE 2)	di/dt=100A/us ,T _J =25°C		14.3		nC

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.





Characteristics Curves

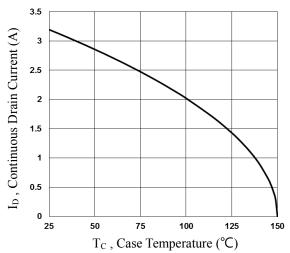


Fig.1 Continuous Drain Current vs. T_c

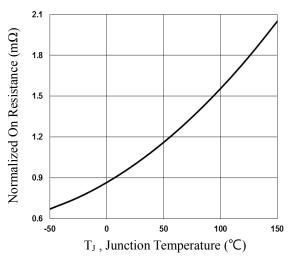


Fig.2 Normalized RDSON vs. T_J

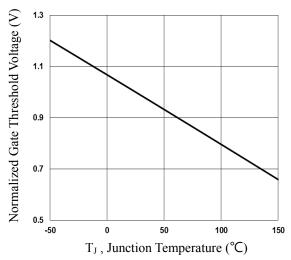


Fig.3 Normalized V_{th} vs. T_J

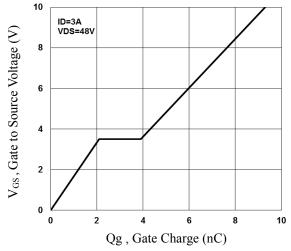


Fig.4 Gate Charge Waveform

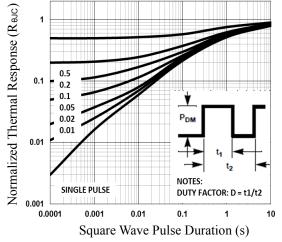


Fig.5 Normalized Transient Response

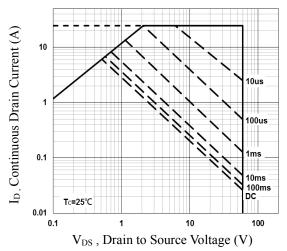


Fig.6 Maximum Safe Operation Area





Characteristics Curves

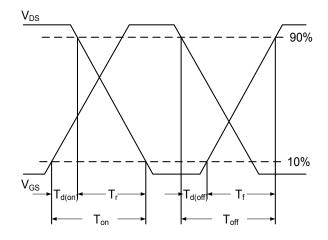


Fig.7 Switching Time Waveform

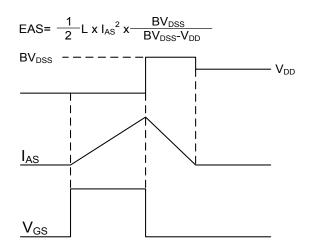
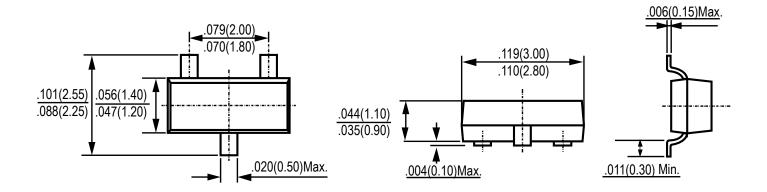


Fig.8 EAS Waveform

Package Outline Dimensions



SOT-23SDimensions in inches and (millimeters)





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