TKMNM095

100V N-Channel MOSFETs

BV_{DSS}	$R_{DS(ON)}$	I_D
100 V	95 mΩ	6.5 A

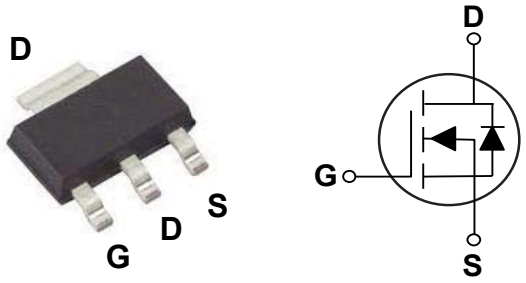
General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- $R_{DS(ON)} \leqq 95m\Omega @ V_{GS}=10V$
- Improved dv/dt Capability
- Fast Switching
- Green Device Available

SOT-223 Pin Configuration



Applications

- Networking
- Load Switch
- LED Applications

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current - Continuous (T_C=25°C)	6.5	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	26	A
P_D	Power Dissipation (T_C=25°C)	9	W
T_J	Operating Junction Temperature Range	-50 to 150	°C
T_{STG}	Storage Temperature Range	-50 to 150	°C
Marking Code		DL0906	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	14	°C/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V , V_{GS}=0V	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=5A	---	---	95	mΩ
		V_{GS}=4.5V , I_D=3A	---	---	110	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D=250uA	1.2	---	2.5	V
gfs	Forward Transconductance	V_{DS}=10V , I_D=3A	---	8.7	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	V_{DS}=48V , V_{GS}=10V , I_D=5A	---	22	---	nC
Q_{gs}	Gate-Source Charge		---	3.9	---	
Q_{gd}	Gate-Drain Charge		---	5.2	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=30V , V_{GS}=10V , R_G=3.3Ω , I_D=1A	---	2.9	---	nS
T_r	Rise Time		---	9.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	18.4	---	
T_f	Fall Time		---	5.3	---	
C_{iss}	Input Capacitance	V_{DS}=50V , V_{GS}=0V , f=1MHz	---	1480	---	pF
C_{oss}	Output Capacitance		---	480	---	
C_{rss}	Reverse Transfer Capacitance		---	35	---	
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	1.3	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	6.5	A
I_{SM}	Pulsed Source Current		---	---	26	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A	---	---	1	V

NOTES：

1. Repetitive Rating：Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

Characteristics Curves

FIG. 1-I_D vs T_C

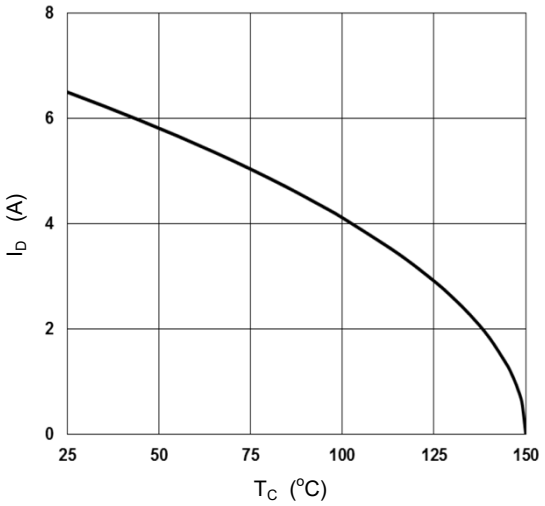


FIG. 2-Normalized $R_{DS(ON)}$ vs T_J

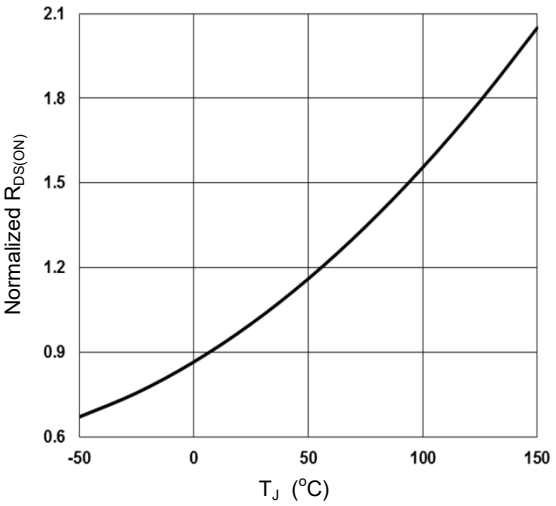


FIG. 3-Normalized V_{th} vs T_J

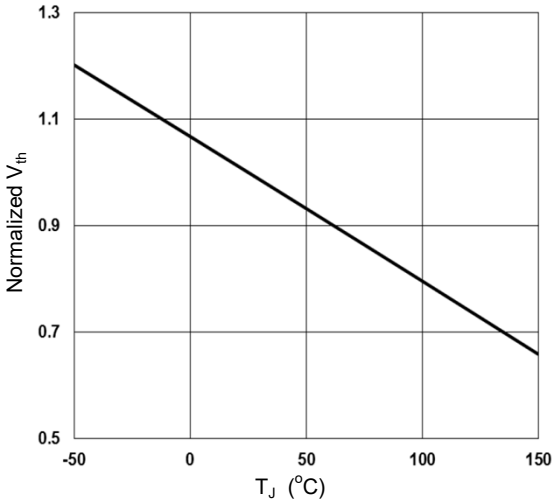


FIG. 4-Gate Charge Waveform

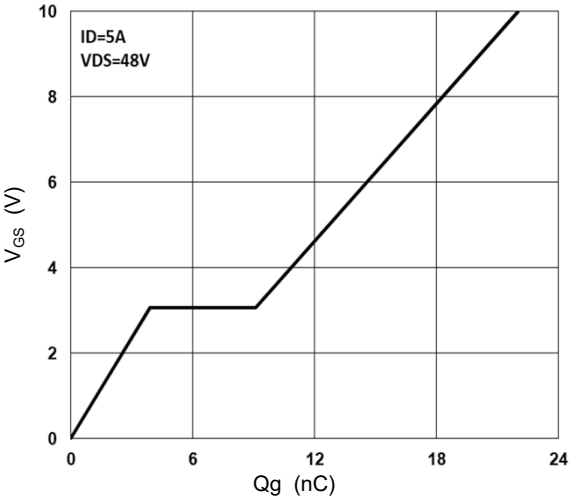


FIG. 5-Normalized Transient Impedance

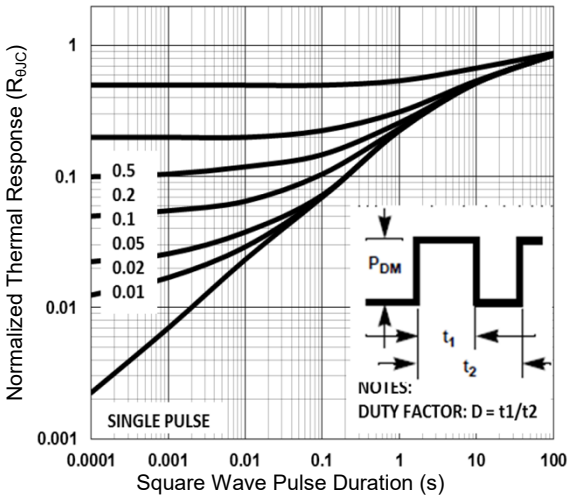
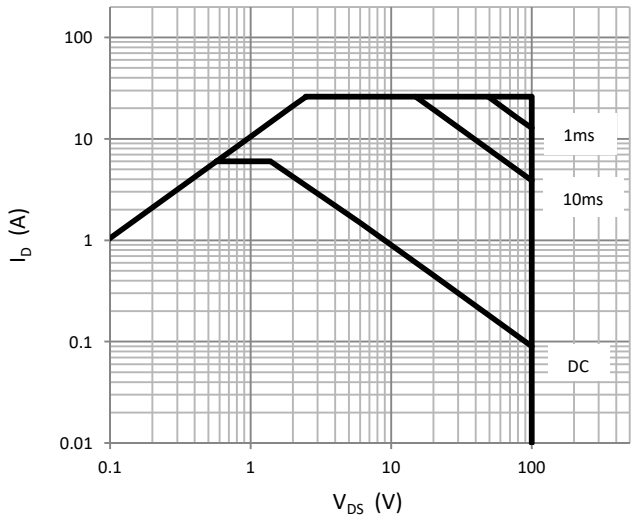


FIG. 6-Safe Operation Area



Characteristics Curves

FIG. 7-Switching Time Waveform

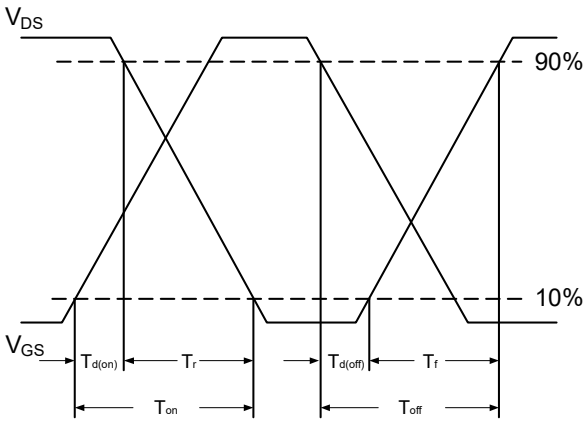
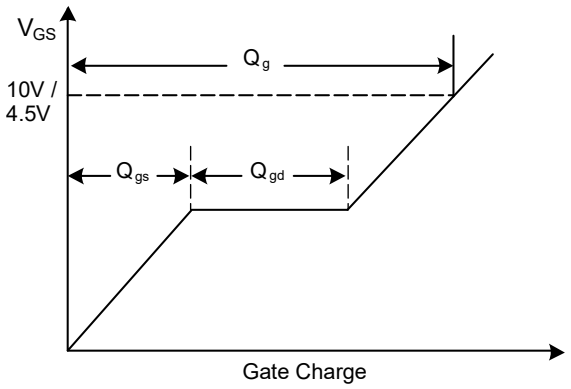
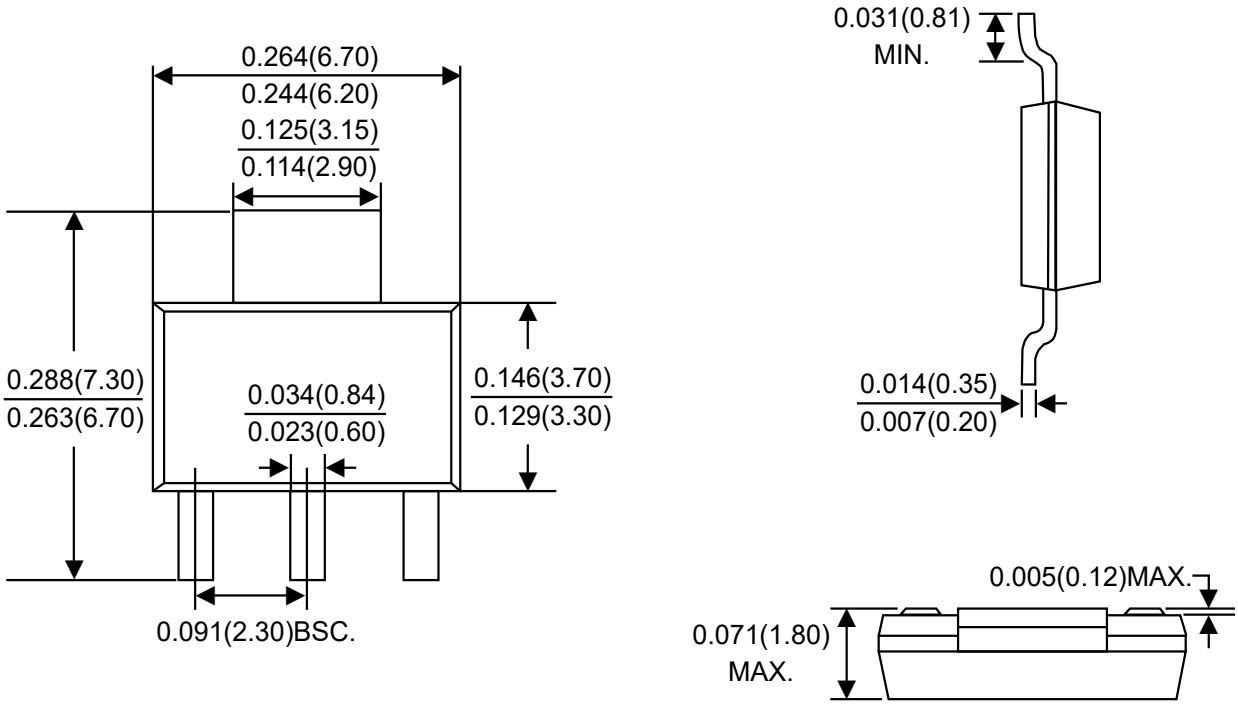


FIG. 8-Gate Charge Waveform



Package Outline Dimensions



SOT-223

Dimensions in inches and (millimeters)

LEGAL DISCLAIMER

- The product is provided “AS IS” without any guarantees or warranty. In association with the product, Eris Technology Corporation, its affiliates, and their directors, officers, employees, agents, successors and assigns (collectively, the “Eris”) makes no warranties of any kind, either express or implied, including but not limited to warranties of merchantability, fitness for a particular purpose, of title, or of non-infringement of third party rights.
- The information in this document and any product described herein are subject to change without notice and should not be construed as a commitment by Eris. Eris assumes no responsibility for any errors that may appear in this document.
- Eris does not assume any liability arising out of the application or use of this document or any product described herein, any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Eris and all the companies whose products are represented on Eris website, harmless against all damages.
- No license, express or implied, by estoppels or otherwise, to any intellectual property is granted by this document or by any conduct of Eris. Product name and markings notes herein may be trademarks of their respective owners.
- Eris does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- Should Customers purchase or use Eris products for any unintended or unauthorized application, Customers shall indemnify and hold Eris and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.
- The official text is written in English and the English version of this document is the only version endorsed by Eris. Any discrepancies or differences created in the translations are not binding and have no legal effect on Eris for compliance or enforcement purposes.