



General Description

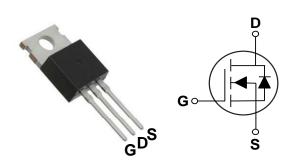
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	I _D
60 V	12 mΩ	55 A

Features

- 60V, 55A, $R_{DS(ON)}$ =12m Ω @ V_{GS} =10V
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

TO-220 Pin Configuration



Applications

- Motor Drive
- · Power Tools
- · LED Lighting

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
ı	Drain Current - Continuous (T _C =25°C)	55	Α
I _D	Drain Current - Continuous (T _C =100°C)	35	Α
I _{DM}	Drain Current - Pulsed (NOTE 1)	220	Α
EAS	Single Pulse Avalanche Energy (NOTE 2)	61	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	35	Α
P_D	Power Dissipation (T _C =25°C)	96	W
FD	Power Dissipation - Derate above 25°C	0.77	W/°C
TJ	Operating Junction Temperature Range	-50 to 150	°C
T_{STG}	Storage Temperature Range	-50 to 150	°C
Marking Code		NG012	

Thermal Characteristics					
Symbol	Parameter	Тур.	Max	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.3	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	60			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =60V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	uA
		V_{DS} =48V , V_{GS} =0V , T_{J} =125 $^{\circ}$ C			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =10A		10	12	mΩ
R _{DS(ON)}	(NOTE 3)	V_{GS} =4.5V , I_D =8A		12	15	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	V_{DS} =10V , I_{D} =6A		11.7		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge (NOTE 3 \ 4)			39.2	59	
Q_{gs}	Gate-Source Charge (NOTE 3 · 4)	V_{DS} =30V , V_{GS} =10V , I_{D} =10A		5.9	9	nC
Q_{gd}	Gate-Drain Charge (NOTE 3 \ 4)			8.8	14	
$T_{d(on)}$	Turn-On Delay Time (NOTE 3 \ 4)			9.6	18	
T _r	Rise Time (NOTE 3 \ 4)	V_{DD} =15V , V_{GS} =10V , R_{G} =6 Ω ,		28.2	54	ns
$T_{d(off)}$	Turn-Off Delay Time (NOTE 3 · 4)	I _D =1A		45.3	86	113
T_f	Fall Time (NOTE 3 \ 4)			10.9	21	
C _{iss}	Input Capacitance			2100	3050	
C _{oss}	Output Capacitance	V_{DS} =25V , V_{GS} =0V , F=1MHz		165	240	pF
C _{rss}	Reverse Transfer Capacitance			80	120	
Rg	Gate resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz		1.6	3.2	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			55	Α
I _{SM}	Pulsed Source Current (NOTE 3)				220	Α
V_{SD}	Diode Forward Voltage (NOTE 3)	V_{GS} =0V , I_{S} =1A , T_{J} =25 $^{\circ}$ C			1	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. $V_{DD}\text{=}25V,\,V_{GS}\text{=}10V,\,L\text{=}0.1\text{mH},\,I_{AS}\text{=}35\text{A},\,R_{G}\text{=}25\,\Omega,\,Starting}\,\,T_{J}\text{=}25^{\circ}\!\text{C}\,.$
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- ${\bf 4.} \ Essentially \ independent \ of \ operating \ temperature.$





Characteristics Curves

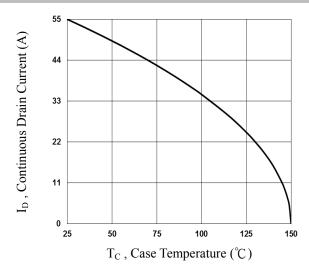


Fig.1 Continuous Drain Current vs. T_c

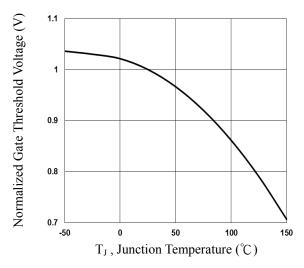


Fig. 3 Normalized V_{th} vs. T $_{J}$

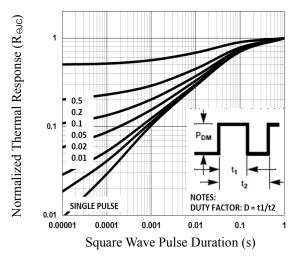


Fig. 5 Normalized Transient Response

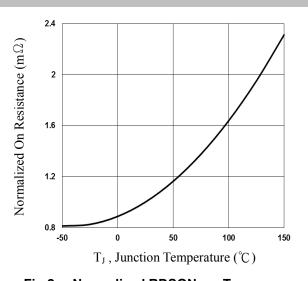


Fig.2 Normalized RDSON vs. T_J

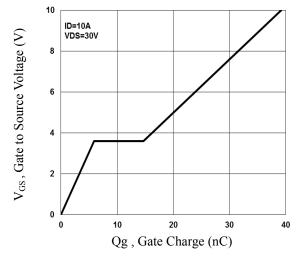


Fig. 4 Gate Charge Waveform

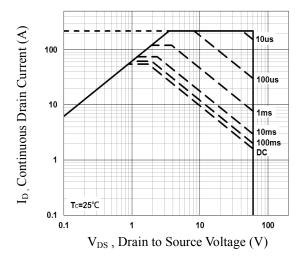


Fig. 6 Maximum Safe Operation Area





Characteristics Curves

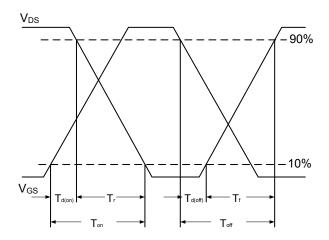


Fig.7 Switching Time Waveform

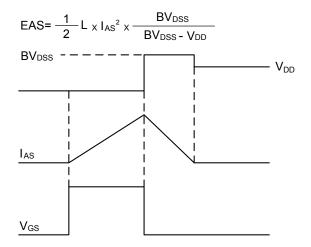
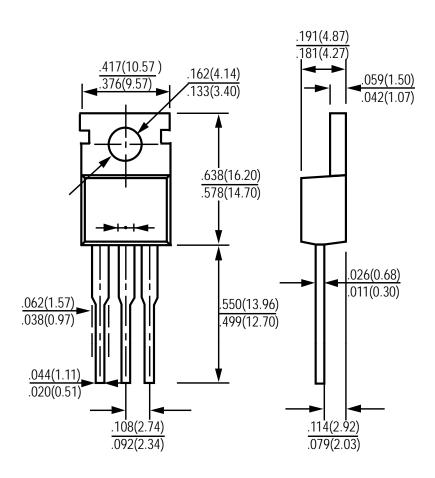


Fig.8 EAS Waveform





Package Outline Dimensions



TO-220 Dimensions in inches and (millimeters)





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