



S8MBC021

RoHS

## 30V N+P Dual Channel MOSFETs

## General Description

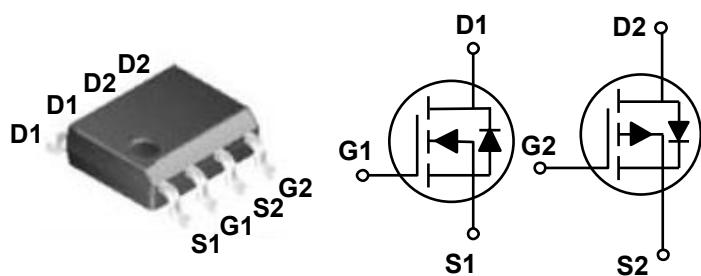
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

| $BV_{DSS}$ | $R_{DS(ON)}$ | $I_D$ |
|------------|--------------|-------|
| 30 V       | 21 mΩ        | 7 A   |
| -30 V      | 35 mΩ        | -6 A  |

## Features

- Fast Switching
- Green Device Available

SOP-8 Pin Configuration



## Applications

- DC Fan
- Inverter
- Synchronous Buck

Absolute Maximum Ratings  $T_A=25^\circ\text{C}$  unless otherwise noted

| Symbol       | Parameter                            | Rating        |          | Units            |
|--------------|--------------------------------------|---------------|----------|------------------|
| $V_{DS}$     | Drain-Source Voltage                 | 30            | -30      | V                |
| $V_{GS}$     | Gate-Source Voltage                  | $\pm 20$      | $\pm 20$ | V                |
| $I_D$        | Drain Current - Continuous           | 7             | -6       | A                |
| $I_{DM}$     | Drain Current - Pulsed (NOTE 1)      | 30            | -30      | A                |
| $P_D$        | Power Dissipation                    | 2             |          | W                |
| $T_J$        | Operating Junction Temperature Range | -55 to 150    |          | $^\circ\text{C}$ |
| $T_{STG}$    | Storage Temperature Range            | -55 to 150    |          | $^\circ\text{C}$ |
| Marking Code |                                      | BC021, 3047EM |          |                  |

## Thermal Characteristics

| Symbol          | Parameter                              | Typ. | Max. | Unit               |
|-----------------|--|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | ---  | 62.5 | $^\circ\text{C/W}$ |





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### Characteristics Curves

FIG. 1-Drain Current

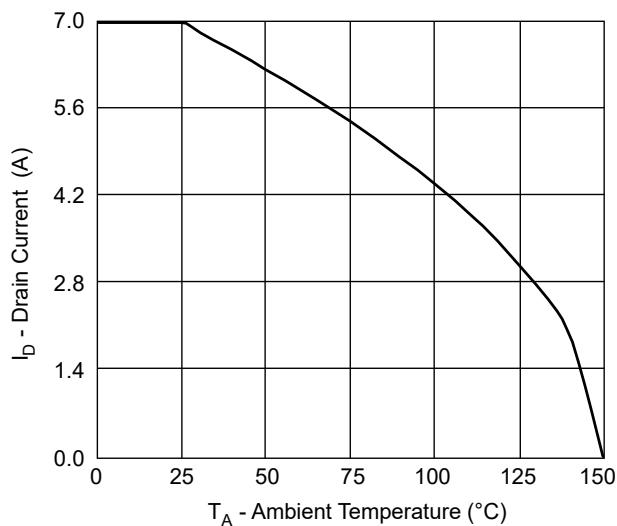


FIG. 2-Normalized  $R_{DS(ON)}$  vs  $T_J$

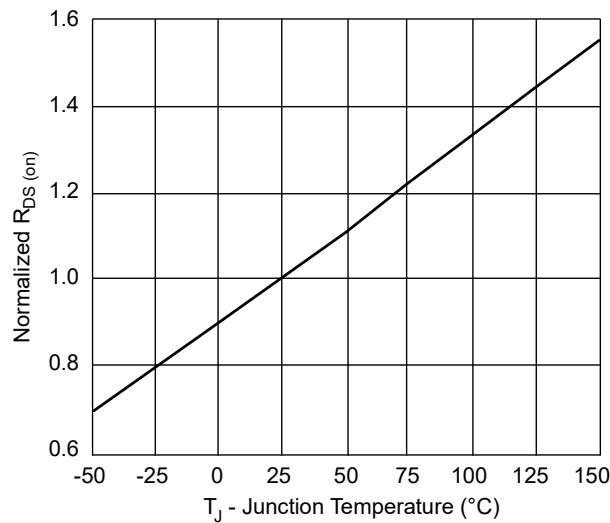


FIG. 5-Forward Characteristics

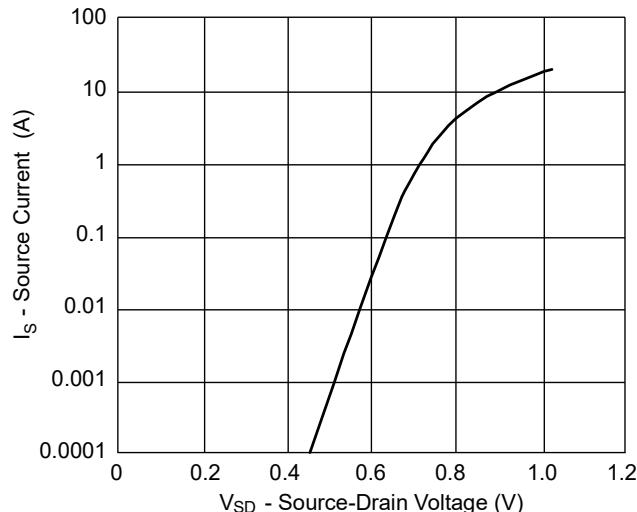


FIG. 2-Normalized  $V_{GS(th)}$  vs  $T_J$

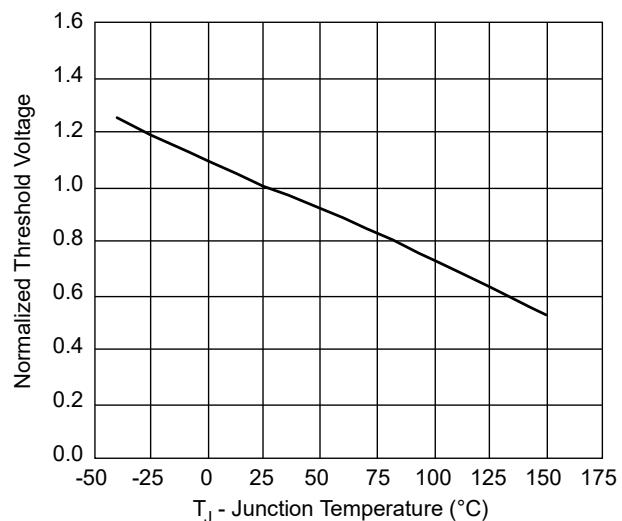
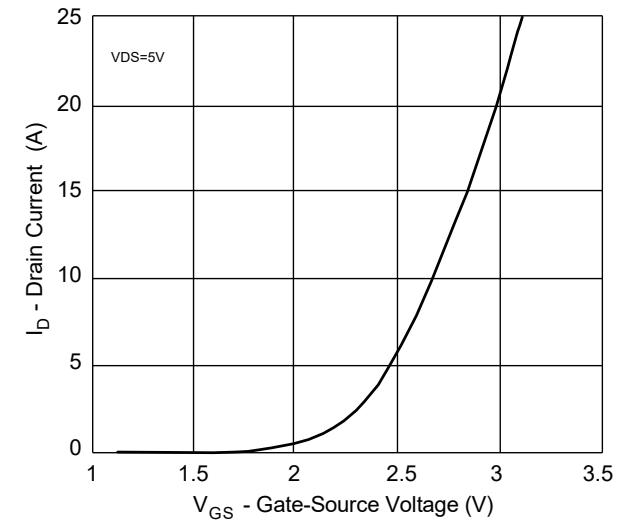


FIG. 4-Transfer Characteristics







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## 30V N+P Dual Channel MOSFETs

### Characteristics Curves

FIG. 6-Drain Current

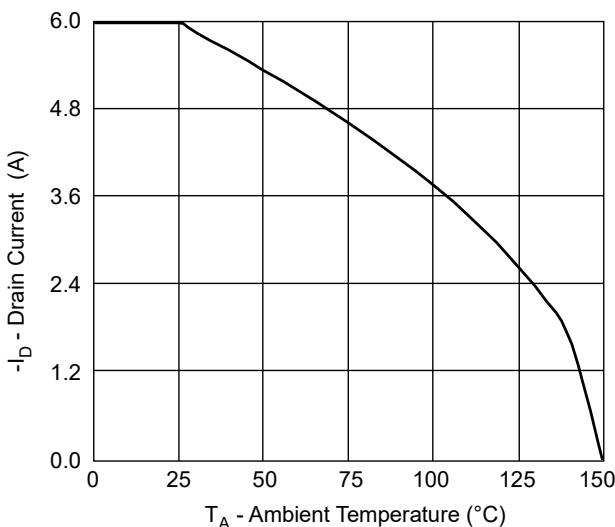


FIG. 8-Normalized R<sub>DS(ON)</sub> vs T<sub>J</sub>

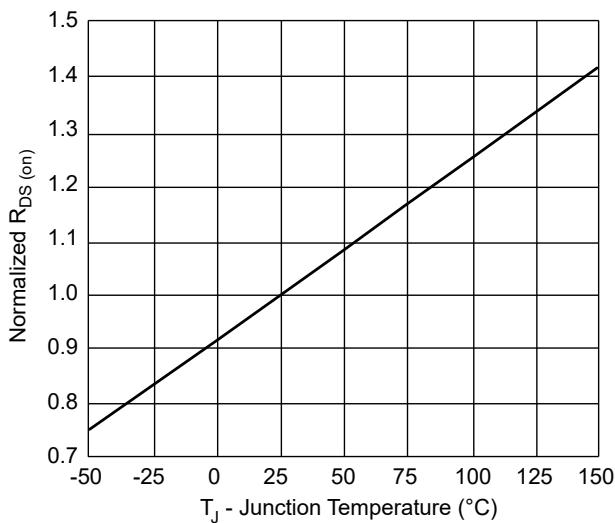


FIG. 10-Forward Characteristics

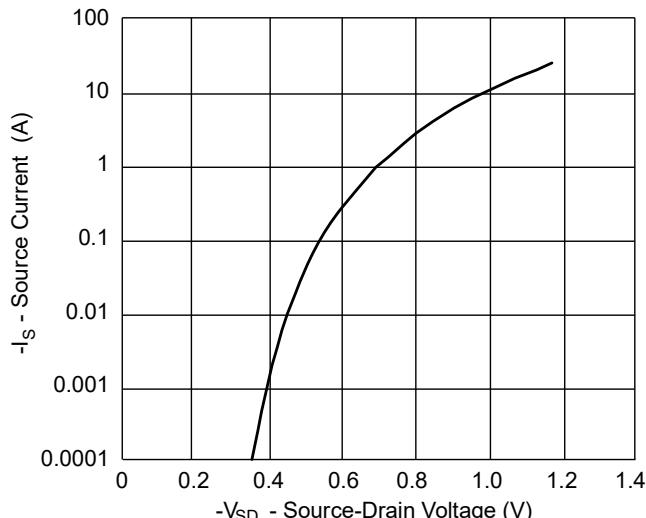


FIG. 7-Normalized V<sub>GS(th)</sub> vs T<sub>J</sub>

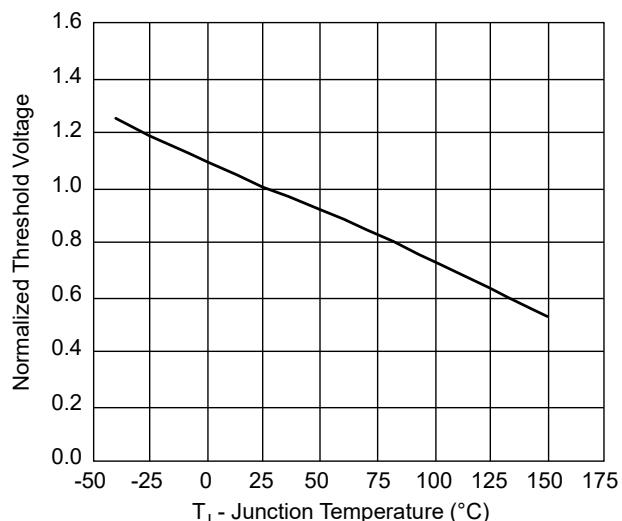
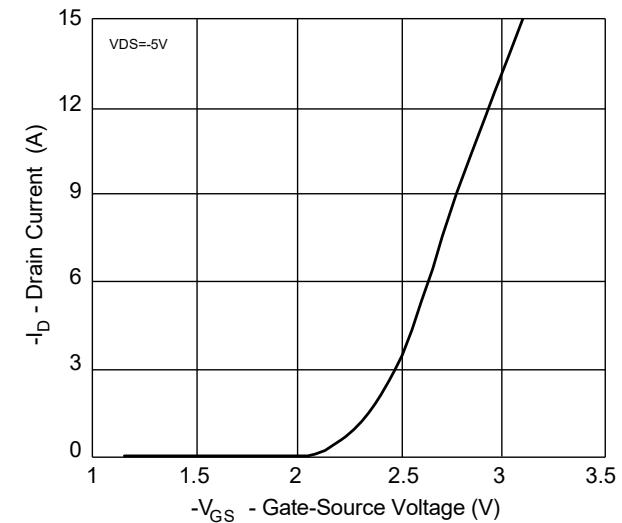


FIG. 9-Transfer Characteristics



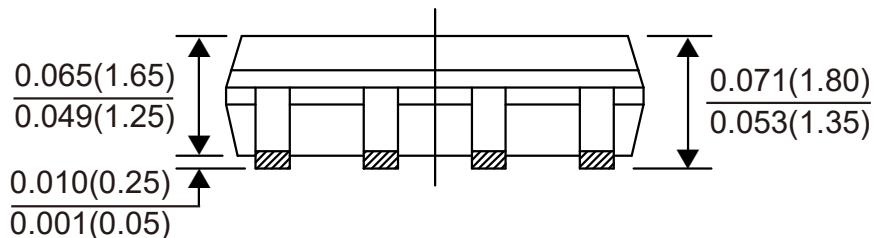
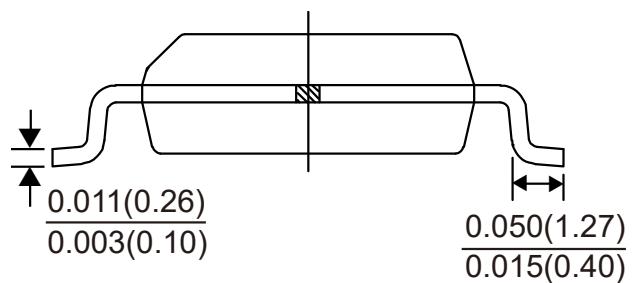
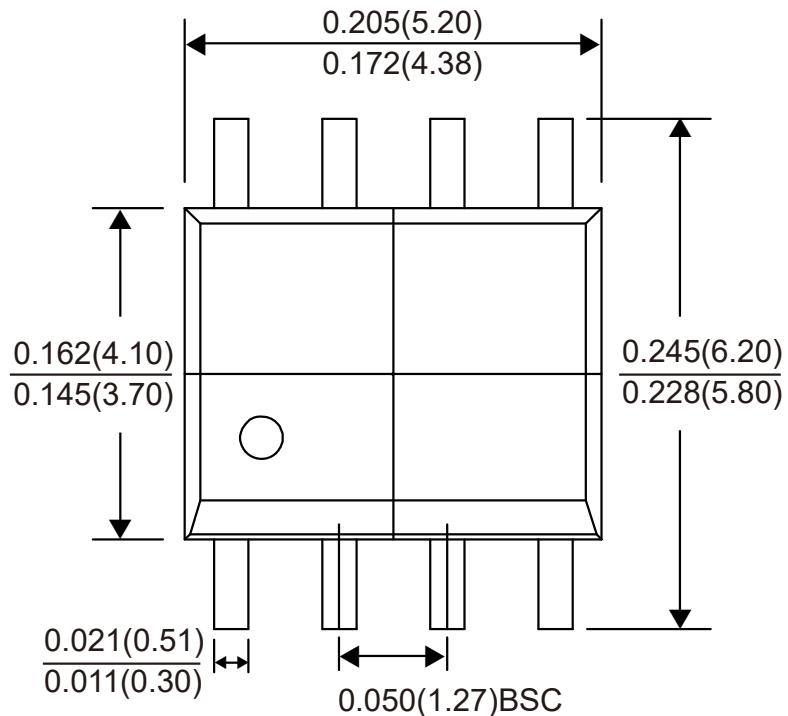


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Package Outline Dimensions



**SOP-8**

Dimensions in inches and (millimeters)



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