



General Description

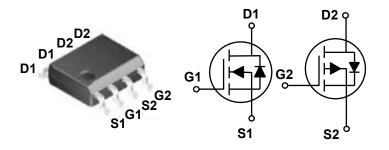
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	Ι _D
30 V	20 mΩ	8 A
-30 V	50 mΩ	-5.5 A

Features

- · Fast Switching
- · Green Device Available
- · Suit for 4.5V Gate Drive Applications

SOP-8 Pin Configuration



Applications

- DC Fan
- · Motor Drive Applications
- Networking
- Half / Full Bridge Topology

Absolute Maximu	Absolute Maximum Ratings T _c =25°C unless otherwise noted								
Symbol	Parameter	Rat	ting	Units					
V_{DS}	Drain-Source Voltage	30	-30	V					
V_{GS}	Gate-Source Voltage	±20	±20	V					
I _D	Drain Current - Continuous (T _A =25°C)	8	-5.5	Α					
I _{DM}	Drain Current - Pulsed (NOTE 1)	32	-22	Α					
EAS	Single Pulse Avalanche Energy (NOTE 2 · 6)	14	5	mJ					
IAS	Single Pulse Avalanched Current	17	-10	Α					
P _D	Power Dissipation (T _A =25°C)		2						
T _J	Operating Junction Temperature Range	-55 to	-55 to 150						
T _{STG}	Storage Temperature Range	-55 to	-55 to 150						
Marking Code		BC020 ,	DS3712						

Thermal Characteristics						
Symbol	Parameter	Тур.	Max.	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62.5	°C/W		





N Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =30V , V_{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =8A			20	mΩ
		V _{GS} =4.5V , I _D =5A			30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2		2.5	V
gfs	Forward Transconductance	V _{DS} =10V , I _D =3A		3		S

Dynamic and Switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			4.1		
Q_gs	Gate-Source Charge	V _{DS} =15V , V _{GS} =4.5V , I _D =8A		1		nC
Q_{gd}	Gate-Drain Charge	1		2.1		
$T_{d(on)}$	Turn-On Delay Time			2.8		
T _r	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =6 Ω ,		7.2		nS
$T_{d(off)}$	Turn-Off Delay Time	I _D =1A		15.8		113
T _f	Fall Time	1		4.6		
C _{iss}	Input Capacitance			345		
C _{oss}	Output Capacitance	V_{DS} =25V , V_{GS} =0V , F=1MHz		55		pF
C_{rss}	Reverse Transfer Capacitance	1		32		
R_g	Gate resistance	V _{GS} =0V , V _{DS} =0V , F=1MHz		3.2		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V,Force Current		-	8	Α
I _{SM}	Pulsed Source Current				16	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A			1	V

NOTES:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. $V_{DD}\text{=}25V,\,V_{GS}\text{=}10V,\,L\text{=}0.1\text{mH},\,I_{AS}\text{=}17A,\,R_{G}\text{=}25\Omega,\,Starting}\;T_{J}\text{=}25^{o}C.$
- 3. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Characteristics Curves

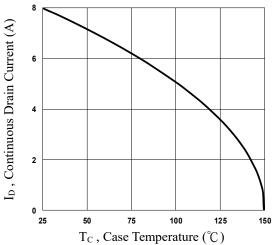


Fig.1 Continuous Drain Current vs. T c

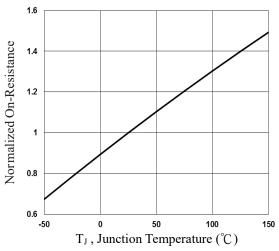


Fig.3 Normalized RDSON vs. T_J

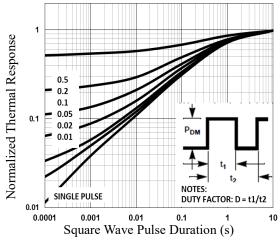


Fig.5 Normalized Transient Impedance

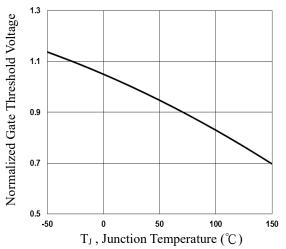


Fig.2 Normalized V_{th} vs. T_J

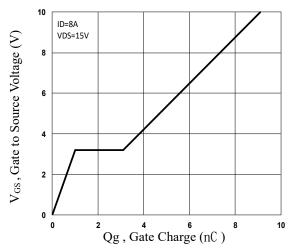


Fig.4 Gate Charge Characteristics

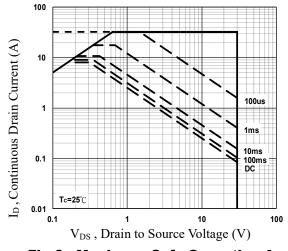


Fig.6 Maximum Safe Operation Area





P Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V , I_D = -250uA	-30			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} = -30V , V_{GS} = 0V			-1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = ±20V , V_{DS} = 0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V_{GS} = -10V , I_D = -5A			50	mΩ
		V_{GS} = -4.5V , I_{D} = -3A			90	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250uA$	-1.2		-2.5	V
gfs	Forward Transconductance	V_{DS} = -10V , I_{D} = -3A		3.5		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	V _{DS} = -15V , V _{GS} = -4.5V ,		5.1		
Q_gs	Gate-Source Charge	I _D = -3A		2		nC
Q_{gd}	Gate-Drain Charge	 		2.2		
$T_{d(on)}$	Turn-On Delay Time			3.4		
T_r	Rise Time	V_{DD} = -15V , V_{GS} = -10V ,		10.8		nS
$T_{d(off)}$	Turn-Off Delay Time	R_G = 6Ω , I_D = -1A		26.9		113
T_f	Fall Time			6.9		
C _{iss}	Input Capacitance			560		
C _{oss}	Output Capacitance	V_{DS} = -15V , V_{GS} = 0V , F= 1MHz		55		pF
C_{rss}	Reverse Transfer Capacitance			40		

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			-5.5	Α
I _{SM}	Pulsed Source Current				-11	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0 V , I_{S} = -1 A			-1	V

NOTES:

- 5. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 6. V_{DD} =-25V, V_{GS} =-10V, L=0.1mH, I_{AS} =-10A, R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C.
- 7. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 8. Essentially independent of operating temperature.





Characteristics Curves

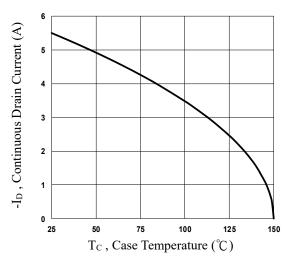


Fig.7 Continuous Drain Current vs. Tc

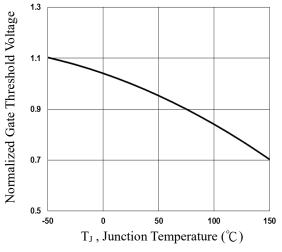


Fig.8 Normalized V_{th} vs. T_J

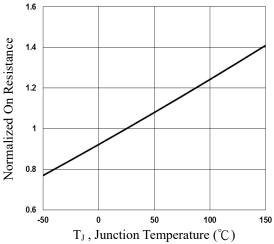


Fig.9 Normalized RDSON vs. TJ

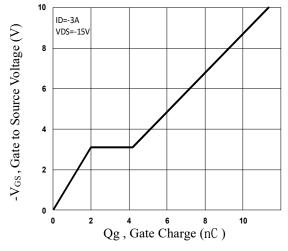


Fig.10 Gate Charge Characteristics

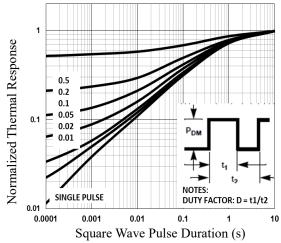


Fig.11 Normalized Transient Impedance

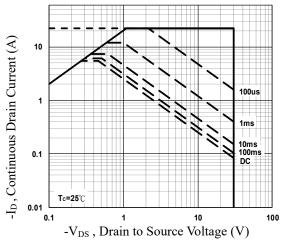
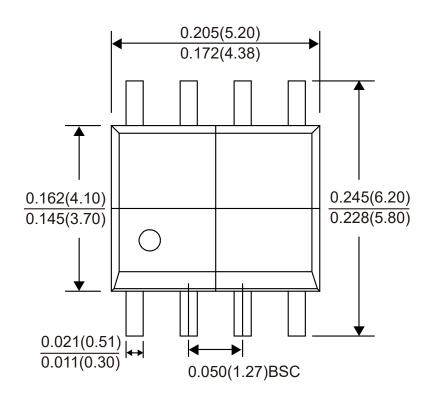


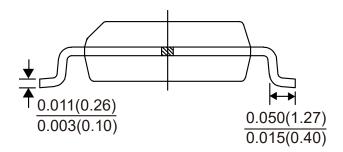
Fig.12 Maximum Safe Operation Area

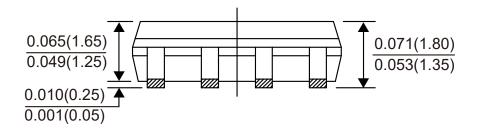




Package Outline Dimensions







SOP-8Dimensions in inches and (millimeters)





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