



# 80V N-Channel MOSFETs

## General Description

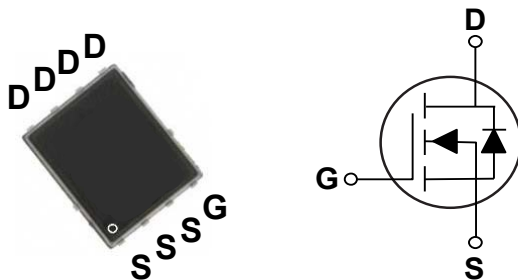
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
80 V	7.2 mΩ	80 A

## Features

- R<sub>DS(ON)</sub> ≤ 7.2mΩ@V<sub>GS</sub>=10V
- Fast switching
- Improved dv/dt capability
- Green Device Available

PPAK5X6 Pin Configuration



## Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

## Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	80	V
V <sub>GS</sub>	Gate-Source Voltage	+20 / -12	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =25°C)	80	A
	Drain Current – Continuous (T <sub>C</sub> =100°C)	50	A
I <sub>DM</sub>	Drain Current – Pulsed (NOTE 1)	320	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	201	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	64	A
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	125	W
	Power Dissipation – Derate above 25°C	1	W/°C
T <sub>J</sub>	Storage Temperature Range	-50 to 150	°C
T <sub>STG</sub>	Operating Junction Temperature Range	-50 to 150	°C
Marking Code		NK7P2	

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to Ambient	---	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	1	°C/W



**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	80	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =64V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	uA
I <sub>GSS(+)</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V	---	---	100	nA

**On Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	5.9	7.2	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.5	3.2	4.0	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =3A	---	10	---	S

**Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =40V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A (NOTE 3 · 4)	---	40.3	60	nC
Q <sub>gs</sub>	Gate-Source Charge		---	9.4	15	
Q <sub>gd</sub>	Gate-Drain Charge		---	16	25	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =40V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω, I <sub>D</sub> =1A (NOTE 3 · 4)	---	14.6	30	nS
T <sub>r</sub>	Rise Time		---	21.5	42	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	52	108	
T <sub>f</sub>	Fall Time		---	83.5	167	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, F=1MHz	---	2490	4980	pF
C <sub>oss</sub>	Output Capacitance		---	823	1640	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	44	80	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	1	---	Ω

**Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	80	A
I <sub>SM</sub>	Pulsed Source Current		---	---	160	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V
T <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =10V, I <sub>S</sub> =10A,	---	50	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/us, T <sub>J</sub> =25°C	---	70	---	nC

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=40V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=64A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



Characteristics Curves

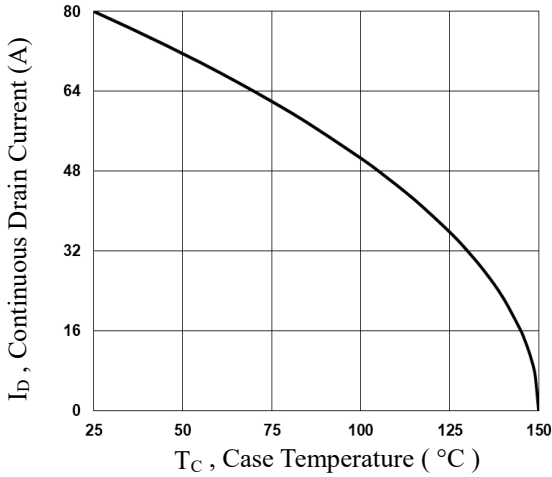


Fig.1 Continuous Drain Current vs.  $T_c$

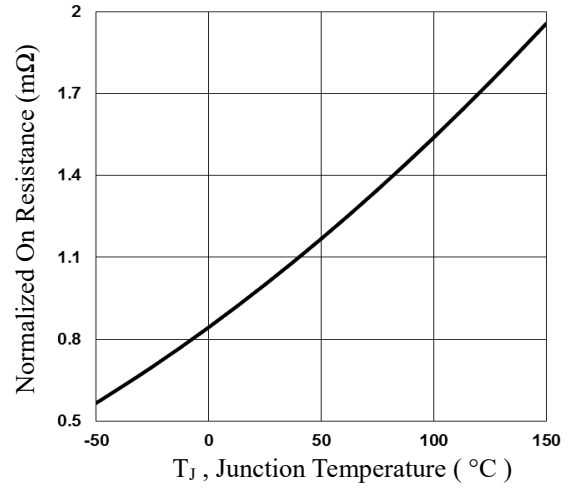


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$

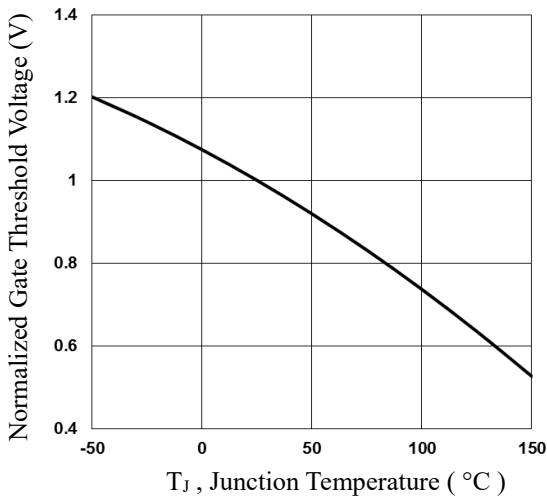


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

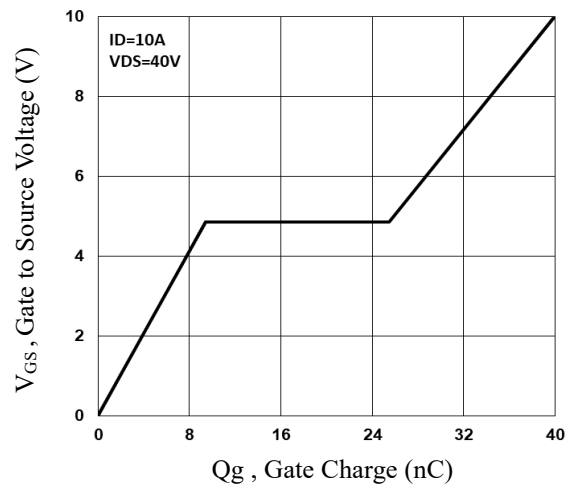


Fig.4 Gate Charge Characteristics

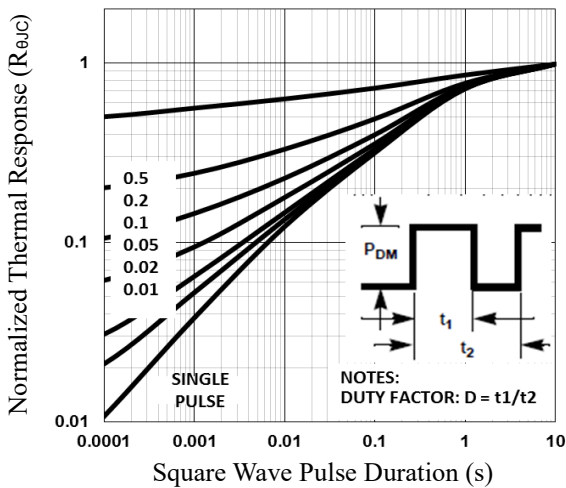


Fig.5 Normalized Transient Impedance

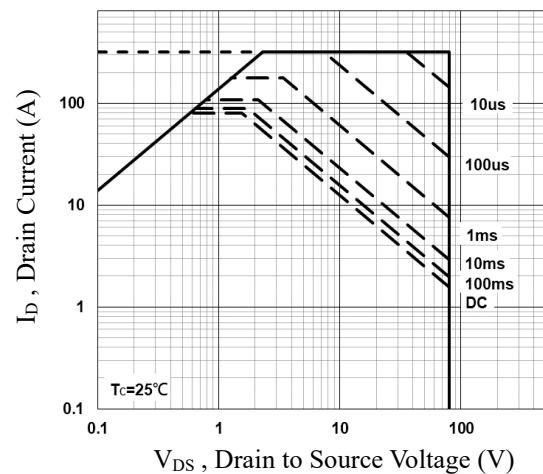


Fig.6 Maximum Safe Operation Area



Characteristics Curves

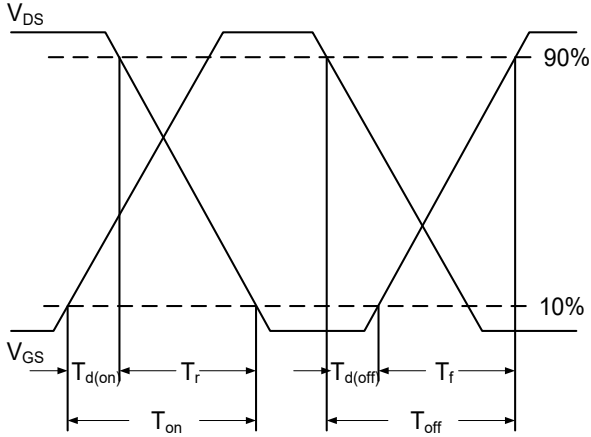


Fig.7 Switching Time Waveform

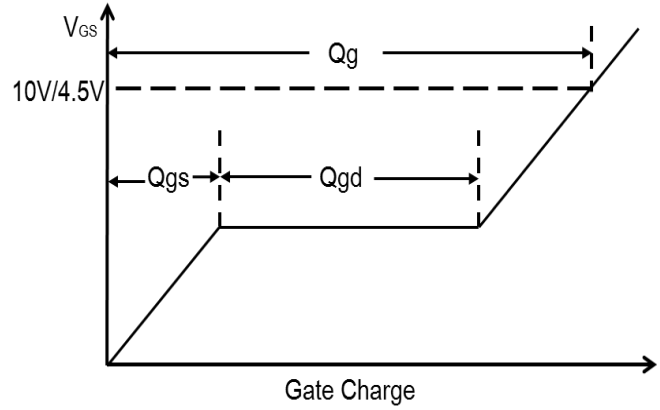
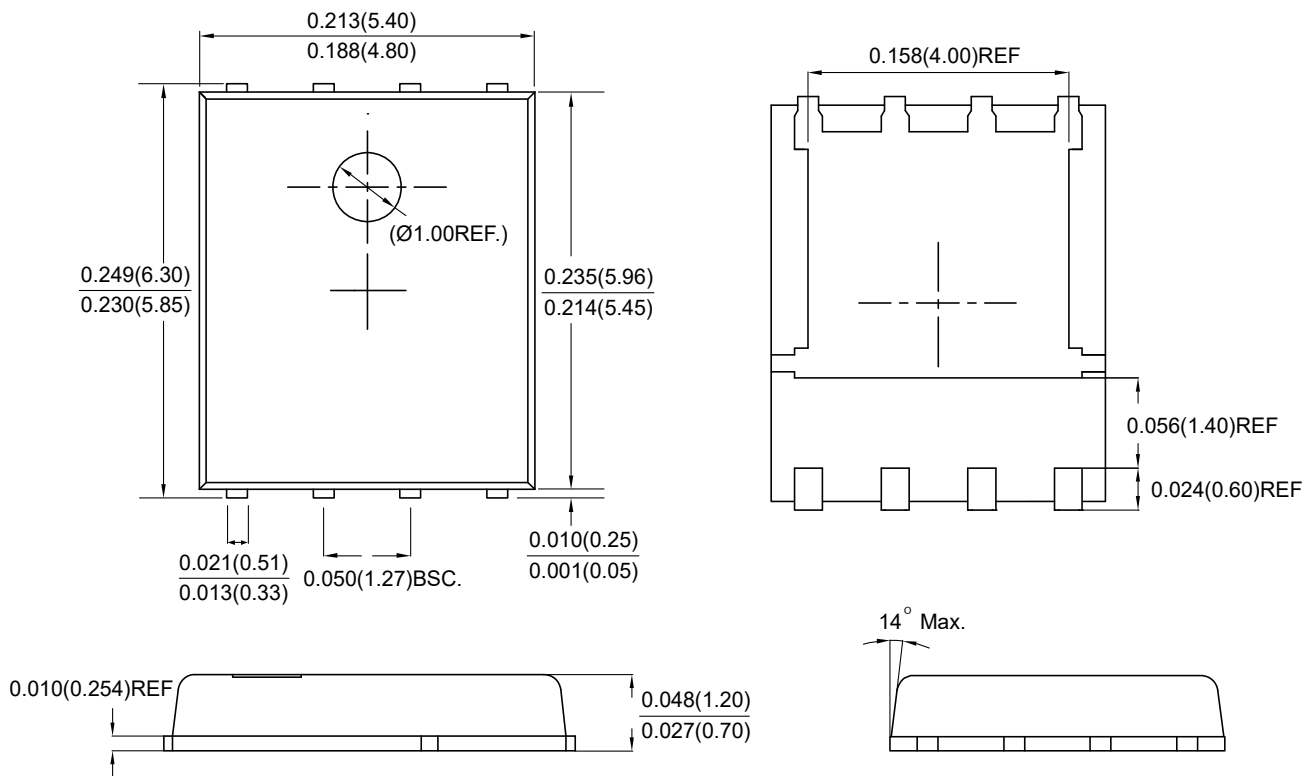


Fig.8 Gate Charge Waveform

Package Outline Dimensions



PPAK5X6

Dimensions in inches and (millimeters)



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