



### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

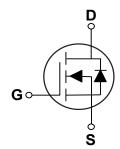
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
65 V	2.8 mΩ	130 A

### **Features**

- $R_{DS(ON)} \leq 2.8 m\Omega @V_{GS} = 10V$
- · Improved dv/dt Capability
- · Fast Switching
- · Green Device Available

# PPAK5X6 Pin Configuration





### **Applications**

- Networking
- · Load Switch
- · LED Applications
- Quick Charger

Absolute Maximum Ratings T <sub>A</sub> =25°C unless otherwise noted						
Symbol	Parameter Rating					
$V_{DS}$	Drain-Source Voltage	65	V			
$V_{GS}$	Gate-Source Voltage	+20 / -12	V			
1	Drain Current – Continuous (T <sub>C</sub> =25°C)	130	Α			
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =100°C)	82	Α			
I <sub>DM</sub>	Drain Current – Pulsed (NOTE 1)	520	Α			
EAS	Single Pulse Avalanche Energy (NOTE 2)	245	mJ			
IAS	Single Pulse Avalanche Current (NOTE 2)	70	Α			
$P_{D}$	Power Dissipation (T <sub>C</sub> =25°C)	142	W			
$T_J$	Operating Junction Temperature Range	-55 to 150	°C			
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C			
Marking Code		NG2P8 , DC6974X-5				

Thermal Characteristics					
Symbol	Parameter	Rating	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	62	°C/W		
$R_{ heta JC}$	Thermal Resistance Junction to Case	0.88	°C/W		





# Electrical Characteristics (T<sub>.1</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	65			V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =60V , $V_{GS}$ =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =20V , $V_{DS}$ =0V			100	nA

### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	IStatic Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A			2.8	mΩ
		$V_{GS}$ =4.5V , $I_D$ =10A			5.4	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.0		2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =5A		11		S

### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	1/ 401/ 1/ 401/ 1 404		59		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =48V , V <sub>GS</sub> =10V , I <sub>D</sub> =10A (NOTE 3 \ 4)		10.4		nC
$Q_{gd}$	Gate-Drain Charge	(10123 4)		19.6		
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}$ =30V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ , $I_{D}$ =1A (NOTE 3 $\cdot$ 4)		22		
T <sub>r</sub>	Rise Time			14		nS
$T_{d(off)}$	Turn-Off Delay Time			40		110
$T_f$	Fall Time			20		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz		4780		
C <sub>oss</sub>	Output Capacitance			1365		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			51		
$R_g$	Gate Resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , F=1MHz		1.8		Ω

# **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			130	Α
I <sub>SM</sub>	Pulsed Source Current				260	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A			1	V

### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =70A,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%.
- 4. Essentially independent of operating temperature.





### **Characteristics Curves**

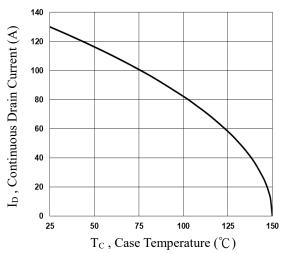


Fig.1 Continuous Drain Current vs. Tc

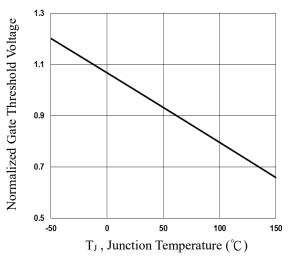


Fig.3 Normalized Vth vs. T<sub>J</sub>

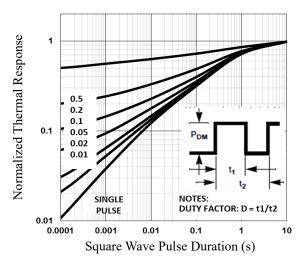


Fig.5 Normalized Transient Impedance

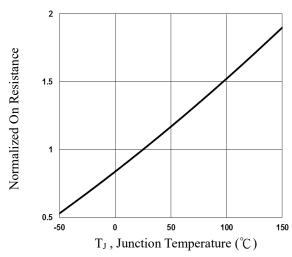


Fig.2 Normalized RDSON vs. T<sub>J</sub>

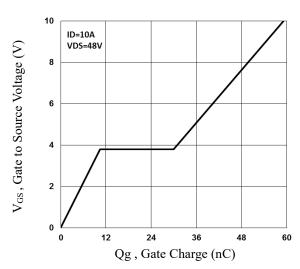


Fig.4 Gate Charge Characteristics

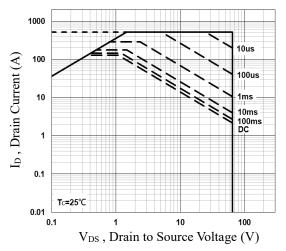


Fig.6 Maximum Safe Operation Area





# **Characteristics Curves**

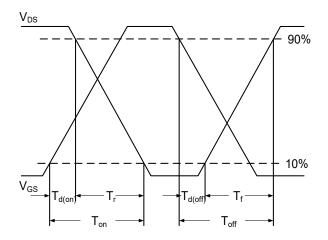


Fig.7 Switching Time Waveform

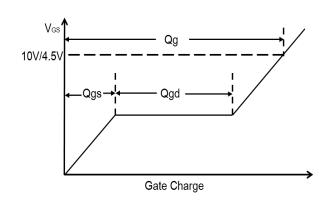
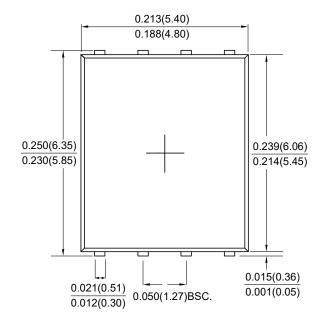
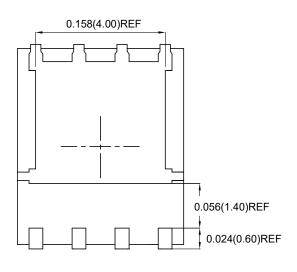


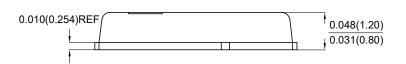
Fig.8 Gate Charge Waveform

# **Package Outline Dimensions**









### PPAK5X6

Dimensions in inches and (millimeters)





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