

#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

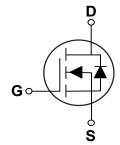
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>		
30V	8.5 mΩ	60 A		

#### **Features**

- $R_{DS(ON)} \leq 8.5 m\Omega @V_{GS} = 10V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

#### PPAK5X6 Pin Configuration





#### **Applications**

- · MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

Absolute Maximum Ratings T <sub>C</sub> =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V <sub>DS</sub>	Drain-Source Voltage	30	V			
$V_{GS}$	Gate-Source Voltage	±20	V			
1	Drain Current - Continuous (T <sub>C</sub> =25°C)	60	Α			
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	38	Α			
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	240	Α			
EAS	Single Pulse Avalanche Energy (NOTE 2)	31	mJ			
IAS	Single Pulse Avalanche Current (NOTE 2)	25	Α			
D	Power Dissipation (T <sub>C</sub> =25°C)	54	W			
$P_{D}$	Power Dissipation - Derate above 25°C	0.43	W/°C			
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C			
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C			
Marking Code		NC8P5				

Thermal Characteristics					
Symbol Parameter Typ. M		Max	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		2.3	°C/W	





#### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
I <sub>DSS</sub>	IDrain-Source Leakage Current	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C			1	uA
		$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
1 2	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =15A			8.5	mΩ
R <sub>DS(ON)</sub>	(NOTE 3)	V <sub>GS</sub> =4.5V , I <sub>D</sub> =8A			11	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	$V_{DS}$ =10V , $I_{D}$ =8A		5.6		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge			23.2		
$Q_{gs}$	Gate-Source Charge	$V_{DS}$ =15V , $V_{GS}$ =10V , $I_{D}$ =1A		3.2		nC
$Q_{gd}$	Gate-Drain Charge			3.7		
$T_{d(on)}$	Turn-On Delay Time		-	7		
T <sub>r</sub>	Rise Time	$V_{DD}$ =10V , $V_{GS}$ =10V , $R_{GEN}$ =2.7 $\Omega$ , $I_{D}$ =30A	-	76.6		nS
$T_{d(off)}$	Turn-Off Delay Time		-	27.1		110
$T_f$	Fall Time			52.6		
C <sub>iss</sub>	Input Capacitance		-	1180		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =15V , $V_{GS}$ =0V , F=1MHz	-	177		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			132		
$R_g$	Gate Resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , F=1MHz		3.2		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V,Force Current		-	60	Α
I <sub>SM</sub>	Pulsed Source Current			-	240	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =1A , $T_{J}$ =25 $^{\circ}$ C			1	V

#### NOTES

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =25A,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.





#### **Characteristics Curves**

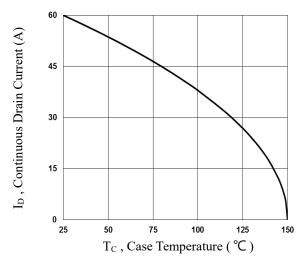


Fig.1 Continuous Drain Current vs. Tc

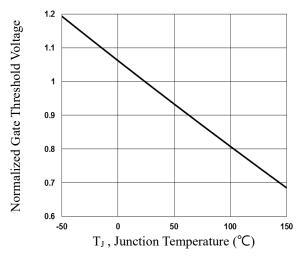


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

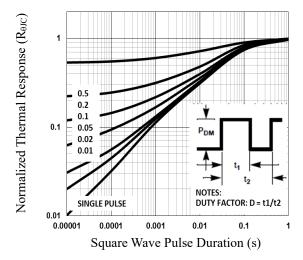


Fig.5 Normalized Transient Impedance

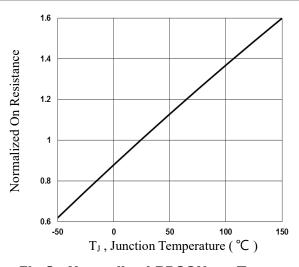


Fig.2 Normalized RDSON vs. T<sub>J</sub>

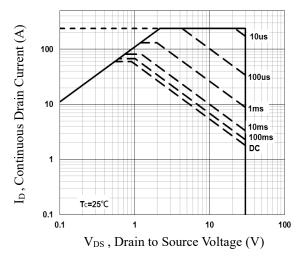


Fig.4 Maximum Safe Operation Area

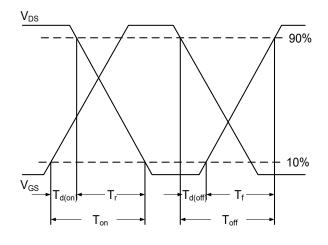


Fig.6 Switching Time Waveform





#### **Characteristics Curves**

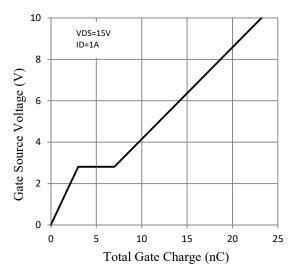
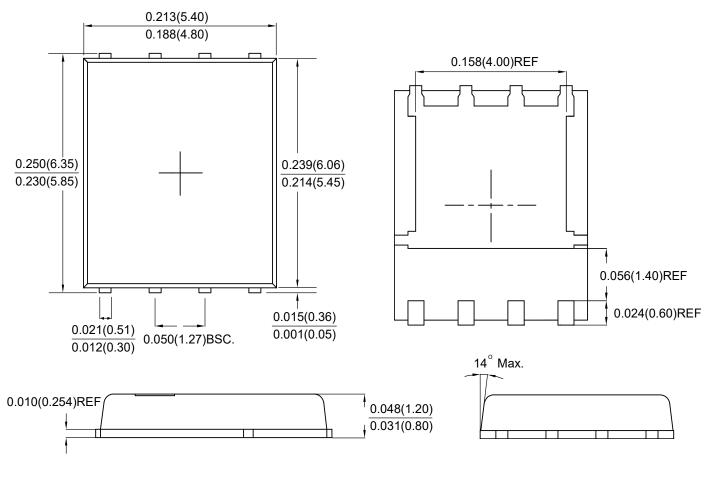


FIG. 7 Gate Charge Characteristics

#### **Package Outline Dimensions**



Dimensions in inches and (millimeters)





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