

**General Description**

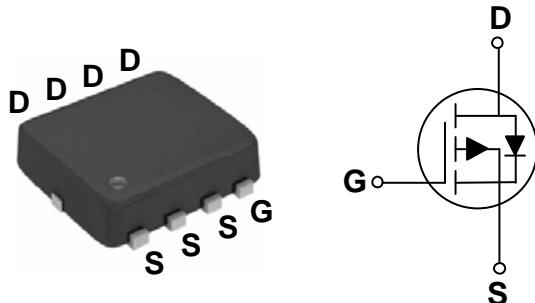
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	$R_{DS(ON)}$	I_D
-40 V	14 mΩ	-38 A

Features

- $R_{DS(ON)} \leq 14\text{m}\Omega @ V_{GS} = -10\text{V}$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

PPAK3X3 Pin Configuration

**Applications**

- MB / VGA / V_{CORE}
- POL Applications
- LED Application
- Load Switch

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current - Continuous ($T_C=25^\circ\text{C}$)	-38	A
	Drain Current - Continuous ($T_C=100^\circ\text{C}$)	-24	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	-152	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	130	mJ
IAS	Single Pulse Avalanche Current (NOTES 2)	51	A
P_D	Power Dissipation ($T_C=25^\circ\text{C}$)	52	W
	Power Dissipation - Derate above 25°C	0.42	$\text{W}/^\circ\text{C}$
T_J	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
Marking Code		PD014 , DC4903	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.4	$^\circ\text{C}/\text{W}$



P3MPD014

40V P-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D = -250\mu\text{A}$	-40	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}} = -40\text{V}$, $V_{\text{GS}} = 0\text{V}$, $T_J = 25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}} = -32\text{V}$, $V_{\text{GS}} = 0\text{V}$, $T_J = 125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 20\text{V}$, $V_{\text{DS}} = 0\text{V}$	---	---	± 100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = -10\text{V}$, $I_D = -15\text{A}$	---	11.3	14	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}$, $I_D = -8\text{A}$	---	15.6	21	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
g_{fs}	Forward Transconductance	$V_{\text{DS}} = -10\text{V}$, $I_D = -4\text{A}$	---	11	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	$V_{\text{DS}} = -32\text{V}$, $V_{\text{GS}} = -4.5\text{V}$, $I_D = -10\text{A}$ (NOTE 3 & 4)	---	22.2	40	nC
Q_{gs}	Gate-Source Charge		---	8.2	16	
Q_{gd}	Gate-Drain Charge		---	8.8	16	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = -20\text{V}$, $V_{\text{GS}} = -10\text{V}$, $R_G = 6\Omega$, $I_D = -1\text{A}$ (NOTE 3 & 4)	---	23	40	nS
T_r	Rise Time		---	10	20	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	135	250	
T_f	Fall Time		---	46	90	
C_{iss}	Input Capacitance	$V_{\text{DS}} = -25\text{V}$, $V_{\text{GS}} = 0\text{V}$, $F = 1\text{MHz}$	---	2757	4000	pF
C_{oss}	Output Capacitance		---	240	360	
C_{rss}	Reverse Transfer Capacitance		---	137	200	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G = V_D = 0\text{V}$, Force Current	---	---	-38	A
I_{SM}	Pulsed Source Current		---	---	-76	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}} = 0\text{V}$, $I_s = -1\text{A}$, $T_J = 25^\circ\text{C}$	---	---	-1	V

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}} = 25\text{V}$, $V_{\text{GS}} = 10\text{V}$, $L = 0.1\text{mH}$, $I_{\text{AS}} = 51\text{A}$, $R_G = 25$, Starting $T_J = 25^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.



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Pb RoHS

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Characteristics Curves

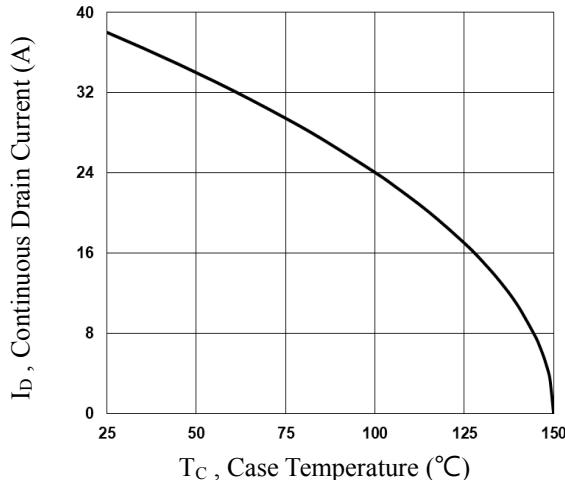


Fig.1 Continuous Drain Current vs. T_c

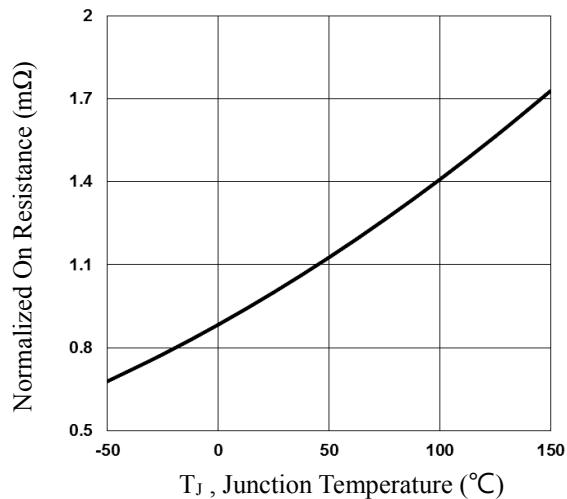


Fig.2 Normalized $R_{DS(ON)}$ vs. T_J

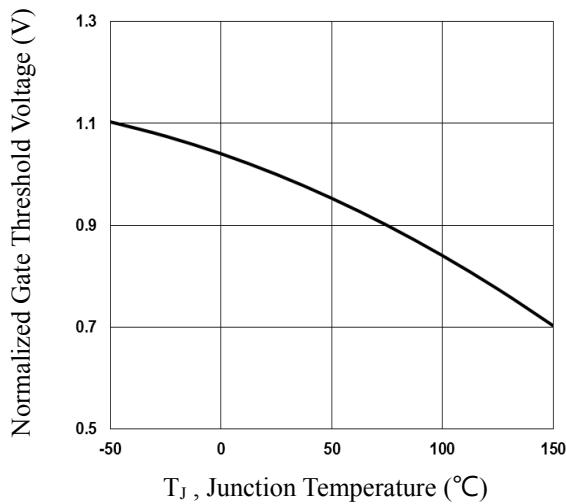


Fig.3 Normalized V_{th} vs. T_J

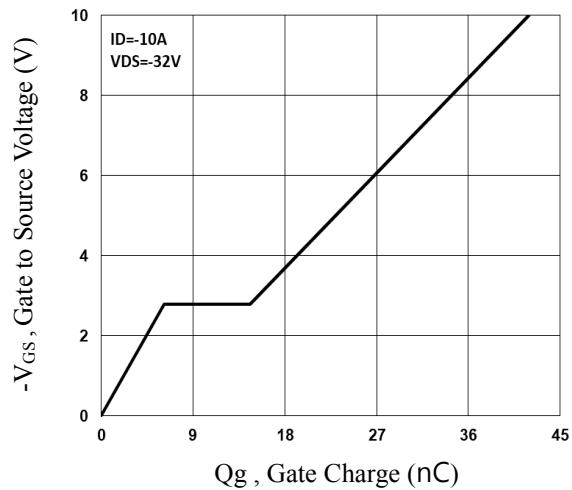


Fig.4 Gate Charge Waveform

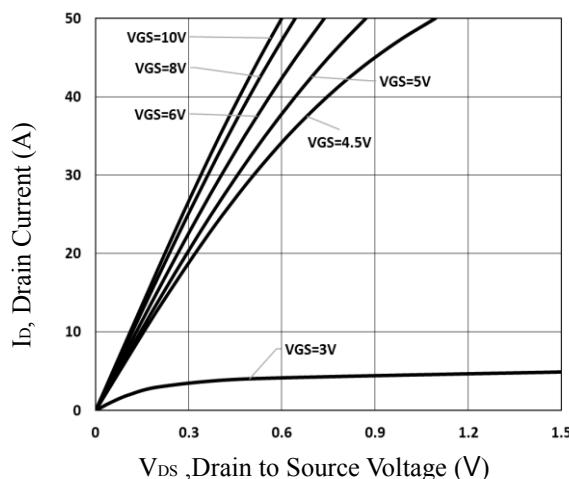


Fig.5 Typical Output Characteristics

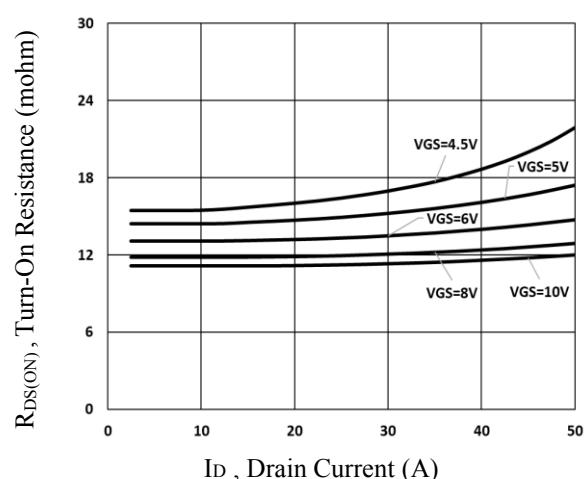


Fig.6 Turn-On Resistance vs. I_D

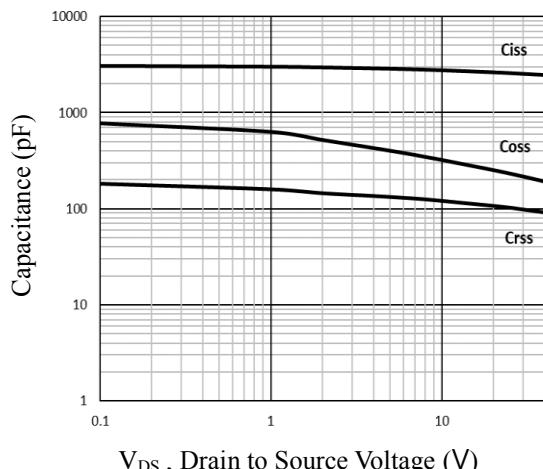


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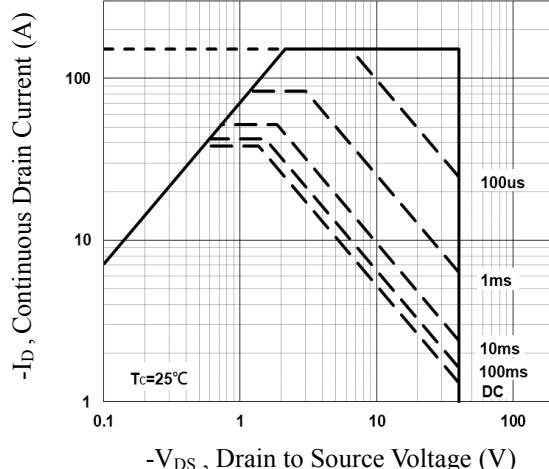
40V P-Channel MOSFETs

Characteristics Curves



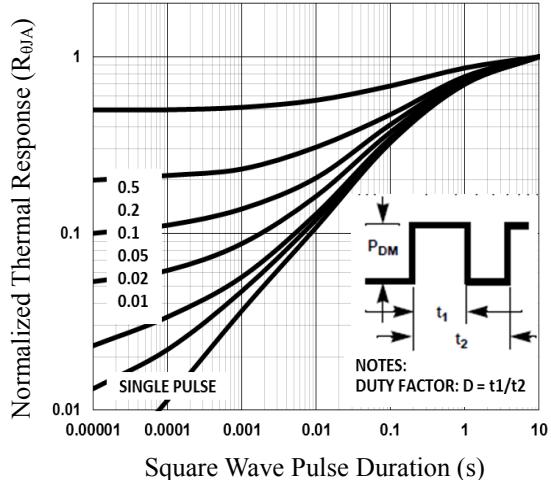
V_{DS}, Drain to Source Voltage (V)

Fig.7 Capacitance Characteristics



-V_{DS}, Drain to Source Voltage (V)

Fig.8 Maximum Safe Operation Area



Square Wave Pulse Duration (s)

Fig.9 Normalized Transient Impedance

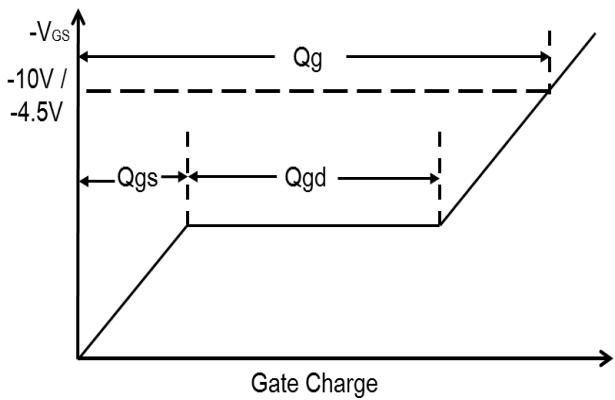


Fig.10 Gate Charge Waveform

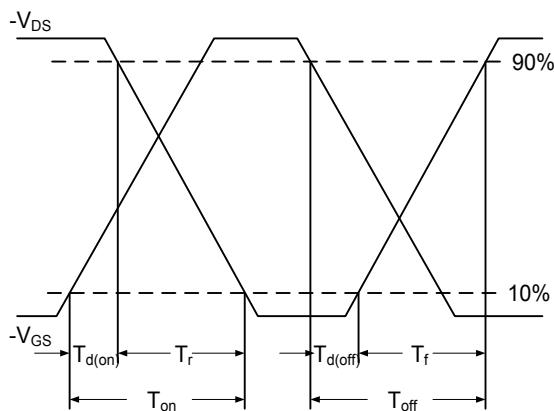


Fig.11 Switching Time Waveform

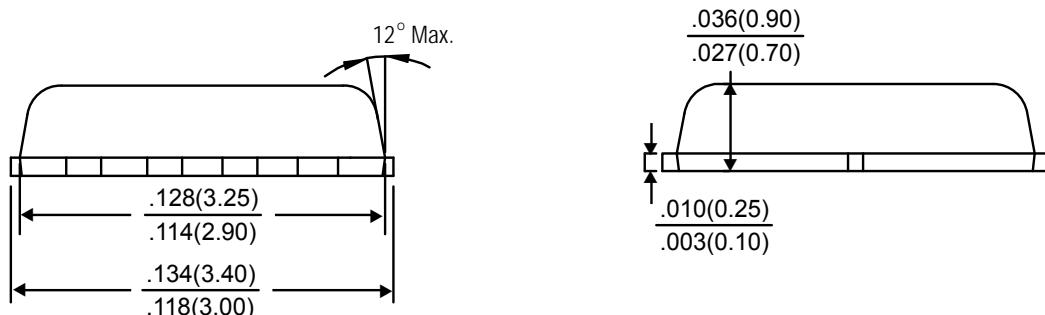
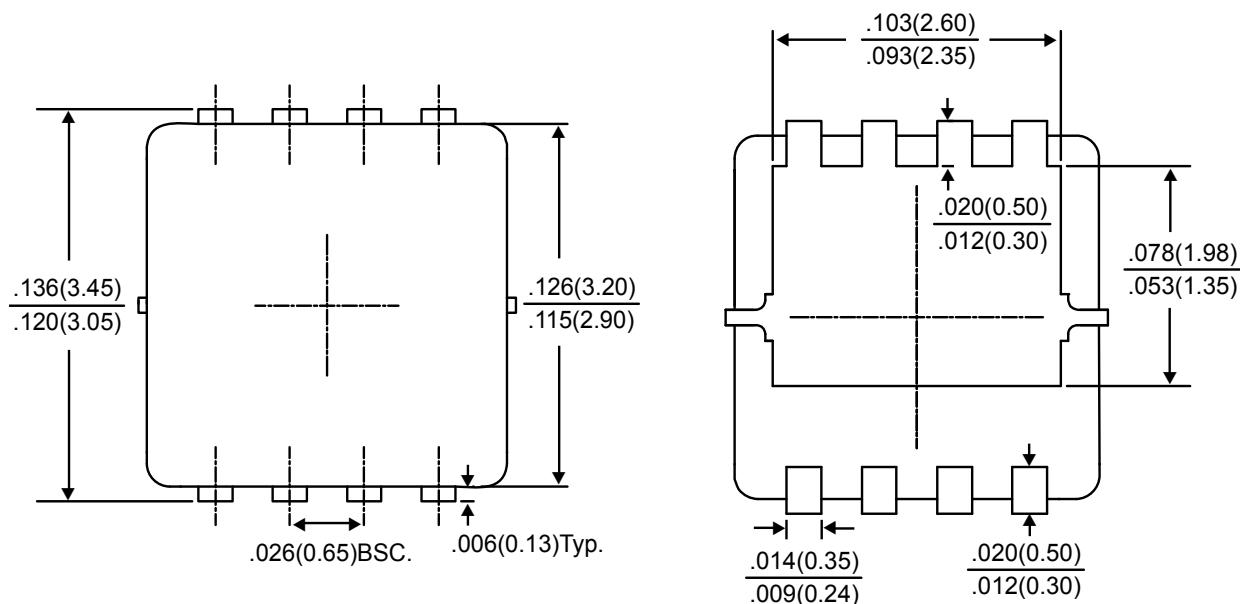


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40V P-Channel MOSFETs

Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)



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