



30V P-Channel MOSFETs

General Description

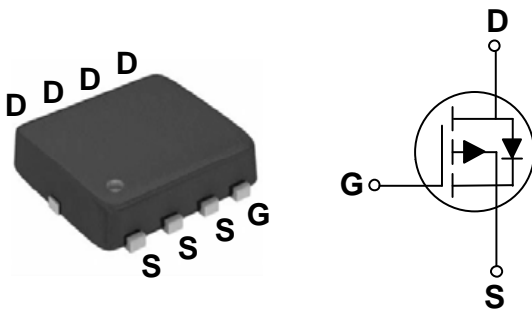
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	R_{DS(ON)}	I_D
-30 V	8.2 mΩ	-55 A

Features

- $R_{DS(ON)} \leq 8.2m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

PPAK3X3 Pin Configuration



Applications

- MB / VGA / V_{CORE}
- POL Applications
- LED Application
- Load Switch

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current - Continuous (T _C =25°C)	-55	A
	Drain Current - Continuous (T _C =100°C)	-34	A
I _{DM}	Drain Current - Pulsed (NOTE 1)	-220	A
P _D	Power Dissipation (T _C =25°C)	59	W
	Power Dissipation - Derate above 25°C	0.47	W/°C
T _J	Operating Junction Temperature Range	-50 to 150	°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
Marking Code		PC8P2 , 3903AZ	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to Ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	2.1	°C/W



Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250uA	-30	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} = -30V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} = -24V, V _{GS} =0V, T _J =100°C	---	---	-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = -10V, I _D = -10A	---	6.8	8.2	mΩ
		V _{GS} = -4.5V, I _D = -8A	---	9.5	12.5	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D = -250uA	-1.2	-1.4	-2.5	V
g _{fs}	Forward Transconductance	V _{DS} = -10V, I _D = -8A	---	14	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q _g	Total Gate Charge	V _{DS} = -15V, V _{GS} = -4.5V, I _D = -10A (NOTE 2、3)	---	35	56	nC
Q _{gs}	Gate-Source Charge		---	10.8	16	
Q _{gd}	Gate-Drain Charge		---	10.6	16	
T _{d(on)}	Turn-On Delay Time	V _{DD} = -15V, V _{GS} = -10V, R _G = 6Ω, I _D = -1A (NOTE 2、3)	---	24.5	38	nS
T _r	Rise Time		---	10.5	16	
T _{d(off)}	Turn-Off Delay Time		---	156.8	230	
T _f	Fall Time		---	50	75	
C _{iss}	Input Capacitance	V _{DS} = -15V, V _{GS} =0V, F=1MHz	---	3300	4800	pF
C _{OSS}	Output Capacitance		---	410	700	
C _{rss}	Reverse Transfer Capacitance		---	280	500	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	8.5	12	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G = V _D = 0V, Force Current	---	---	-55	A
I _{SM}	Pulsed Source Current		---	---	-110	A
V _{SD}	Diode Forward Voltage	V _{GS} = 0V, I _S = -1A, T _J =25°C	---	---	-1	V

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



Characteristics Curves

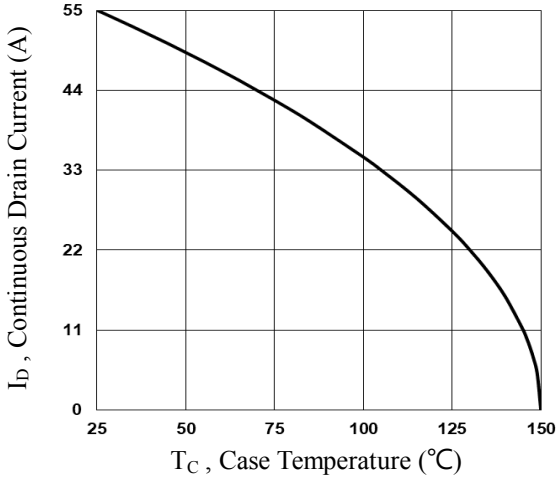


Fig.1 Continuous Drain Current vs. T_c

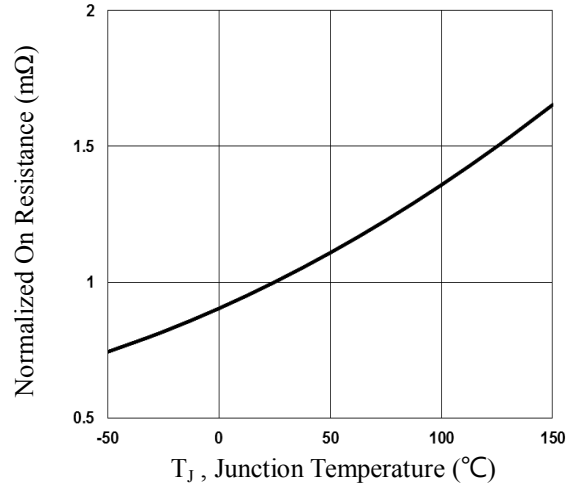


Fig.2 Normalized R_{DS(on)} vs. T_j

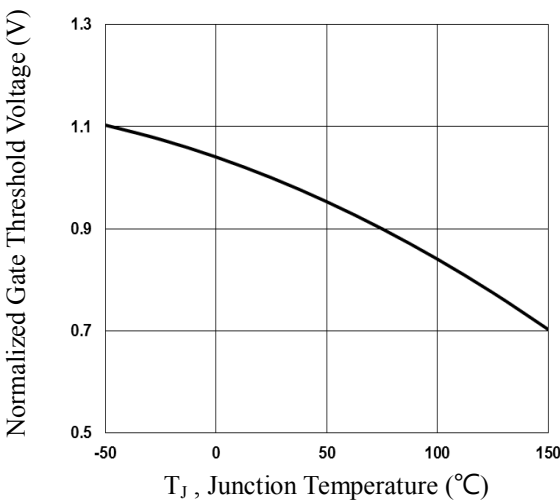


Fig.3 Normalized V_{th} vs. T_j

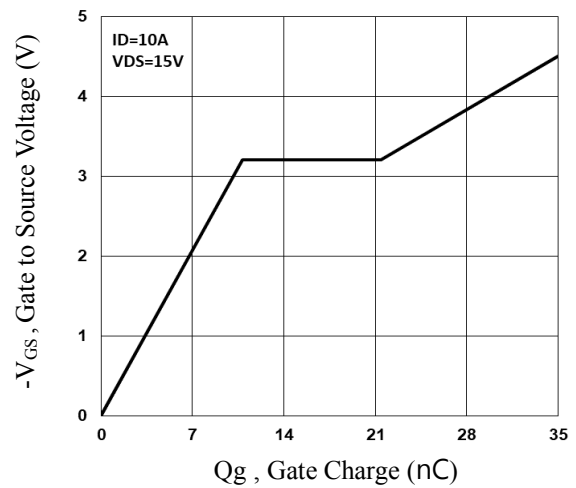


Fig.4 Gate Charge Waveform

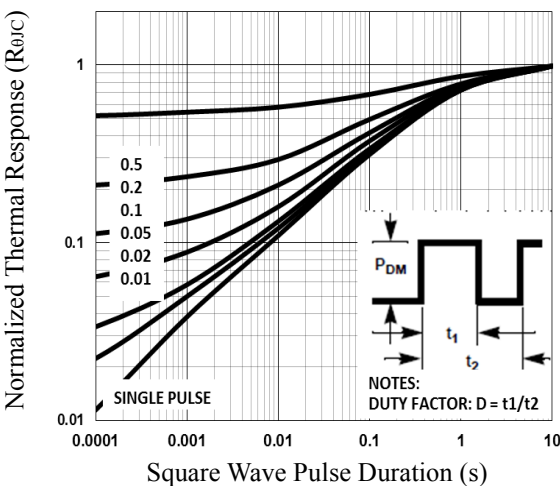


Fig.5 Normalized Transient Impedance

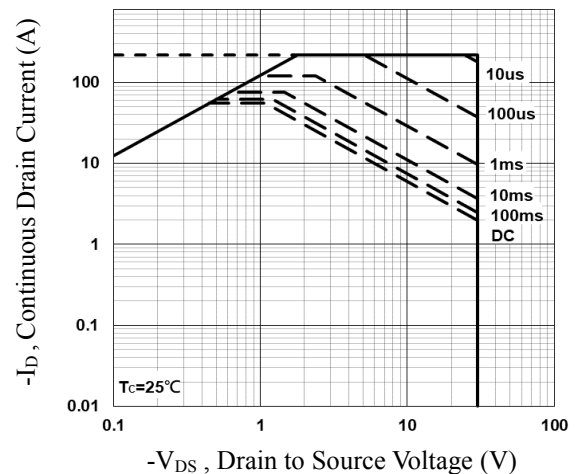


Fig.6 Maximum Safe Operation Area



Characteristics Curves

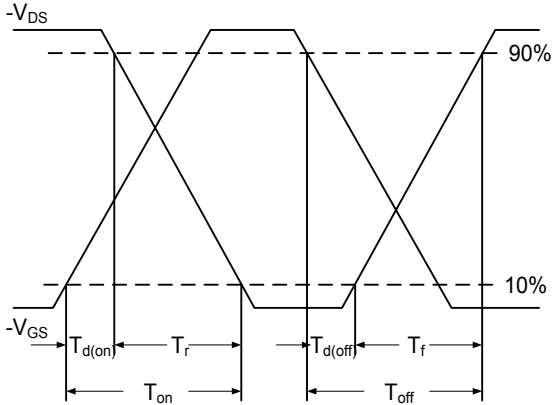


Fig.7 Switching Time Waveform

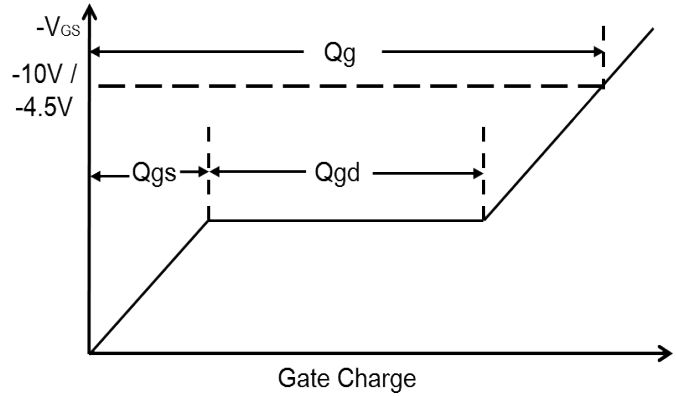
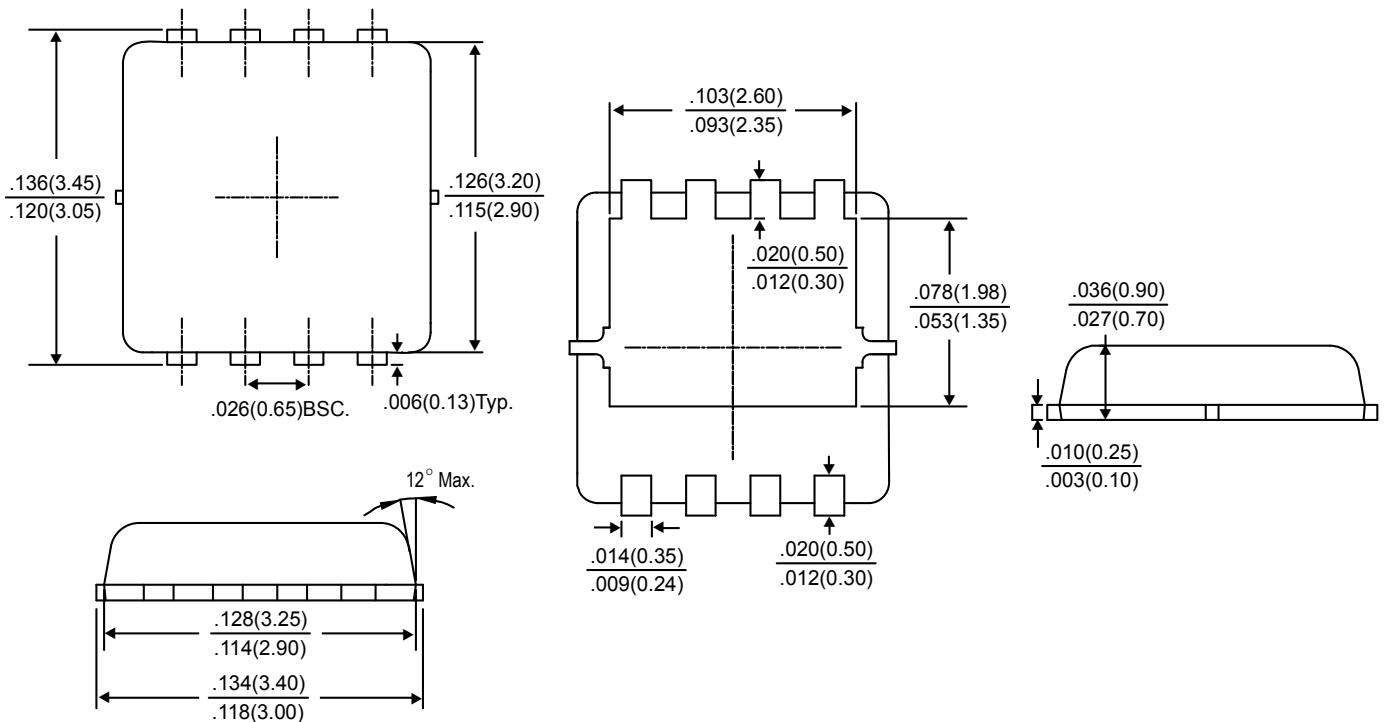


Fig.8 Gate Charge Waveform

Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)



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