



# 100V N-Channel MOSFETs

## General Description

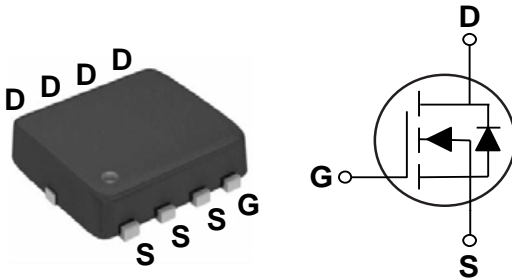
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

| $BV_{DSS}$ | $R_{DS(ON)}$    | $I_D$ |
|------------|-----------------|-------|
| 100 V      | 10.3 m $\Omega$ | 48 A  |

## Features

- 100V, 48A,  $R_{DS(ON)}=10.3m\Omega @V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

PPAK3X3 Pin Configuration



## Applications

- Networking
- Load Switch
- LED applications

## Absolute Maximum Ratings $T_C=25^\circ C$ unless otherwise noted

| Symbol       | Parameter  | Rating         | Units         |
|--------------|--|----------------|---------------|
| $V_{DS}$     | Drain-Source Voltage                             | 100            | V             |
| $V_{GS}$     | Gate-Source Voltage                              | $\pm 20$       | V             |
| $I_D$        | Drain Current - Continuous ( $T_C=25^\circ C$ )  | 48             | A             |
|              | Drain Current - Continuous ( $T_C=100^\circ C$ ) | 30             | A             |
| $I_{DM}$     | Drain Current - Pulsed (NOTE 1)                  | 192            | A             |
| EAS          | Single Pulse Avalanche Energy (NOTE 2)           | 115            | mJ            |
| IAS          | Single Pulse Avalanche Current (NOTE 2)          | 48             | A             |
| $P_D$        | Power Dissipation ( $T_C=25^\circ C$ )           | 61             | W             |
|              | Power Dissipation - Derate above $25^\circ C$    | 0.49           | W/ $^\circ C$ |
| $T_J$        | Operating Junction Temperature Range             | -50 to 150     | $^\circ C$    |
| $T_{STG}$    | Storage Temperature Range                        | -50 to 150     | $^\circ C$    |
| Marking Code |  | NM010 / 0982BZ |               |

## Thermal Characteristics

| Symbol          | Parameter                              | Typ. | Max. | Unit         |
|-----------------|--|------|------|--------------|
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | ---  | 62   | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case    | ---  | 2.04 | $^\circ C/W$ |



**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

**Off Characteristics**

| Symbol            | Parameter                      | Conditions  | Min. | Typ. | Max. | Unit |
|-------------------|--------------------------------|---|------|------|------|------|
| BV <sub>DSS</sub> | Drain-Source Breakdown Voltage | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA                      | 100  | ---  | ---  | V    |
| I <sub>DSS</sub>  | Drain-Source Leakage Current   | V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C | ---  | ---  | 1    | uA   |
|                   |                                | V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C | ---  | ---  | 10   | uA   |
| I <sub>GSS</sub>  | Gate-Source Leakage Current    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V                      | ---  | ---  | ±100 | nA   |

**On Characteristics**

| Symbol              | Parameter                         | Conditions   | Min. | Typ. | Max. | Unit |
|---------------------|-----------------------------------|--|------|------|------|------|
| R <sub>DS(ON)</sub> | Static Drain-Source On-Resistance | V <sub>GS</sub> =10V, I <sub>D</sub> =20A                | ---  | 8.6  | 10.3 | mΩ   |
|                     |                                   | V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A               | ---  | 11.5 | 15   |      |
| V <sub>GS(th)</sub> | Gate Threshold Voltage            | V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA | 1.2  | 1.5  | 2.5  | V    |
| g <sub>fs</sub>     | Forward Transconductance          | V <sub>DS</sub> =10V, I <sub>D</sub> =3A                 | ---  | 10   | ---  | S    |

**Dynamic and switching Characteristics**

| Symbol              | Parameter                      | Conditions   | Min. | Typ. | Max. | Unit |
|---------------------|--------------------------------|--|------|------|------|------|
| Q <sub>g</sub>      | Total Gate Charge (NOTE 3、4)   | V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A                    | ---  | 26.1 | 39   | nC   |
| Q <sub>gs</sub>     | Gate-Source Charge (NOTE 3、4)  |  | ---  | 6.5  | 10   |      |
| Q <sub>gd</sub>     | Gate-Drain Charge (NOTE 3、4)   |  | ---  | 5.3  | 8    |      |
| T <sub>d(on)</sub>  | Turn-On Delay Time (NOTE 3、4)  | V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω, I <sub>D</sub> =1A | ---  | 14.2 | 28   | nS   |
| T <sub>r</sub>      | Rise Time (NOTE 3、4)           |  | ---  | 20.8 | 42   |      |
| T <sub>d(off)</sub> | Turn-Off Delay Time (NOTE 3、4) |  | ---  | 42   | 84   |      |
| T <sub>f</sub>      | Fall Time (NOTE 3、4)           |  | ---  | 30   | 60   |      |
| C <sub>iss</sub>    | Input Capacitance              | V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, F=1MHz                                  | ---  | 1430 | 2145 | pF   |
| C <sub>oss</sub>    | Output Capacitance             |  | ---  | 215  | 322  |      |
| C <sub>rss</sub>    | Reverse Transfer Capacitance   |  | ---  | 8    | 20   |      |
| R <sub>g</sub>      | Gate resistance                | V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz                                   | ---  | 1.04 | ---  | Ω    |

**Drain-Source Diode Characteristics and Ratings**

| Symbol          | Parameter                 | Conditions  | Min. | Typ. | Max. | Unit |
|-----------------|---------------------------|---|------|------|------|------|
| I <sub>S</sub>  | Continuous Source Current | V <sub>G</sub> =V <sub>D</sub> =0V, Force Current             | ---  | ---  | 48   | A    |
| I <sub>SM</sub> | Pulsed Source Current     |   | ---  | ---  | 96   | A    |
| V <sub>SD</sub> | Diode Forward Voltage     | V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C | ---  | ---  | 1    | V    |
| t <sub>rr</sub> | Reverse Recovery Time     | I <sub>S</sub> =5A, di/dt=100A/us,                            | ---  | 38   | ---  | nS   |
| Q <sub>rr</sub> | Reverse Recovery Charge   | T <sub>J</sub> =25°C  | ---  | 80   | ---  | nC   |

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=48A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



Characteristics Curves

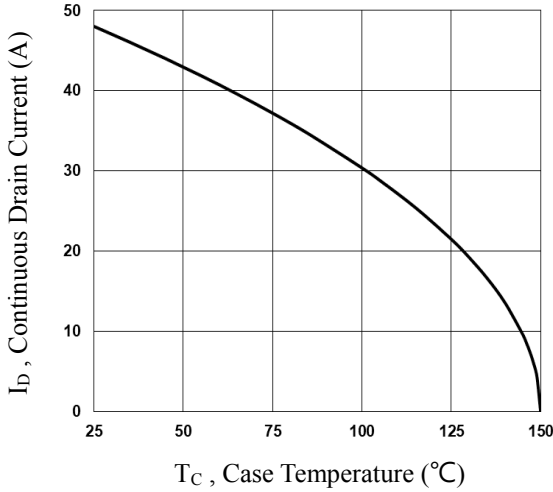


Fig.1 Continuous Drain Current vs.  $T_c$

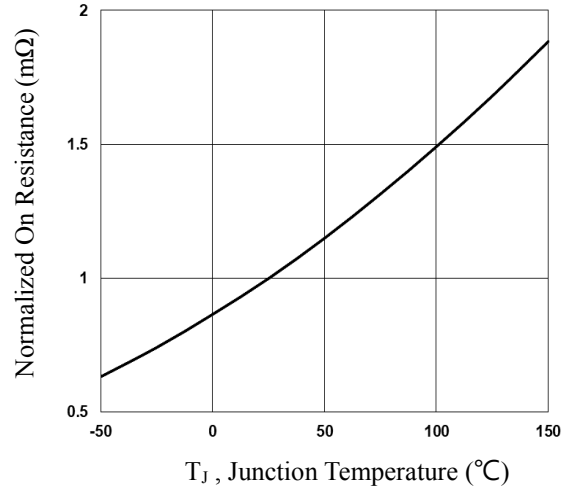


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$

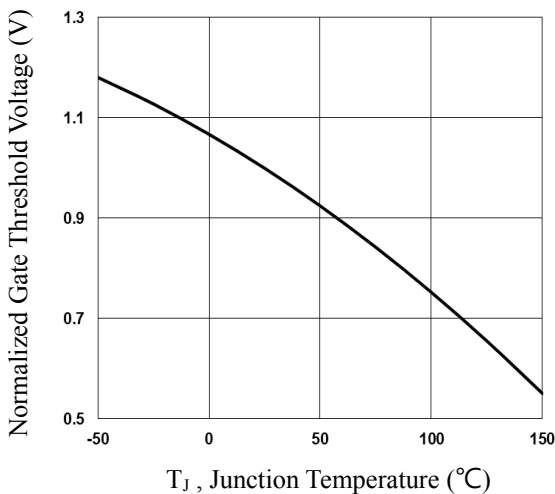


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

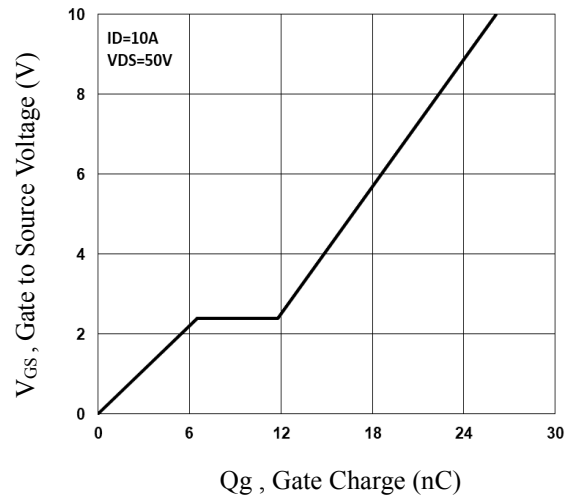


Fig.4 Gate Charge Characteristics

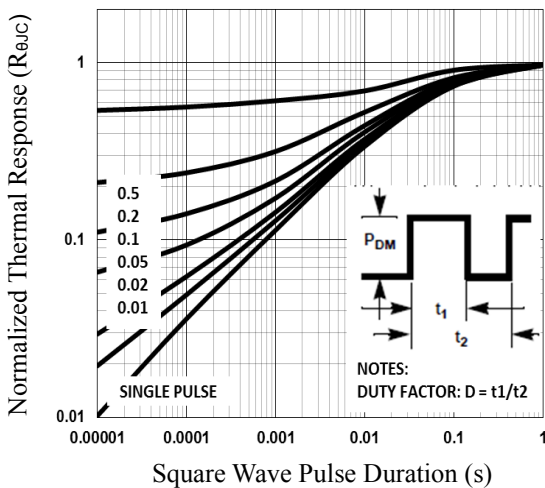


Fig.5 Normalized Transient Impedance

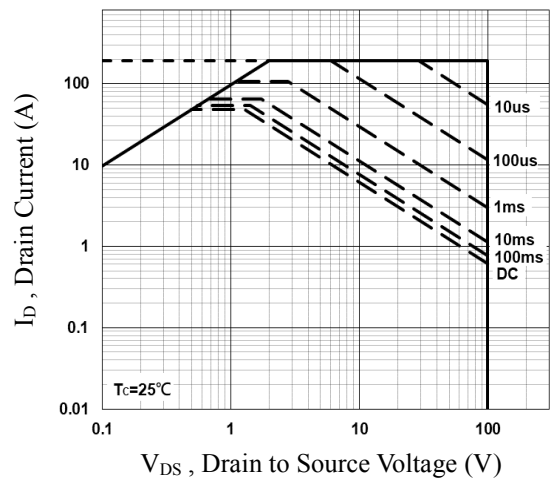


Fig.6 Maximum Safe Operation Area



Characteristics Curves

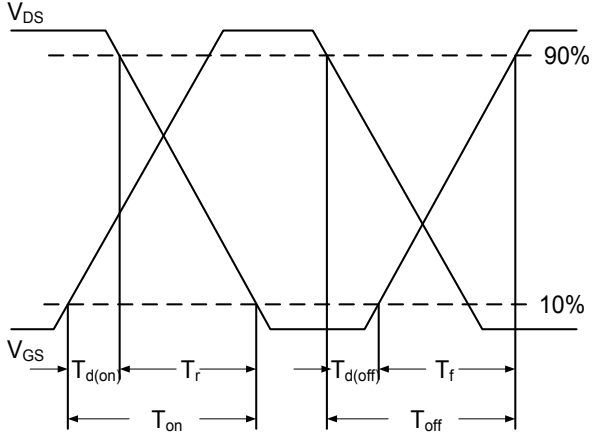


Fig.7 Switching Time Waveform

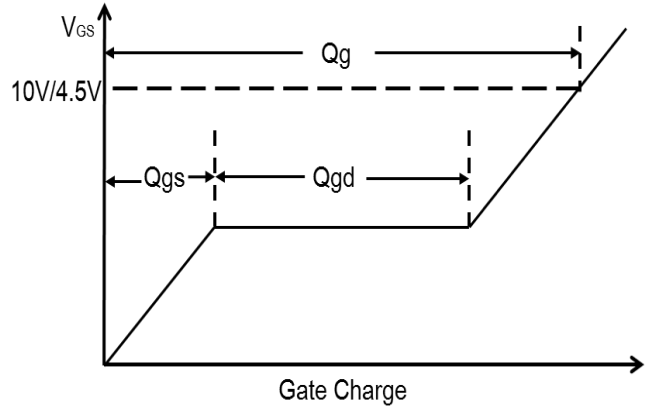
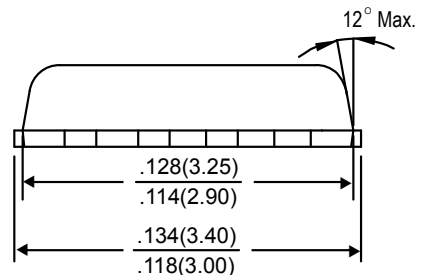
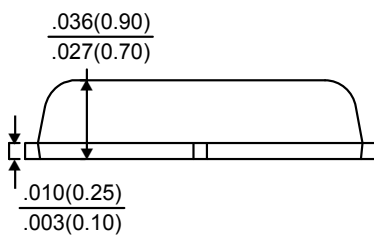
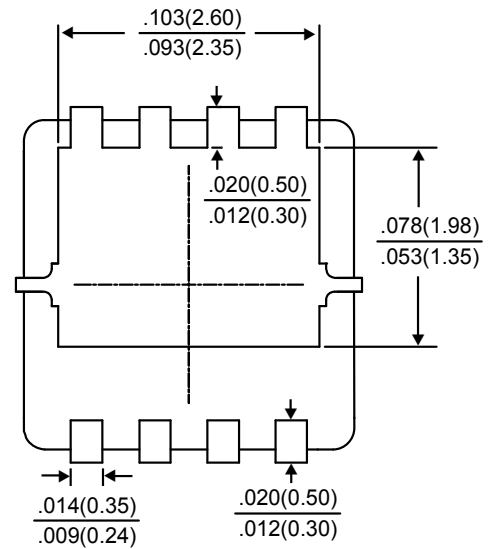
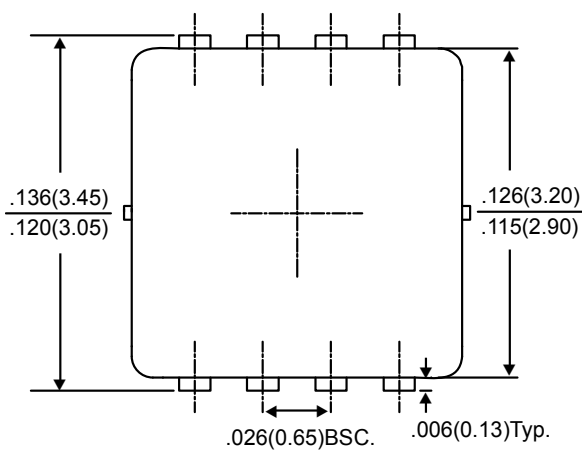


Fig.8 Gate Charge Waveform

Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)



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