P3MND6P5

40V N-Channel MOSFETs

General Description

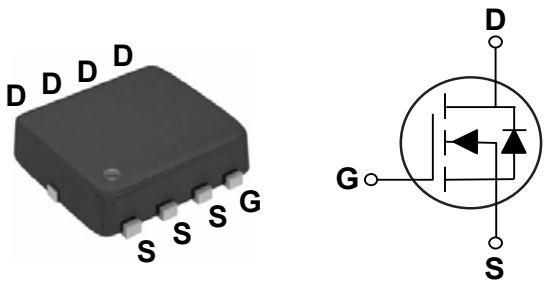
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	$R_{DS(ON)}$	I_D
40 V	6.5 mΩ	43 A

Features

- $R_{DS(ON)} \leqq 6.5m\Omega @ V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

PPAK3X3 Pin Configuration



Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current - Continuous (T_C=25°C)	43	A
	Drain Current - Continuous (T_C=100°C)	28	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	60	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	48	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	31	A
P_D	Power Dissipation (T_C=25°C)	27.8	W
T_J	Operating Junction Temperature Range	-55 to 150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
Marking Code		ND6P5 , DC4964	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4.5	°C/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	40	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=32V , V_{GS}=0V , T_J=25°C	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance (NOTE 3)	V_{GS}=10V , I_D=10A	---	5.5	6.5	mΩ
		V_{GS}=4.5V , I_D=5A	---	8.7	9.7	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D=250uA	1.2	1.6	2.5	V
gfs	Forward Transconductance	V_{DS}=10V , I_D=5A	---	8	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	V_{DS}=20V , V_{GS}=4.5V , I_D=12A (NOTE 3、4)	---	5.8	---	nC
Q_{gs}	Gate-Source Charge		---	3	---	
Q_{gd}	Gate-Drain Charge		---	1.2	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=15V , V_{GS}=10V , R_G=3.3Ω , I_D=1A (NOTE 3、4)	---	14.3	---	nS
T_r	Rise Time		---	5.6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	20	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , F=1MHz	---	690	---	pF
C_{oss}	Output Capacitance		---	193	---	
C_{rss}	Reverse Transfer Capacitance		---	38	---	
Rg	Gate Resistance	V_{GS}=0V , V_{DS}=0V , F=1MHz	---	1.7	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	20	A
V_{SD}	Diode Forward Voltage (NOTE 3)	V_{GS}=0V , I_S=1A , T_J=25°C	---	---	1	V

NOTES：

1. Repetitive Rating：Pulsed width limited by maximum junction temperature.
2. V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=31A.
3. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
4. Essentially independent of operating temperature.

Characteristics Curves

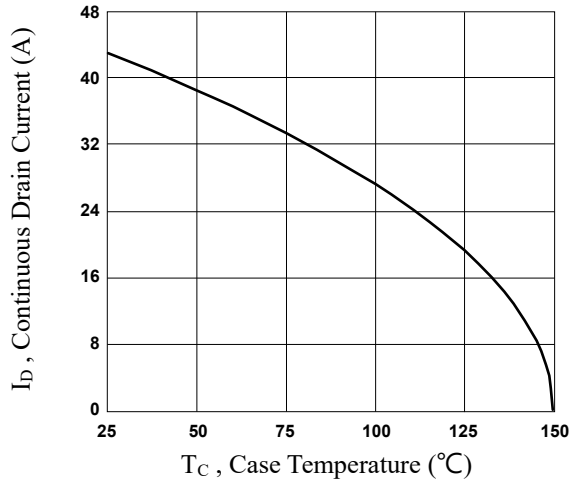


Fig.1 Continuous Drain Current vs. T_C

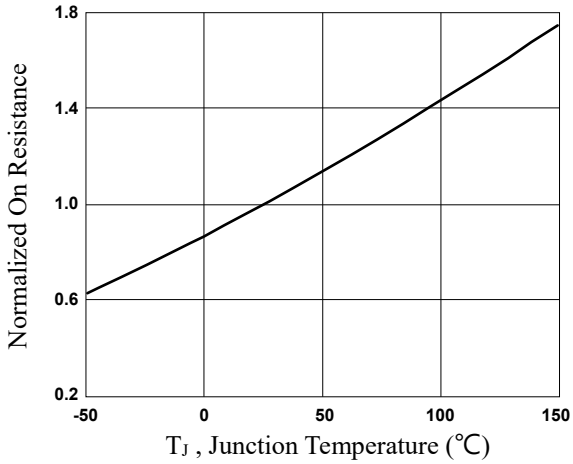


Fig.2 Normalized RDSON vs. T_J

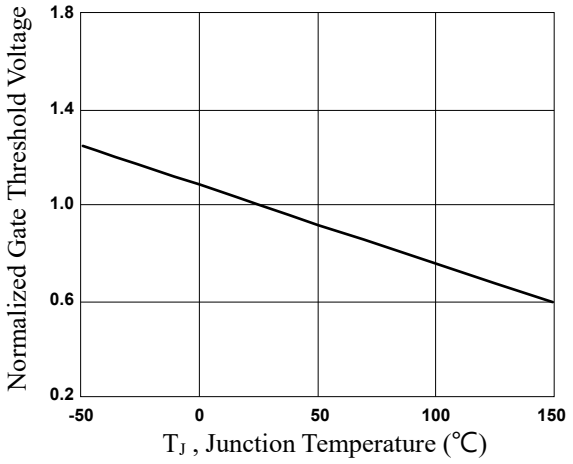


Fig.3 Normalized V_{th} vs. T_J

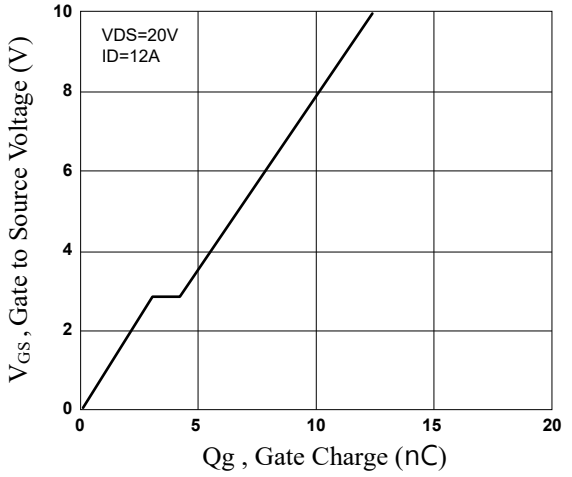


Fig.4 Gate Charge Waveform

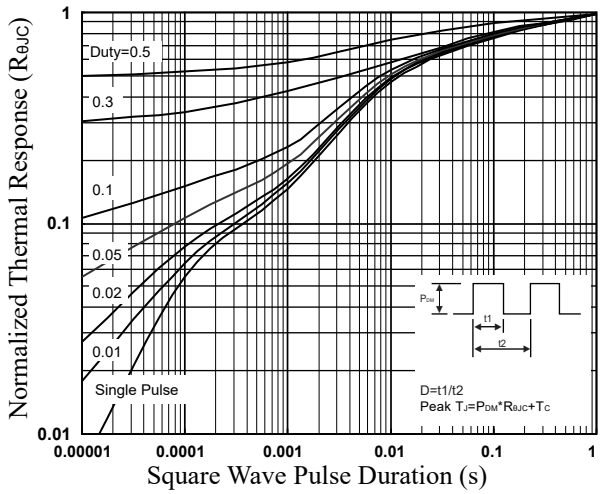


Fig.5 Normalized Transient Impedance

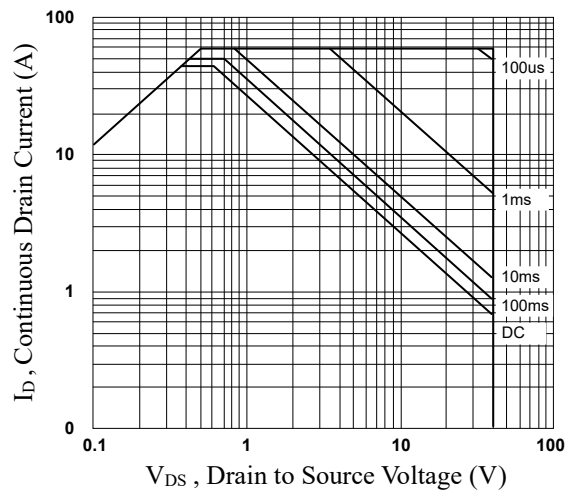


Fig.6 Maximum Safe Operation Area

Characteristics Curves

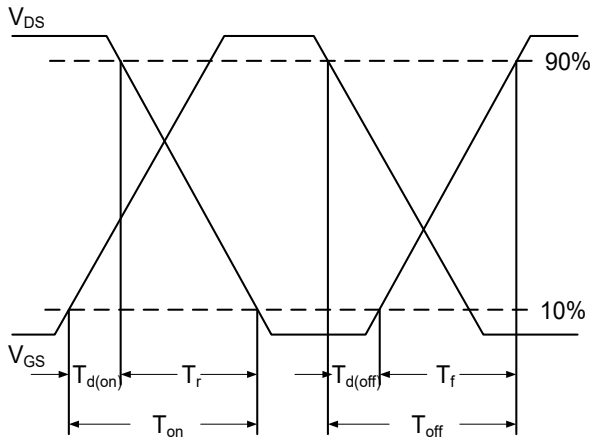
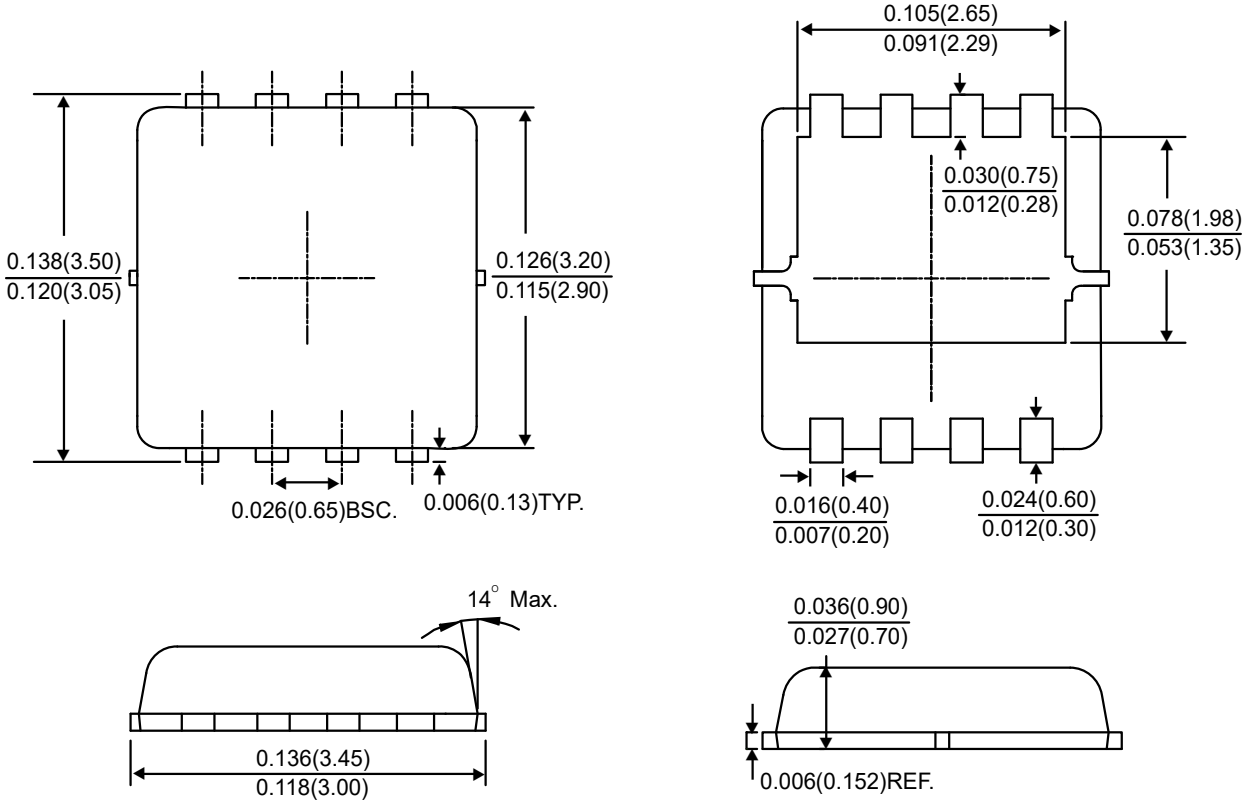


Fig.7 Switching Time Waveform

Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)

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