



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

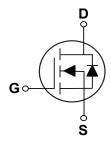
BV _{DSS}	R _{DS(ON)}	I _D
30 V	6 mΩ	60 A

Features

- $\bullet \; R_{DS(ON)} {\leq} 6m\Omega @V_{GS} {=} 10V$
- · Improved dv/dt Capability
- Fast Switching
- · Green Device Available

PPAK3X3 Pin Configuration





Applications

- · MB / VGA / Vcore
- POL Applications
- · SMPS 2nd SR

Absolute Maximum Ratings T _c =25°C unless otherwise noted							
Symbol	Parameter	Rating	Units				
V_{DS}	Drain-Source Voltage	30	V				
V_{GS}	Gate-Source Voltage	±20	V				
I _D	Drain Current - Continuous (T _C =25°C)	60	Α				
I _{DM}	Drain Current - Pulsed (NOTE 1)	240	Α				
E _{AS}	Single Pulse Avalanche Energy (NOTE 2)	45	mJ				
I _{AS}	Single Pulse Avalanche Current (NOTE 2)	30	Α				
P_{D}	Power Dissipation (T _C =25°C)	45	W				
T_J	Operating Junction Temperature Range	-55 to 150	°C				
T _{STG}	Storage Temperature Range	-55 to 150	°C				
Marking Code		NC6P0					

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		2.8	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =24V , V_{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =10A			6	mΩ
		V_{GS} =4.5V , I_D =8A			9	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2		2.5	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			23.3		
Q_{gs}	Gate-Source Charge	V_{DS} =15V , V_{GS} =10V , I_{D} =10A		3.11		nC
Q_{gd}	Gate-Drain Charge			6.26		
$T_{d(on)}$	Turn-On Delay Time			7.5		
T _r	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω ,		14.5		nS
$T_{d(off)}$	Turn-Off Delay Time	I _D =15A		35.2		113
T_f	Fall Time			9.6		
C_{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , F=1MHz		1120		
C _{oss}	Output Capacitance			237		pF
C_{rss}	Reverse Transfer Capacitance			217		
Rg	Gate Resistance	V _{GS} =0V , V _{DS} =0V , F=1MHz		0.95		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			60	Α
I _{SM}	Pulsed Source Current (NOTE 3)				240	Α
V_{SD}	Diode Forward Voltage (NOTE 3)	V_{GS} =0V , I_{S} =1A , T_{J} =25 $^{\circ}$ C			1	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. $V_{DD}\text{=}25V,\,V_{GS}\text{=}10V,\,L\text{=}0.1\text{mH},\,I_{AS}\text{=}30A,\,R_{G}\text{=}25\Omega,\,Starting}\,\,T_{J}\text{=}25^{\circ}C.$
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Characteristics Curves

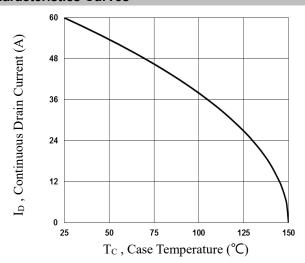


Fig.1 Continuous Drain Current vs. Tc

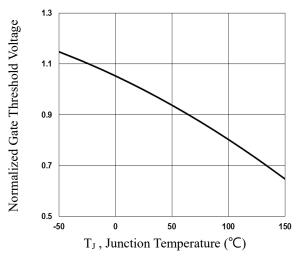


Fig.3 Normalized V_{th} vs. T_J

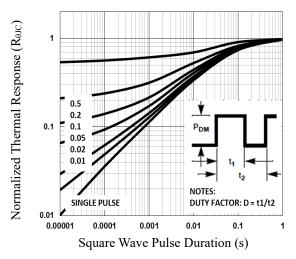


Fig.5 Normalized Transient Impedance

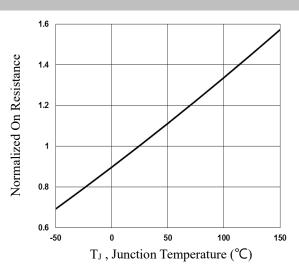


Fig.2 Normalized RDSON vs. T_J

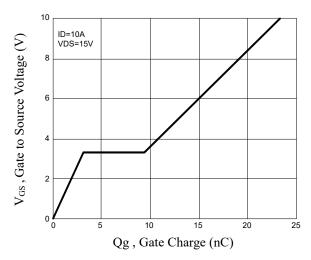


Fig.4 Gate Charge Waveform

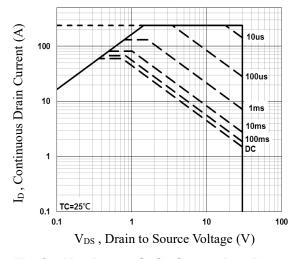


Fig.6 Maximum Safe Operation Area





Characteristics Curves

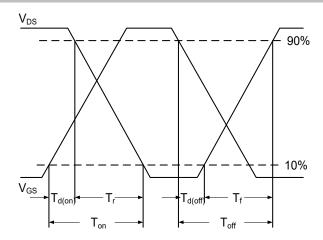
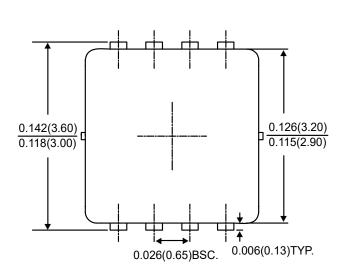
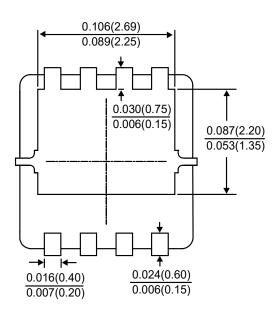
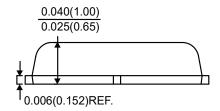


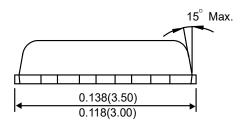
Fig.7 Switching Time Waveform

Package Outline Dimensions









PPAK3X3

Dimensions in inches and (millimeters)





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