



General Description

The P3MNC4P0 is the high cell density trenched N-ch MOSFETs, which provide excellent R_{DSON} and gate charge for most of the synchronous buck converter applications.

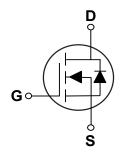
BV _{DSS}	R _{DS(ON)}	I _D
30 V	4 mΩ	80 A

Features

- $R_{DS(ON)} \le 4m\Omega@V_{GS} = 10V$
- · Super Low Gate Charge
- Excellent CdV/dt effect decline
- · Green Device Available

PPAK3X3 Pin Configuration





Applications

- · MB / VGA / Vcore
- POL Applications
- · SMPS 2nd SR

Absolute Maximum Ratings T _c =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V _{DS}	Drain-Source Voltage	30	V			
V_{GS}	Gate-Source Voltage	±20	V			
I_	Drain Current - Continuous (T _C =25°C)	80	Α			
I _D	Drain Current - Continuous (T _C =100°C)	50	Α			
I _{DM}	Pulsed Drain Current (NOTE 1)	160	Α			
EAS	Single Pulse Avalanche Energy (NOTE 2)	144.7	mJ			
IAS	Avalanche Current	53.8	Α			
P _D	Power Dissipation (NOTE 3)	43.4	W			
T_J	Operating Junction Temperature Range	-55 to 150	°C			
T _{STG}	Storage Temperature Range	-55 to 150	°C			
Marking Code		NC4P0, B3016				

Thermal Characteristics					
Symbol Parameter Typ. Max.		Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		75	°C/W	
$R_{ heta JC}$	R _{eJC} Thermal Resistance Junction to Case		2.88	°C/W	





Electrical Characteristics (T_{.1}=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
I _{DSS} Drain-S	Drain-Source Leakage Current	V_{DS} =24V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	- uA
	Diani-Source Leakage Current	V_{DS} =24V , V_{GS} =0V , T_{J} =55 $^{\circ}$ C			5	
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V_{GS} =10V , I_D =20A			4	mΩ
		V _{GS} =4.5V , I _D =15A			6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.0		2.5	V
gfs	Forward Transconductance	V_{DS} =5V , I_D =30A		26.5		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			70		
Q_gs	Gate-Source Charge	V_{DS} =15V , V_{GS} =4.5V , I_{D} =15A		12		nC
Q_{gd}	Gate-Drain Charge			17		
$T_{d(on)}$	Turn-On Delay Time			11		
T_r	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω ,		120		nS
$T_{d(off)}$	Turn-Off Delay Time	I _D =15A		25		113
T_f	Fall Time			60		
C _{iss}	Input Capacitance			3500		
C _{oss}	Output Capacitance	V_{DS} =15V , V_{GS} =0V , F=1MHz		386		pF
C_{rss}	Reverse Transfer Capacitance			358		
Rg	Gate resistance	V _{GS} =0V , V _{DS} =0V , F=1MHz		1.4		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current (NOTE 4)	−V _G =V _D =0V , Force Current			80	Α
I _{SM}	Pulsed Source Current (NOTE 1 \ 4)				160	Α
V_{SD}	Diode Forward Voltage (NOTE 1)	V_{GS} =0V , I_S =1A , T_J =25°C			1.2	V

NOTES:

- 1. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 2. The EAS data shows Max. rating. The test condition is V_{DD} =25V, V_{GS} =10V, I_{AS} =53.8A, L=0.1mH.
- 3. The power dissipation is limited by 150°C junction temperature.
- 4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.





Characteristics Curves

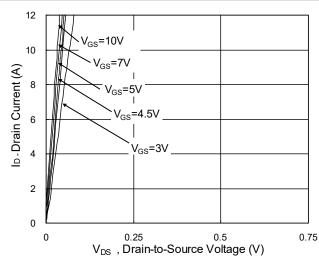


Fig.1 Typical Output Characteristics

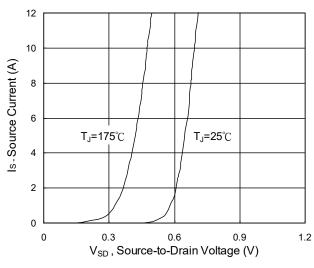


Fig.3 Forward Characteristics of Reverse

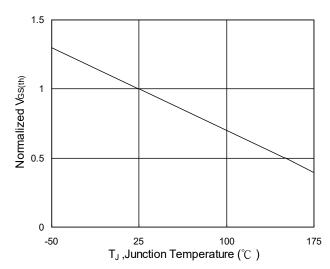


Fig.5 Normalized $V_{\text{GS(th)}}$ vs. T_{J}

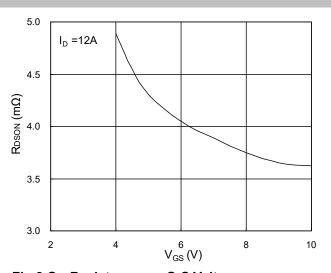


Fig.2 On-Resistance vs. G-S Voltage

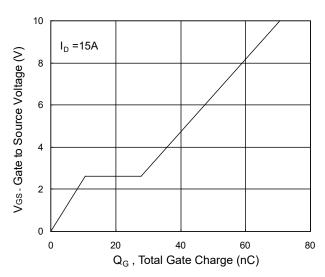


Fig.4 Gate-Charge Characteristics

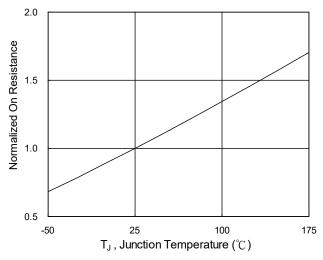


Fig.6 Normalized R_{DSON} vs. T_J





Characteristics Curves

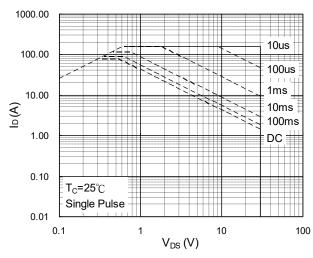


Fig.7 Safe Operating Area

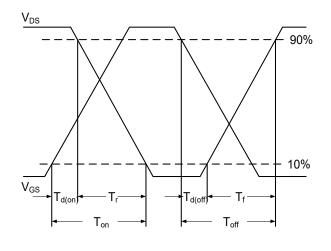
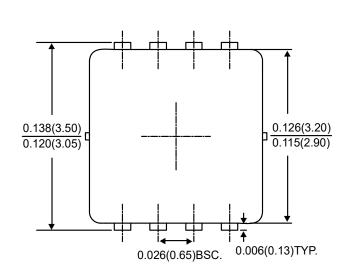
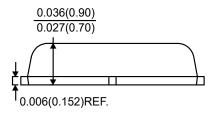
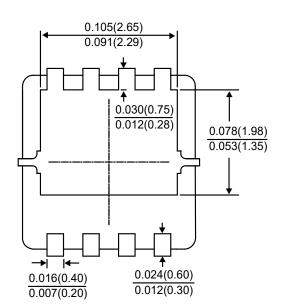


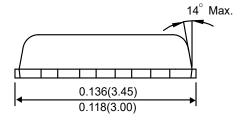
Fig.8 Switching Time Waveform

Package Outline Dimensions









PPAK3X3

Dimensions in inches and (millimeters)





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