



#### **General Description**

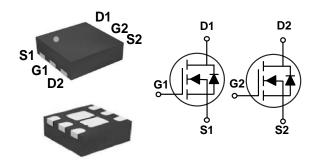
These dual N Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
30 V	30 mΩ	5 A

#### **Features**

- 30V, 5A,  $R_{DS(ON)}$ =30m  $\Omega$  @V<sub>GS</sub>=10V
- · Green Device Available
- Fast switching
- · Improved dv/dt capability

#### DFN2X2 Dual 2EP Pin Configuration



#### **Applications**

- · MB / VGA / Vcore
- POL Applications
- · SMPS 2<sup>nd</sup> SR

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	30	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
1	Drain Current - Continuous (T <sub>A</sub> =25°C)	5	Α	
I <sub>D</sub>	Drain Current - Continuous (T <sub>A</sub> =70°C)	4	Α	
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	20	Α	
$P_{D}$	Power Dissipation (T <sub>A</sub> =25°C)	1.25	W	
' D	Power Dissipation - Derate above 25°C	0.01	W/°(	
T <sub>J</sub> Operating Junction Temperature Range		-50 to 150	°C	
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C	

Thermal Characteristics						
Symbol Parameter		Тур.	Max	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		100	°C/W		





### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
ı	Drain-Source Leakage Current	$V_{DS}$ =30V , $V_{GS}$ =0V , $T_J$ =25°C			1	uA
IDSS	Dialii-Source Leakage Guireiit	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =3A		22	30	mΩ
NDS(ON)	(NOTE 3)	$V_{GS}$ =4.5V , $I_D$ =2A		35	46	11177
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2	1.5	2.5	V
gfs	Forward Transconductance	$V_{DS}$ =10V , $I_{D}$ =3A		4		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge (NOTE 2 · 3)			5.1	10	
$Q_gs$	Gate-Source Charge (NOTE 2 · 3)	$V_{DS}$ =24V , $V_{GS}$ =10V , $I_{D}$ =3A		0.4	1	nC
$Q_gd$	Gate-Drain Charge (NOTE 2 \cdot 3)			2.2	4.5	
$T_{d(on)}$	Turn-On Delay Time (NOTE 2 \ 3)			2.6	5	
$T_r$	Rise Time (NOTE 2 \cdot 3)	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		8.8	16	20
$T_{d(off)}$	Turn-Off Delay Time (NOTE 2 \ 3)	I <sub>D</sub> =1A		18.4	35	ns
$T_f$	Fall Time (NOTE 2 \ 3)			5.1	10	
$C_{iss}$	Input Capacitance			333	660	
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , F=1MHz		52	100	pF
$C_{rss}$	Reverse Transfer Capacitance			43	85	
Rg	Gate resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , F=1MHz		0.95	2	Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			5	Α
I <sub>SM</sub>	Pulsed Source Current (NOTE 3)	v <sub>G</sub> -v <sub>D</sub> -ov , Tolce Cullent			10	Α
$V_{SD}$	Diode Forward Voltage (NOTE 3)	$V_{GS}$ =0V , $I_{S}$ =1A , $T_{J}$ =25 $^{\circ}$ C			1	٧

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%.
- 3. Essentially independent of operating temperature.





#### **Characteristics Curves**

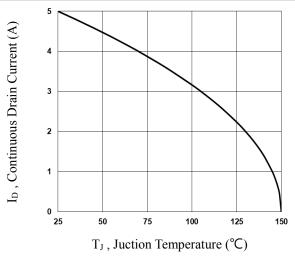


Fig.1 Continuous Drain Current vs. TJ

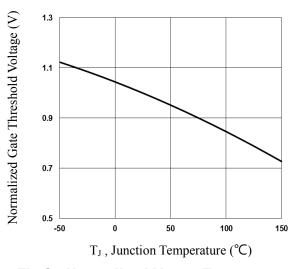


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

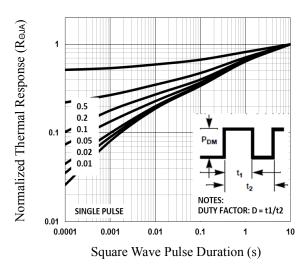


Fig.5 Normalized Transient Response

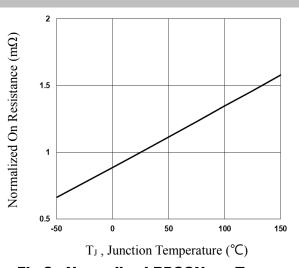


Fig.2 Normalized RDSON vs. T<sub>J</sub>

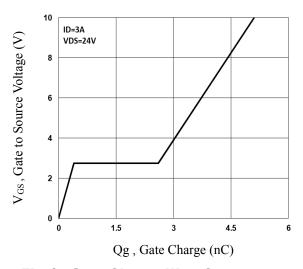


Fig.4 Gate Charge Waveform

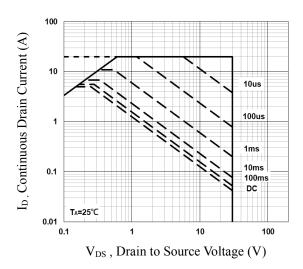
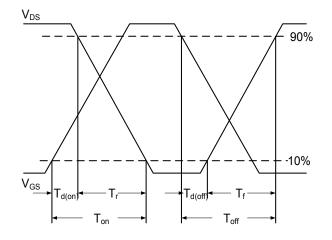


Fig.6 Maximum Safe Operation Area





#### **Characteristics Curves**



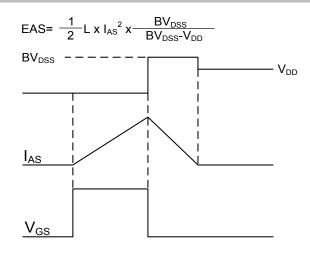
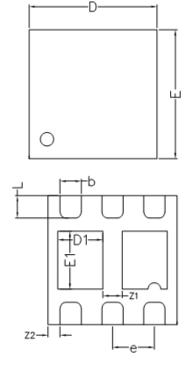


Fig.7 Switching Time Waveform

Fig.8 EAS Waveform

### **Package Outline Dimensions**



NOTE: ALL DIMENSIONS IN MM

MIN	NOM	MAX
1.95	2.00	2.05
1.95	2.00	2.05
0.65	0.70	0.75
0.85	0.90	0.95
0.30	0.35	0.40
0.28	0.33	0.38
	0.650BSC	
0.45	0.50	0.55
	0.15REF	
0.00	_	0.05
0.25	0.30	0.35
0.135	0.185	0.235
	1.95 1.95 0.65 0.85 0.30 0.28 0.45	1.95 2.00   1.95 2.00   0.65 0.70   0.85 0.90   0.30 0.35   0.28 0.33   0.650BSC   0.45 0.50   0.15REF   0.00 -   0.25 0.30

**DFN2X2 Dual 2EP** 





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