



#### **General Description**

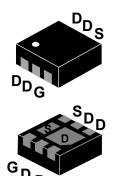
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

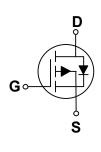
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
-20 V	17 mΩ	-11.5 A

### **Features**

- $R_{DS(ON)} \le 17 m\Omega@V_{GS} = -4.5V$
- · Fast Switching
- · Green Device Available
- · Improved dv/dt Capability

DFN2x2-6L Pin Configuration





### **Applications**

- · Battery Protection
- · Power Management
- · Load Switch

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	-20	V	
$V_{GS}$	Gate-Source Voltage	±12	V	
$I_D$	Drain Current - Continuous (T <sub>A</sub> =25°C)	-11.5	Α	
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	-46	Α	
EAS	Single Pulse Avalanche Energy (NOTE 2)	20	mJ	
$P_D$	Power Dissipation (T <sub>A</sub> =25°C)	3.1	W	
$T_J$	Operating Junction Temperature Range	-55 to 150	°C	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C	

Thermal Characteristics						
Symbol	Parameter	Rating	Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	40.3	°C/W			





### Electrical Characteristics (T<sub>.J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V , I <sub>D</sub> = -250uA	-20			V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ = -20V , $V_{GS}$ = 0V			-1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±12V , $V_{DS}$ = 0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
		V <sub>GS</sub> = -4.5V , I <sub>D</sub> = -8A			17	mΩ
K <sub>DS(ON)</sub>   Static Drain-Source On-Resistance	$V_{GS}$ = -2.5V , $I_{D}$ = -6A			22	11122	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-0.3		-1.0	V

#### **Dynamic and switching Characteristics (NOTE 4)**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V = 10V V = 4.5V		12		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> = -10V , V <sub>GS</sub> = -4.5V , I <sub>D</sub> = -8A	-	1.8		nC
$Q_{gd}$	Gate-Drain Charge	ID S/K	-	3.2		
$T_{d(on)}$	Turn-On Delay Time		-	17		
$T_r$	Rise Time	$V_{DD}$ = -10V , $V_{GS}$ = -4.5V ,		25.5		nS
$T_{d(off)}$	Turn-Off Delay Time	$R_G = 3\Omega$ , $I_D = -8A$		32		110
$T_f$	Fall Time	1		15		
$C_{iss}$	Input Capacitance			1555		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ = -10V , $V_{GS}$ = 0V , F= 1MHz		224		pF
$C_{rss}$	Reverse Transfer Capacitance			195		
$R_g$	Gate Resistance	$V_{DS}$ = -10V , $V_{GS}$ = 0V , F= 1MHz		10		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	$V_G = V_D = 0V$ , Force Current			-11.5	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ = 0V , $I_{S}$ = -8A			-1.2	V

### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The EAS data shows Max. rating. The test condition is  $V_{DD}$ = -20V,  $V_{GS}$ = -10V, L=0.1mH,  $I_{AS}$ = -20A.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. This value is guaranteed by design hence it is not included in the production test.





### **Characteristics Curves**

FIG. 1-Transfer Characteristics

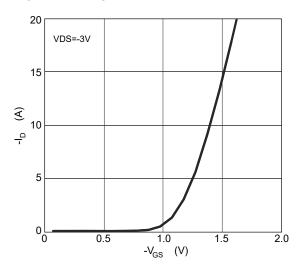


FIG. 2- $I_S$  vs  $V_{SD}$ 

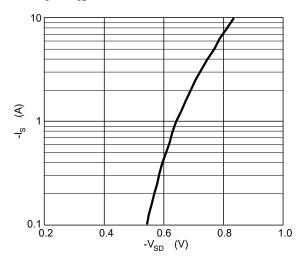


FIG. 3- $R_{DS(ON)}$  vs  $I_D$ 

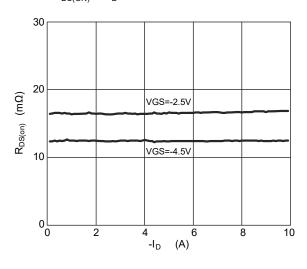


FIG. 4-Normalized  $R_{DS(ON)}$  vs  $T_J$ 

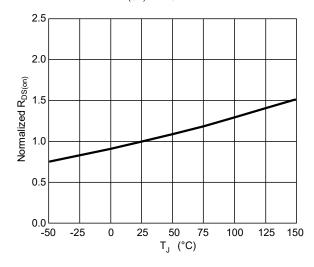


FIG. 5-Gate Charge Characteristics

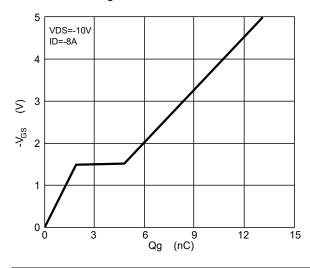
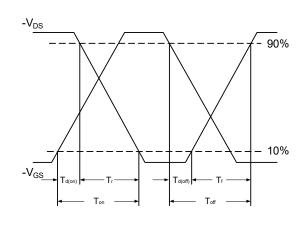


FIG. 6-Switching Time Waveform

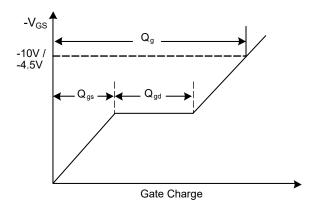




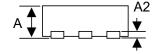


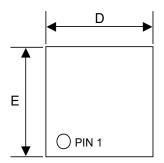
### **Characteristics Curves**

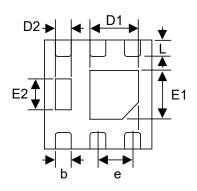
### FIG. 7-Gate Charge Waveform



## **Package Outline Dimensions**







	Dimens	sions in	Dimensions in		
Symbol	mm		inc	hes	
	Min.	Max.	Min.	Max.	
Α	0.50	0.80	0.019	0.032	
A2	0.203	REF	0.008	REF	
b	0.15	0.35	0.005	0.014	
D	1.90	2.10	0.074	0.083	
D1	0.80	1.20	0.031	0.048	
D2	0.15	0.40	0.005	0.016	
E	1.90	2.10	0.074	0.083	
E1	0.80	1.30	0.031	0.052	
E2	0.46	0.90	0.018	0.036	
е	0.65	0.65 BSC		BSC	
L	0.20	0.35	0.007	0.014	

### DFN2x2-6L





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