



#### Features

- High speed 10Mbit/s
- High isolation voltage between input and output (Viso=5000 Vrms )
- Patented coplanar structure DMC-Isolator®
- Operating temperature range -55°C to 110°C
- RoHS and REACH compliance
- Halogen Free compliance
- MSL class 1
- Regulatory Approvals
  - ✓ UL - UL1577 (E364000)
  - ✓ VDE - EN60747-5-5(VDE0884-5)
  - ✓ CQC – GB4943.1, GB8898(14001104779)
  - ✓ IEC62368 (FI/41119)

#### Description

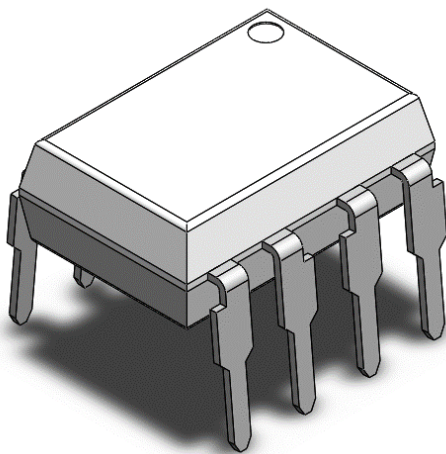
The CT2630, CT2631, devices each consist of an infrared emitting diode, optically coupled to a very high speed integrated photo-detector logic gate with a strobable output. This output features an open collector,there by permitting wired OR outputs.

The devices are packaged in an 8-pin DIP DMC-Isolator® package and also available in gullwing (400mil) and surface mount lead forming.

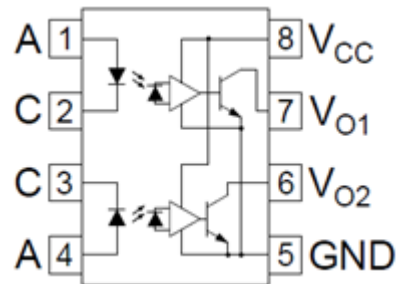
#### Applications

- Line receivers
- Telecommunication equipment
- High speed logic ground isolation
- Feedback loop in switch-mode power supplies
- Home appliances

#### Package Outline



#### Schematic



#### Truth Table

(POSITIVE LOGIC)	
Input	Output
L	H
H	L

Note: Different lead forming options available. See package dimension.



10Mbit/s High Speed Logic Gate Optocoupler

**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$ , unless otherwise specified

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Ratings	Units	Notes
V <sub>ISO</sub>	Isolation voltage (AC, 1 minute, 40 ~ 60% R.H.)	5000	V <sub>RMS</sub>	
T <sub>OPR</sub>	Operating temperature	-55 ~ +110	°C	
T <sub>STG</sub>	Storage temperature	-55 ~ +150	°C	
T <sub>SOL</sub>	Soldering temperature (For 10 seconds)	260	°C	
<b>Emitter</b>				
I <sub>F</sub>	Forward current	25	mA	
V <sub>R</sub>	Reverse voltage	6	V	
P <sub>D</sub>	Power dissipation	40	mW	
<b>Detector</b>				
P <sub>D</sub>	Power dissipation	85	mW	
I <sub>O</sub>	Average Output current	50	mA	
V <sub>CC</sub>	Supply voltage	7	V	
V <sub>O</sub>	Output voltage	7	V	



10Mbit/s High Speed Logic Gate Optocoupler

**Electrical Characteristics** *TA = -40 - 85°C (unless otherwise specified). Typical values are measured at TA = 250C and VCC=5V*

**Emitter Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
V <sub>F</sub>	Forward voltage	I <sub>F</sub> = 10mA	-	1.6	1.8	V	
V <sub>R</sub>	Reverse Voltage	I <sub>R</sub> = 5μA	5.0	-	-	V	
ΔV <sub>F</sub> /ΔT <sub>A</sub>	Temperature coefficient of forward voltage	I <sub>F</sub> =10mA	-	-1.6	-	mV/°C	

**Detector Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
I <sub>CCL</sub>	Logic Low Supply Current	I <sub>F</sub> =10mA, V <sub>O</sub> =Open, V <sub>CC</sub> =5.5V	-	15	20	mA	1
		I <sub>F1</sub> =I <sub>F2</sub> =10mA, V <sub>O</sub> =Open, V <sub>CC</sub> =5.5V			25		2
I <sub>CCH</sub>	Logic High Supply Current	I <sub>F</sub> =0mA, V <sub>O</sub> =Open, V <sub>CC</sub> =5.5V	-	13	15	mA	

**Transfer Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
I <sub>OH</sub>	Logic High Output Current	I <sub>F</sub> =250uA, V <sub>O</sub> = 5.5V,		2	100	uA	
I <sub>FT</sub>	Input Threshold Current	V <sub>CC</sub> =5.5V, V <sub>O</sub> =0.6V, I <sub>O</sub> =13mA	-	3.3	5	mA	
V <sub>OL</sub>	Logic Low Output Voltage	I <sub>F</sub> =5mA, I <sub>O</sub> =13mA, V <sub>CC</sub> =5.5V,	-	0.35	0.6	V	

Notes

- 1. Single Channel
- 2. Dual Channel



**10Mbit/s High Speed Logic Gate Optocoupler**

**Electrical Characteristics** *TA = -40 - 85°C (unless otherwise specified). Typical values are measured at TA = 25°C and VCC=5V*

**Switching Characteristics**

Symbol	Parameters		Test Conditions	Min	Typ	Max	Units	Notes
T <sub>PHL</sub>	Propagation Delay Time Logic High to Logic Low		C <sub>L</sub> =15pF, R <sub>L</sub> =350Ω	-	42	75	ns	
T <sub>PLH</sub>	Propagation Delay Time Logic Low to Logic High			-	38	75	ns	
P <sub>WD</sub>	Pulse Width Distortion			-	5	34	ns	
Tr	Output Rise Time			-	40	-	ns	
Tf	Output Fall Time			-	10	-	ns	
CM <sub>H</sub>	Common Mode Transient Immunity at Logic Low	CT2630	I <sub>F</sub> = 0mA , V <sub>OH</sub> =2.0V, R <sub>L</sub> =350Ω, TA=25°C, V <sub>CM</sub> =10Vp-p	-	-	-	V/μs	
		CT2631	I <sub>F</sub> = 0mA , V <sub>OH</sub> =2.0V, R <sub>L</sub> =350Ω, TA=25°C, V <sub>CM</sub> =50Vp-p	5000	-	-		
CM <sub>L</sub>	Common Mode Transient Immunity at Logic High	CT2630	I <sub>F</sub> = 7.5mA , V <sub>OL</sub> =0.8V, R <sub>L</sub> =350Ω, TA=25°C, V <sub>CM</sub> =10Vp-p	-	-	-	V/μs	
		CT2631	I <sub>F</sub> = 7.5mA , V <sub>OL</sub> =0.8V, R <sub>L</sub> =350Ω, TA=25°C, V <sub>CM</sub> =50Vp-p	5000	-	-		



Typical Characteristic Curves  $T_A = 25^\circ\text{C}$ , unless otherwise specified

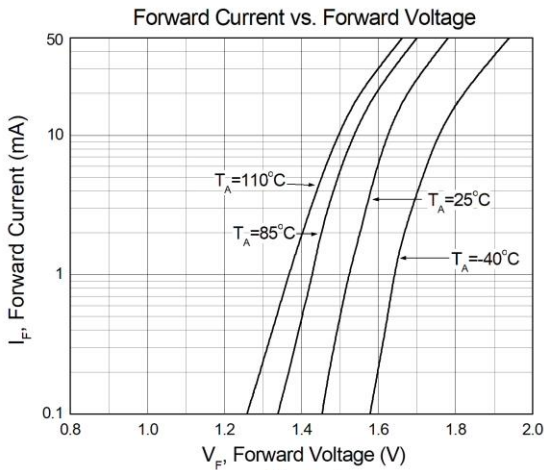


Figure 1

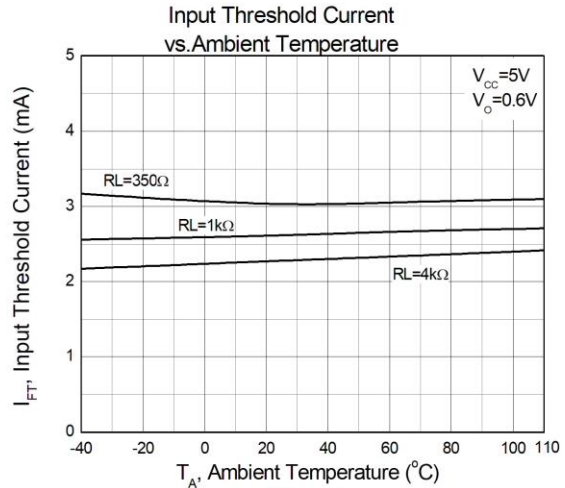


Figure 2

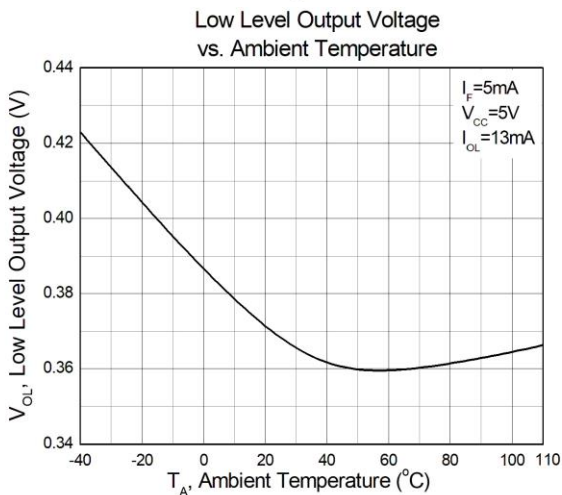


Figure 3

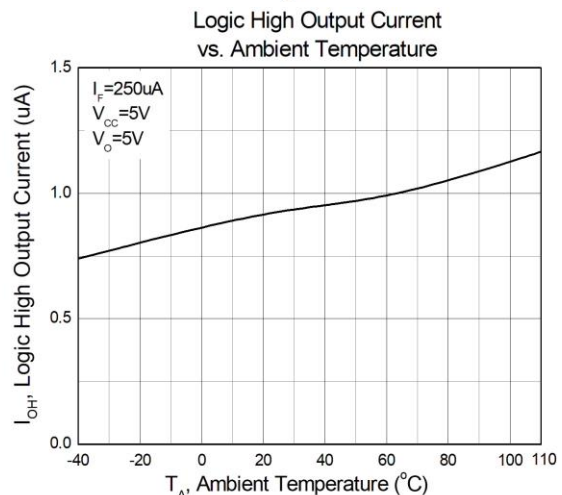


Figure 4

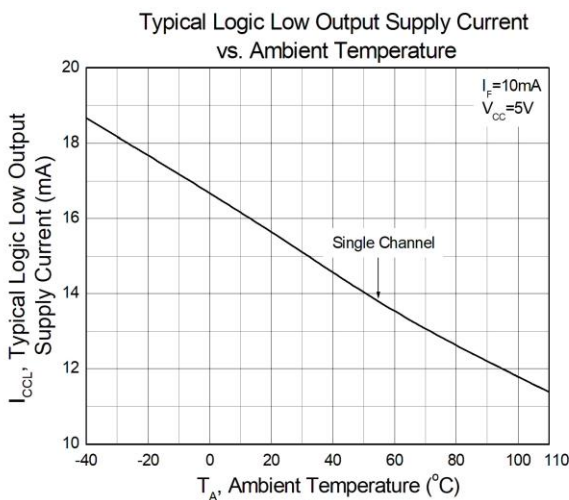


Figure 5

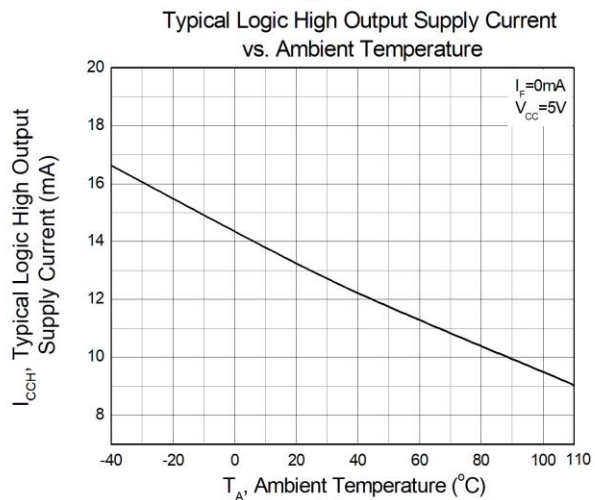


Figure 6



10Mbit/s High Speed Logic Gate Optocoupler

Typical Characteristic Curves  $T_A = 25^\circ\text{C}$ , unless otherwise specified

Typical Logic Output Supply Current vs. Output Supply Voltage

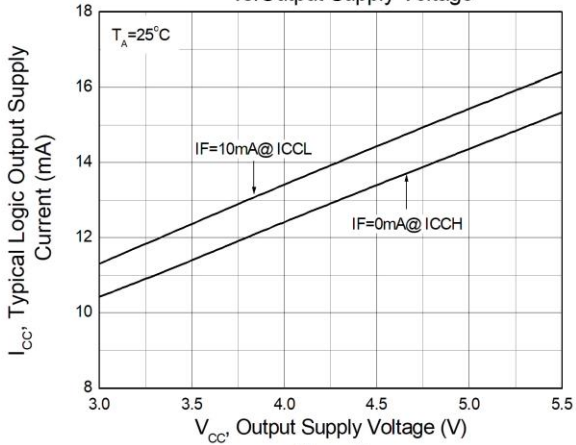


Figure 7

Propagation Delay vs. Ambient Temperature

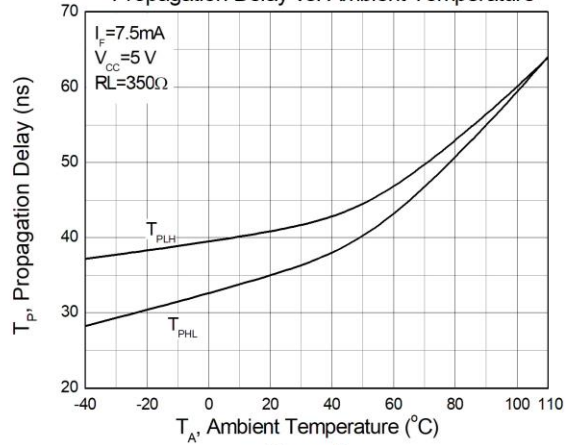


Figure 8

Pulse Width Distortion vs. Ambient Temperature

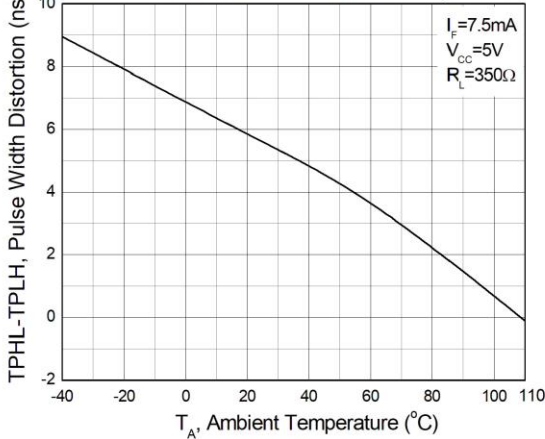


Figure 9

Rise And Fall Time vs. Ambient Temperature

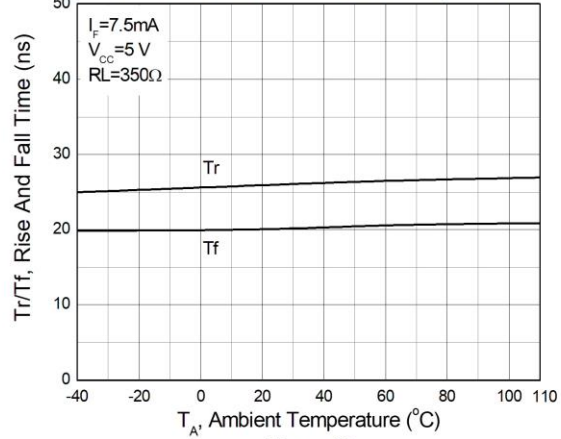


Figure 10

Pulse Width Distortion vs. Ambient Temperature

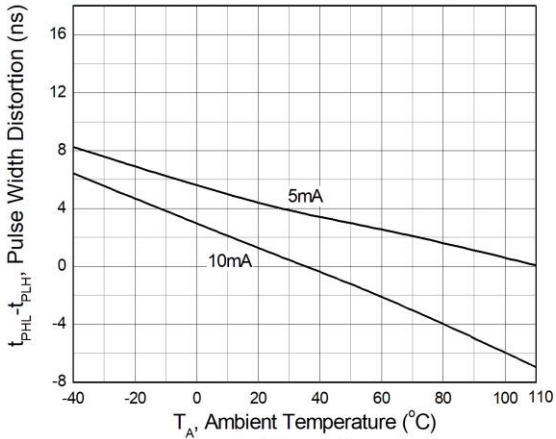


Figure 11



### Test Circuits

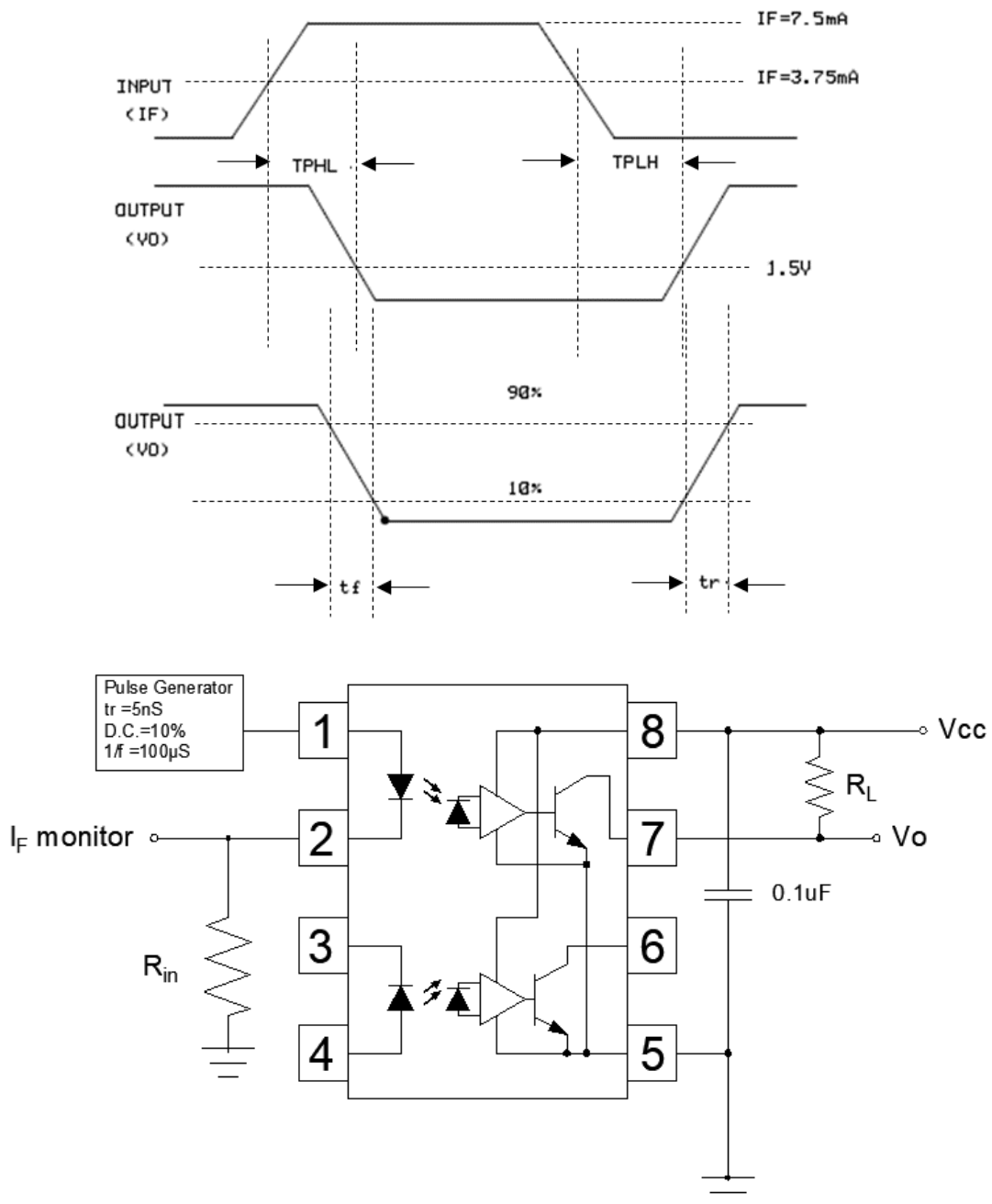


Figure 12: Switching Time Test Circuits



### Test Circuits

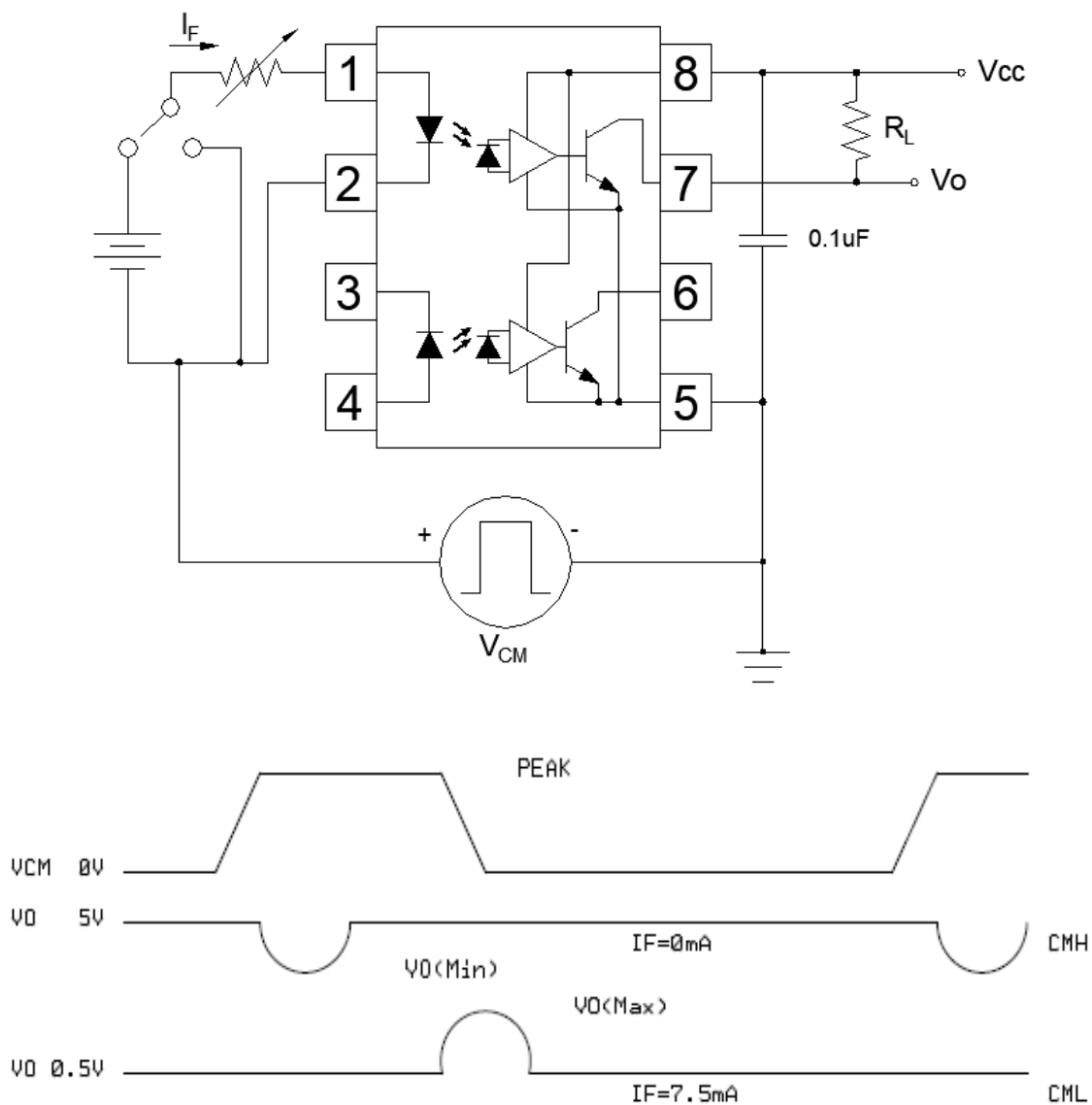


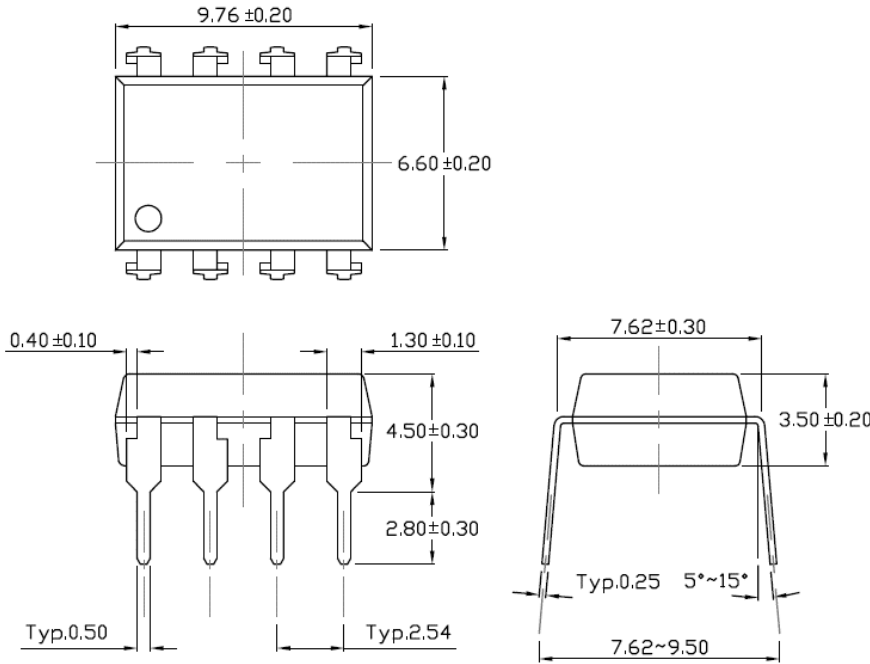
Figure 13: CMR Test Circuits





Package Dimension *Dimensions in mm unless otherwise stated*

Standard DIP – Through Hole

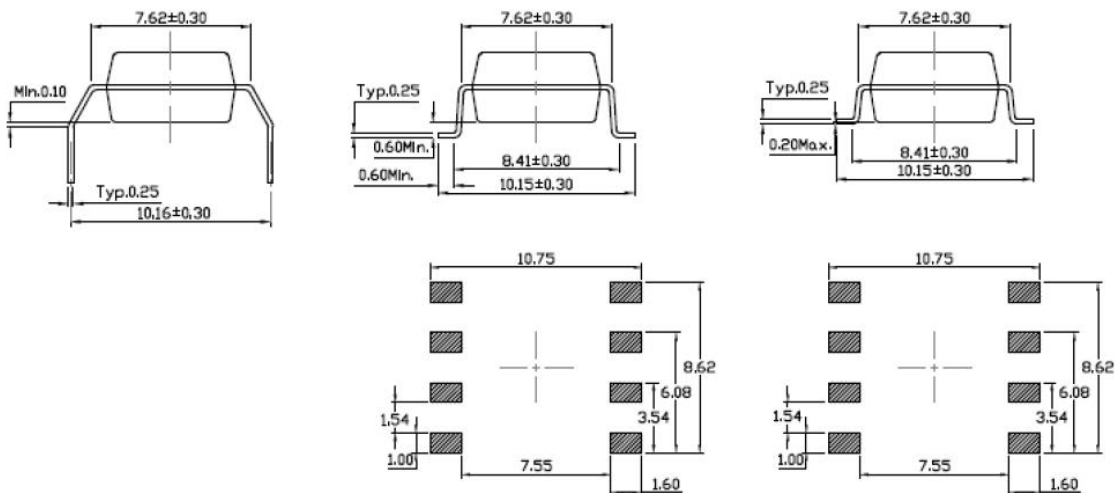


Forming Option

M Type

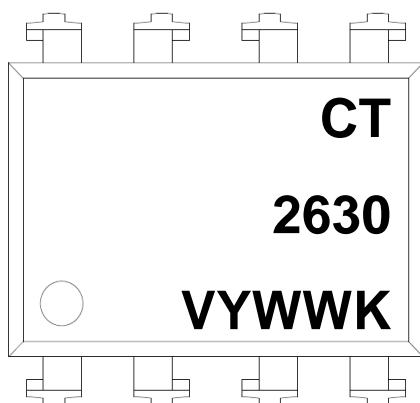
S Type

SL Type





### Marking Information



**Note:**

- CT : Denotes “CT Micro”
- 2630 : Part Number
- V : VDE Safety Mark Option (Blank or V)
- Y : One Digit Year Code
- WW : Two Digit Work Week
- K : Manufacturing Code

### Ordering Information

#### CT263X (V)(Y)(Z)

- CT = Denotes “CT Micro”
- 263X = Part Number
- X = Part No. (0 or 1)
- V = VDE Safety Mark Option (Blank or V)
- Y = Lead Form Option (Blank, S, SL or M)
- Z = Tape and Reel Option (Blank, T1 or T2)

<i>Option</i>	<i>Description</i>	<i>Quantity</i>
None	Standard 8 Pin Dip	40 Units/Tube
M	Gullwing (400mil) Lead Forming	40 Units/Tube
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1000 Units/Reel
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1000 Units/Reel
SL(T1)	Surface Mount (Low Profile) Lead Forming– With Option 1 Taping	1000 Units/Reel
SL(T2)	Surface Mount (Low Profile) Lead Forming– With Option 2 Taping	1000 Units/Reel



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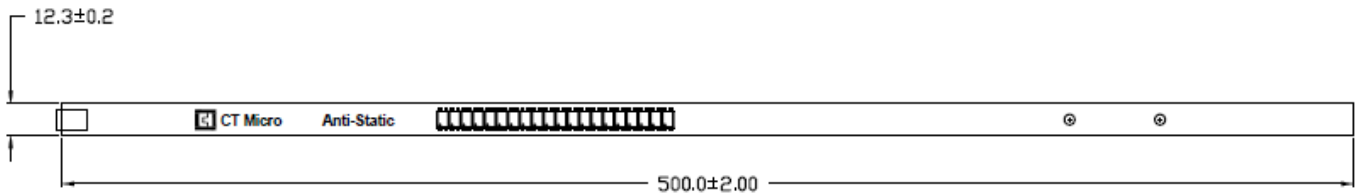
CT2630, CT2631

8-Pin DMC-Isolator®

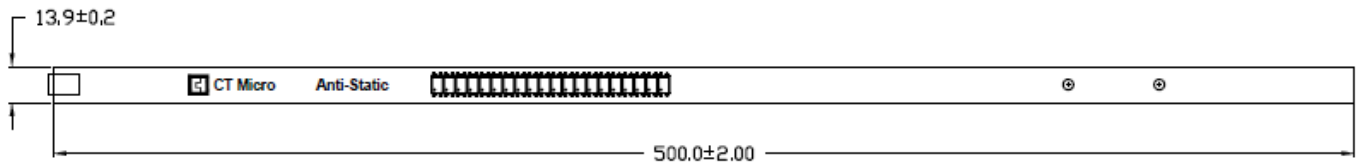
10Mbit/s High Speed Logic Gate Optocoupler

**Carrier Specifications** *Dimensions in mm unless otherwise stated*

**Tube Option Standard DIP**

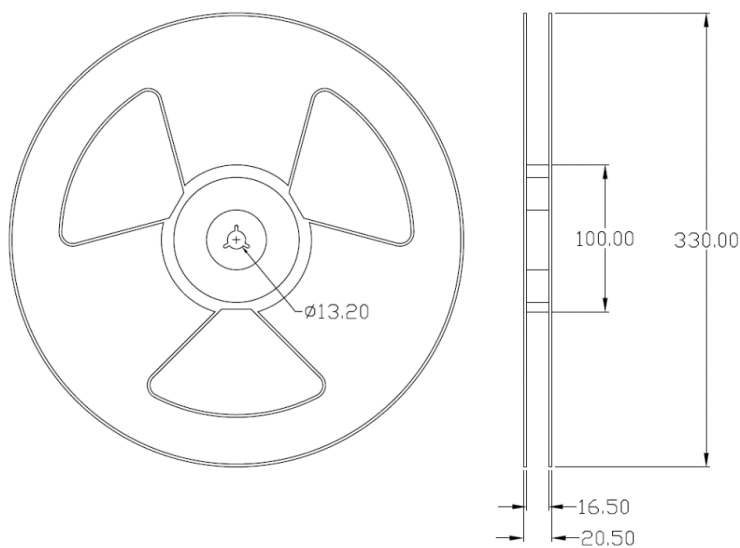


**Tube Option M Type**



**Reel Dimension** *All dimensions are in mm, unless otherwise stated*

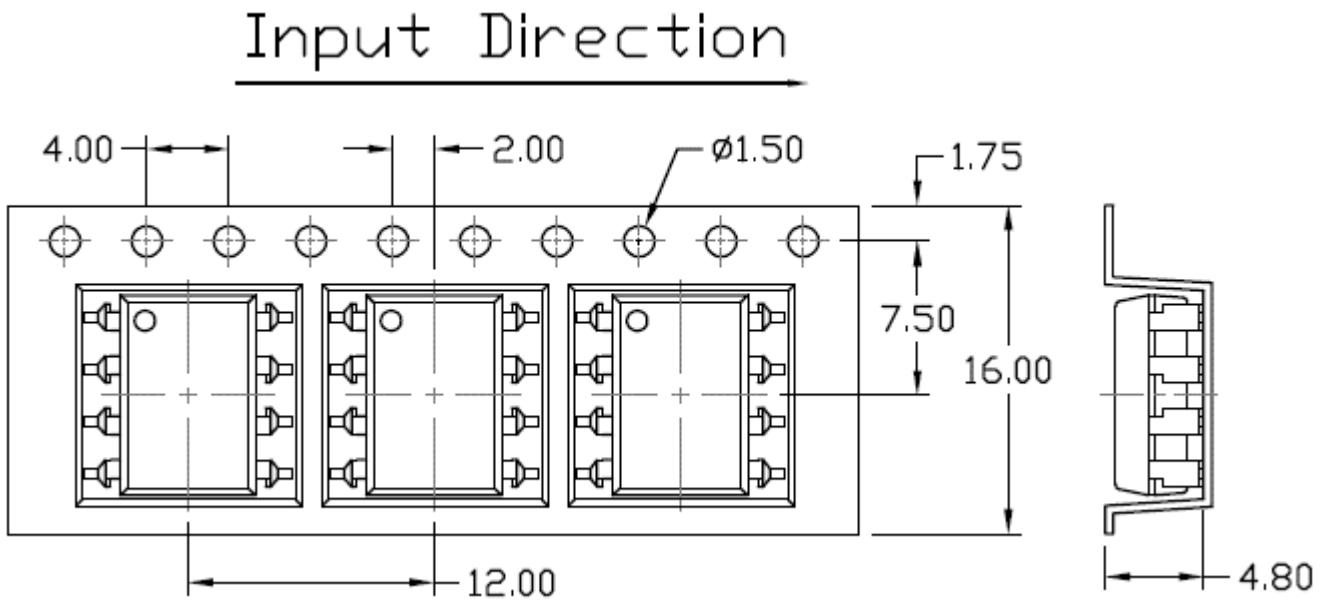
**Option S(T1/T2) & SL(T1/T2)**



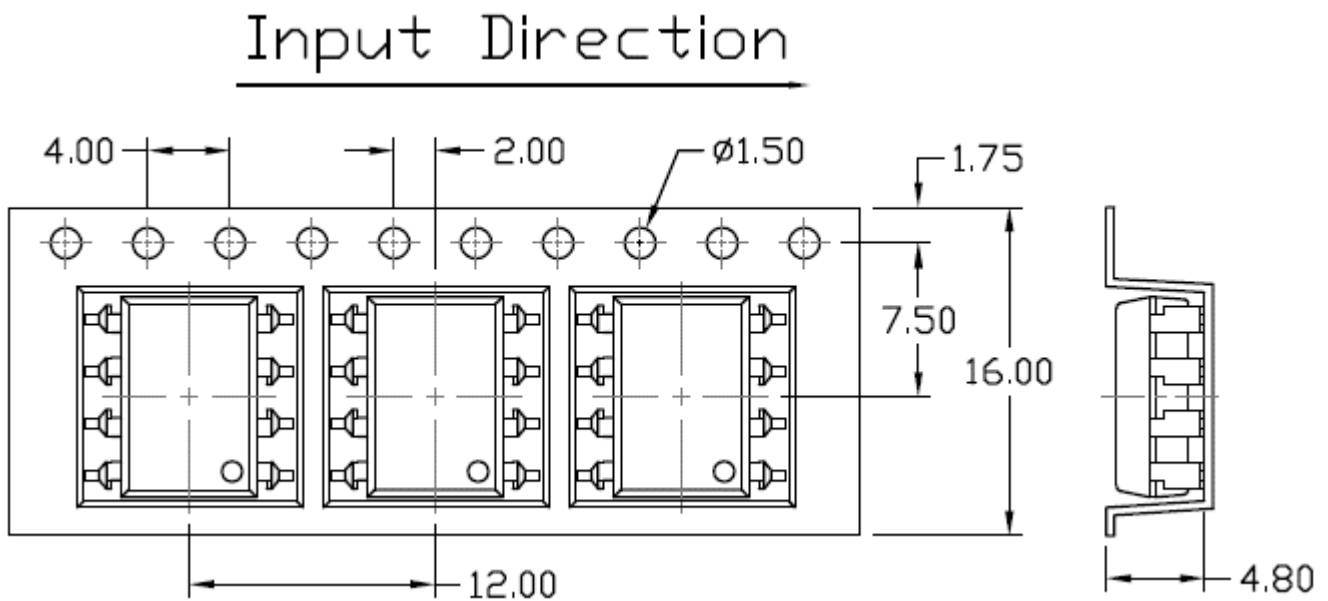


**Carrier Tape Specifications** *Dimensions in mm unless otherwise stated*

**Option S(T1) & SL(T1)**



**Option S(T2) & SL(T2)**





### Solderability spec (Follow the JEDEC standard JESD22-B102)

Reflow Soldering: Immersed surface, other than the end of pin as cut-surface, must be covered by solder.

Solder-Bath: More than 95% of the electrode must be covered with solder.

### Wave soldering (Follow the JEDEC standard JESD22-A111)

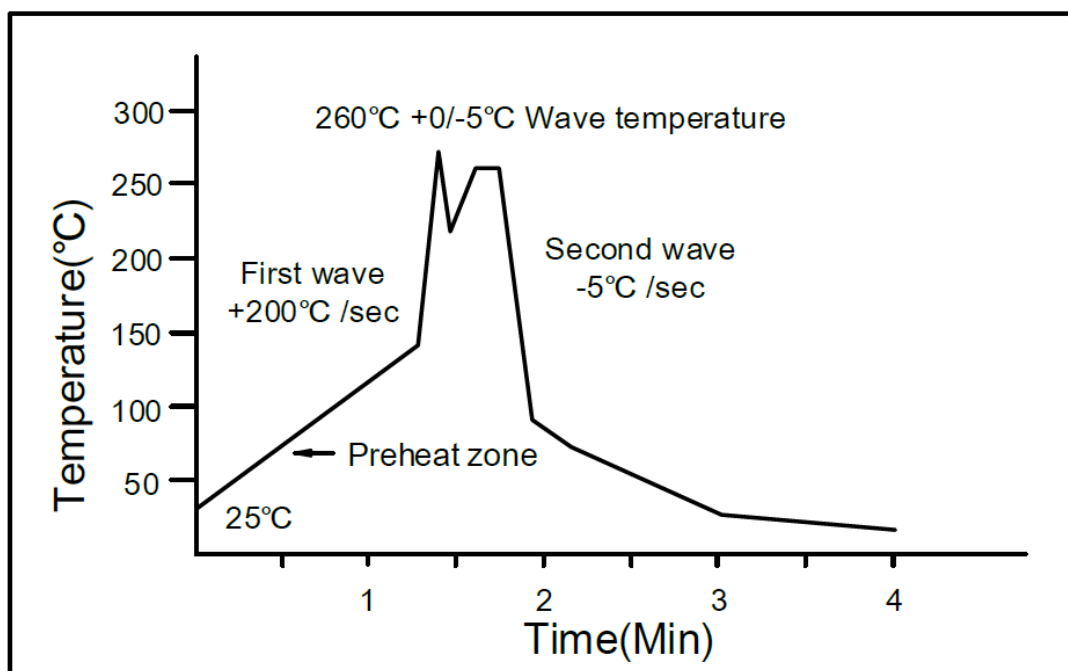
One time soldering is recommended within the condition of temperature.

Temperature:  $260 \pm 0/-5^\circ\text{C}$ .

Time: 10 sec.

Preheat temperature: 25 to  $140^\circ\text{C}$ .

Preheat time: 30 to 80 sec.



### Iron soldering (Follow the standard MIL-STD 202G, Method 210F)

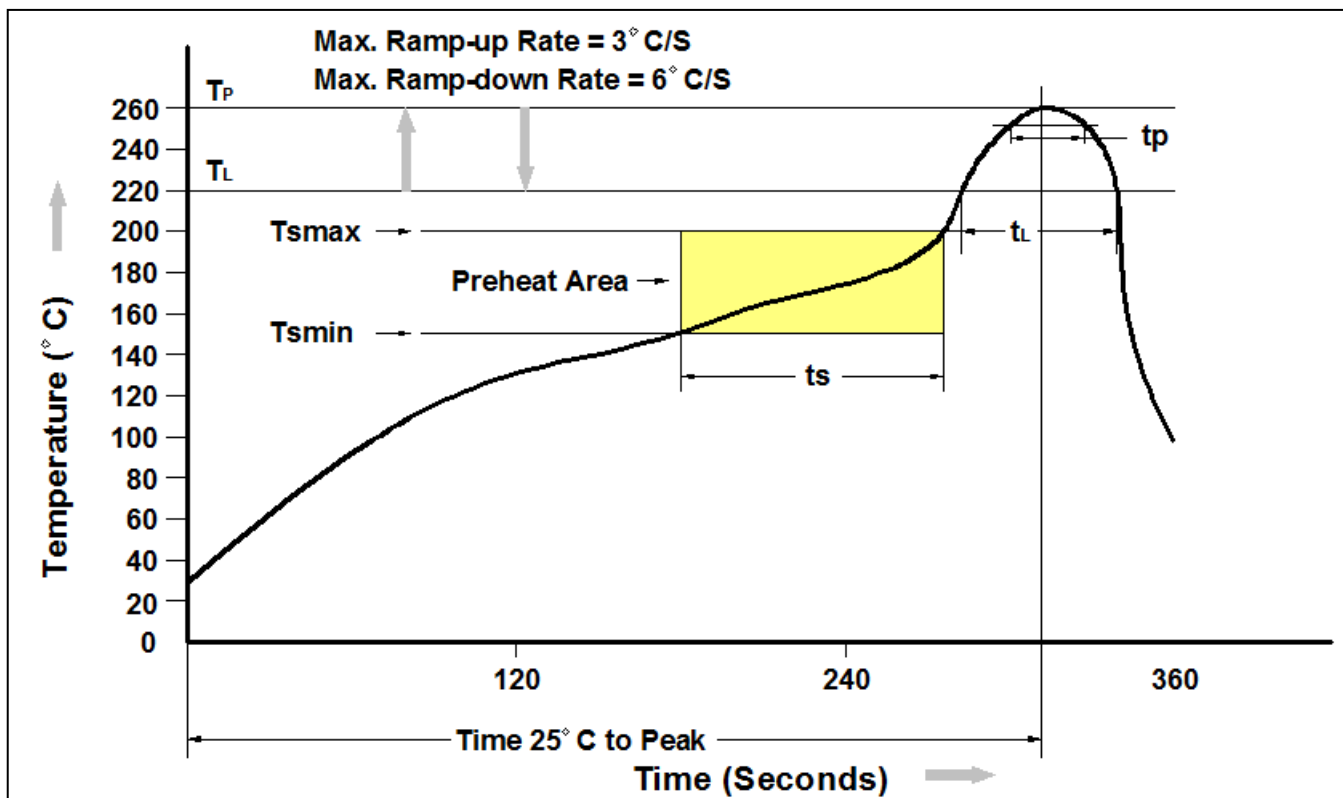
Allow single lead soldering in every single process.

One time soldering is recommended. Temperature:  $350 \pm 10^\circ\text{C}$

Time: 5 sec max.



**Reflow Profile (Follow the JEDEC standard J-STD-020)**



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T <sub>smin</sub> )	150°C
Temperature Max. (T <sub>smax</sub> )	200°C
Time (t <sub>s</sub> ) from (T <sub>smin</sub> to T <sub>smax</sub> )	60-120 seconds
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



## 10Mbit/s High Speed Logic Gate Optocoupler

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- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.*